

#### **Features**

- Low power consumption
- General purpose leads
- Bulk, Available on tape and reel
- Fast response time
- High photo sensitivity
- Small junction capacitance
- Compliance with EU REACH
- The product itself remain within RoHS compliant version

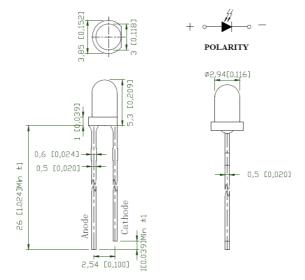
### **Applications**

- High speed photo detector
- Automatic door sensor
- Security system
- Industrial equipment
- Infrared application system

### **Description**

- The INL-3ANPD80 is a high speed and high sensitive silicon PIN photodiode in a standard 3mm epoxy package.
- Due to its black epoxy, the device is sensitive to near and infrared radiation.

## Package Dimensions in mm



#### Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is  $\pm$  0.25 mm (.010  $^{\prime\prime}$  ) unless otherwise noted.

Figure 1. INL-3ANPD80 Package Dimensions



Absolute Maximum Rating at 25°C

Symbol	Parameters	Ratings	Units	Notes
VR	Reverse Voltage	32	V	1
Topr	Operating Temperature	-40~+80	$^{\circ}$	
Tstg	Storage Temperature	-40~+85	$^{\circ}$	
Tsol	Soldering Temperature	260	$^{\circ}$	2
PD	Total Power Dissipation	150	mW	

#### **Notes**

1. Test conditions: IR=100µA, Ee=0mW/cm<sub>2</sub>.

2. Soldering time ≤ 5 seconds.

## **Electro-Optical Characteristics**

Symbol	Parameters	Test conditions	Min	Тур	Max	Units
λD	Rang of Spectral Bandwidth		400	-	1100	nm
λP	Wavelength of Peak Sensitivity		-	850		nm
VBR	Reverse Breakdown Voltage	E <sub>e</sub> =0mW/cm <sub>2</sub> IR=100uA	32	170	-	V
Voc	Open-Circuit Voltage	Ee=1mW/cm <sup>2</sup> λ <sub>P</sub> =850nm	-	0.4	-	V
Isc	Short-Circuit Current	Ee=1mW/cm² λ <sub>P</sub> =850nm	-	35	-	uA
lо	Dark Current	Ee=0mW/cm <sup>2</sup> VR=10V	-	5	30	nA
lι	Reverse Light Current	Ee=1mW/cm <sup>2</sup> λ <sub>P</sub> =850nm, VR=5V	20	35	-	uA
<b>t</b> r	Rise Time	V <sub>R</sub> =10V,	-	45	-	uS
tf	Fall Time	RL=100Ω	-	45	-	uS
Ст	Transition Capacitance	E <sub>e</sub> =0mW/cm <sup>2</sup> f=1MHz, VR=5V		18		pF
2θ1/2	Receiving Angle	IF=20mA		80		Deg.

#### **ESD Precaution**

ATTENTION: Electrostatic Discharge (ESD) protection

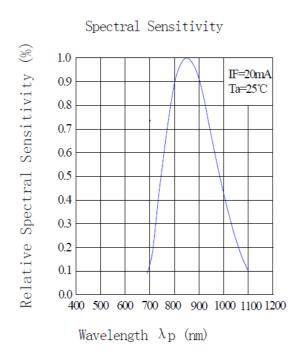


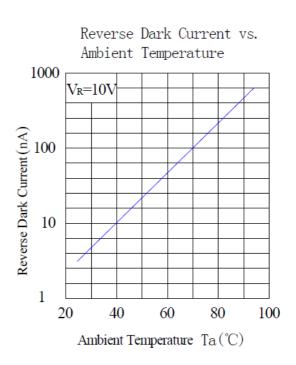
The symbol above denotes that ESD precaution is needed. ESD protection for GaP and AlGaAs based chips is necessary even though they are relatively safe in the presence of low static-electric discharge. Parts built with AllnGaP, GaN, or/and InGaN based chips are STATIC SENSITIVE devices. ESD precaution must be taken during design and assembly. If manual work or processing is needed, please ensure the device is adequately protected from ESD during the process.

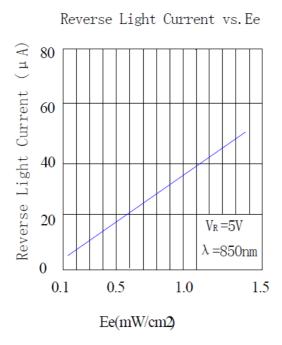
Please be advised that normal static precautions should be taken in the handling and assembly of this device to prevent damage or degradation which may be induced by electrostatic discharge (ESD).

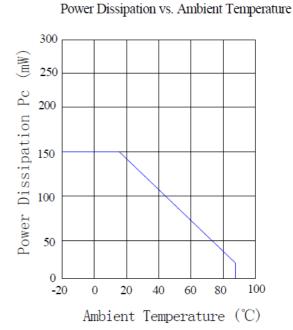


## **Typical Characteristic Curves**





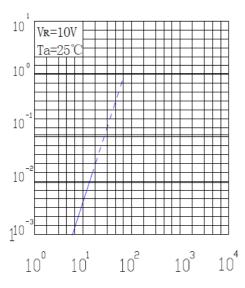






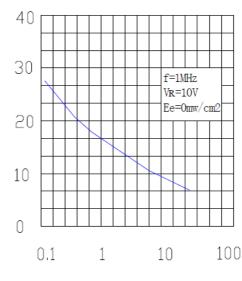
Rasponse Time vs. Losd Resistance

Rasponse Time tr, tf(us)



Terminal Capacitance vs. Reverse voltage

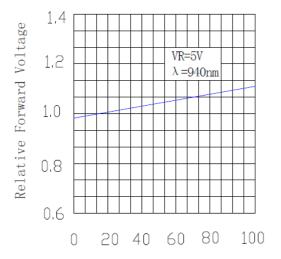
K



Losd Resistance  $RL(\Omega)$  Reverse voltage (V)

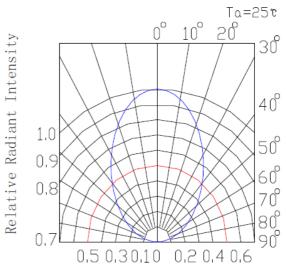
Terminal CapacitanceCt(pF)

Relative Reverse Light Current vs. Ambient Temperatyre( $^{\circ}$ C)



Tamb-Ambient Temperature (°C)

Relative Radiant Intensity vs. Angular Displacement

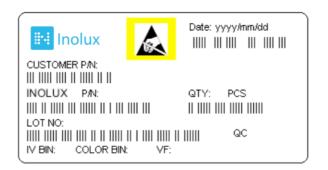




## **Ordering Information**

Product	Symbol	Parameters	Test conditions	Min	Тур	Max	Units	Orderable Part Number
INL-3ANPD80	ΙL	Reverse Light Current	Ee=1mW/cm <sup>2</sup> λ <sub>P</sub> =850nm, VR=5V	20	35	-	uA	INL-3ANPD80

## **Label Specifications**



### **Inolux P/N:**

I	N	L	-	3	Α	N	PD	8	0	Х	Χ	Χ	Х
				Pacl	kage	Lens	Color	View A	Angle			mized p-off	
	Inolux Lamp Typ	e		stan	\ = dard nm	N = Black Epoxy	PD = Photo Diode	80 = 80	) deg.				

### Lot No.:

Z	2	0	1	7	01	24	001
Internal		Voor (2017	2019 \	Month	Date	Carial	
Tracker		Year (2017, 2018,)				Date	Serial



# Reliability

Item	Frequency/ lots/ samples/ failures	Standards Reference	Conditions
	For all reliability	J-STD-020	1.) Baking at 85°C for 24hrs
Precondition	monitoring tests according		2.) Moisture storage at 85°C/ 60% R.H. for
recondition	to JEDEC Level 2		168hrs
	1Q/ 1/ 22/ 0	JESD22-B102-B	Accelerated aging 155°C/ 24hrs
Solderability	10/1/22/0	And CNS-5068	Tinning speed: 2.5+0.5cm/s
Coldciability		And ONO 3000	Tinning: A: 215°C/ 3+1s or B: 260°C/ 10+1s
		CNS-5067	Dipping soldering terminal only
Resistance to		0110 0001	Soldering bath temperature
soldering heat			A: 260+/-5°C; 10+/-1s
boldoning node			B: 350+/-10°C; 3+/-0.5s
	1Q/ 1/ 40/ 0	CNS-11829	1.) Precondition: 85°C baking for 24hrs
Operating life test	1 4, 1, 16, 6	0.10 1.020	85°C/ 60%R.H. for 168hrs
operating in a tool			2.) Tamb25°C; IF=20mA; duration 1000hrs
High humidity,	1Q/ 1/ 45/ 0	JESD-A101-B	Tamb: 85°C
high temperature	1 47 17 167 5	0205 / 1101 5	Humidity: 85% R.H., IF=5mA
bias			Duration: 1000hrs
	1Q/ 1/ 20	IN specs.	Tamb: 55°C
High temperature			IF=20mA
bias			Duration: 1000hrs
	1Q/ 1/ 40/ 0		Tamb25°C, If=20mA,, Ip=100mA, Duty
Pulse life test			cycle=0.125 (tp=125 $\mu$ s,T=1sec)
			Duration 500hrs)
	1Q/ 1/ 76/ 0	JESD-A104-A	A cycle: -40 degree C 15min; +85 degree C
		IEC 68-2-14, Nb	15min
Temperature		, .	Thermal steady within 5 min
cycle			300 cycles
			2 chamber/ Air-to-air type
High humidity	1Q/ 1/ 40/ 0	CNS-6117	60+3°C
storage test			90+5/-10% R.H. for 500hrs
High temperature	1Q/ 1/ 40/ 0	CNS-554	100+10°C for 500hrs
storage test			
Low temperature	1Q/ 1/ 40/ 0	CNS-6118	-40+5°C for 500hrs
storage test			



## **Revision History**

Changes since last revision	Page	Version No.	Revision Date
Initial Release		1.0	01-24-2019

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- 2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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VTP1220FBH VTP1232FH VTP4085H SFH 2400 OP913WSL OPF794 PD70-01C/TR7 LTR-536AD VTP8651H VTD206KH VTB1013H
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