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Intel® Xeon® Processor E5-2620 v2 (15M Cache, 2.10 GHz)

Specifications

PCI Express Revision

PCI Express Configurations #

Max # of PCI Express Lanes

Advanced Technologies

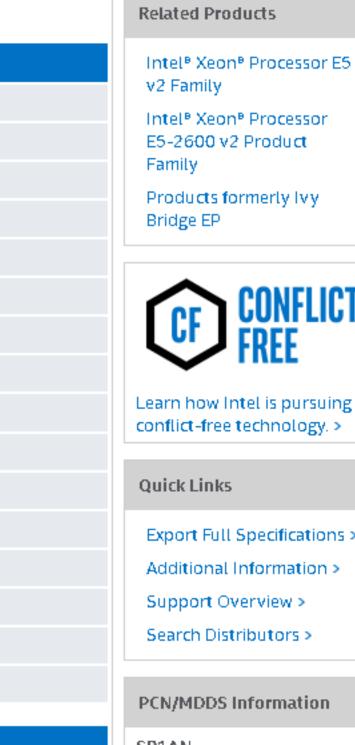
Specifications Essentials Performance Memory Specifications **Expansion Options** Package Specifications Advanced Technologies Intel® Data Protection Technology Intel® Platform Protection Technology

Compatible Products Ordering / sSpecs / Steppings

Download Drivers

 Essentials Launched Status Q3'13 Launch Date Processor Number E5-2620V2 15 MB SmartCache Cache Bus Speed 7.2 GT/s QPI 2 # of QPI Links 64-bit Instruction Set AVX. Instruction Set Extensions Q No Embedded Options Available Lithography 22 nm Scalability 2S Only VID Voltage Range 0.65-1.30V \$406.00 - \$410.00 Recommended Customer Price Conflict Free Yes Link Datasheet Additional Information URL Link

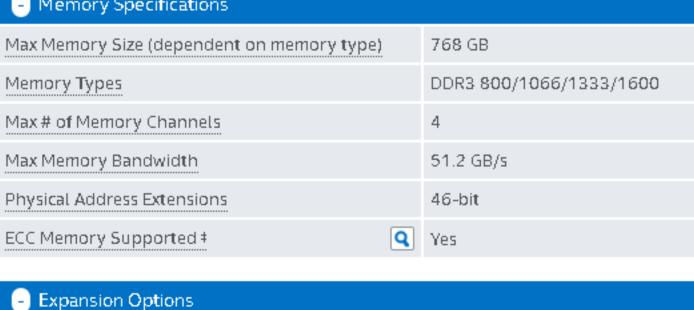




Export Full Specifications > Additional Information > Support Overview > Search Distributors > PCN/MDDS Information

SR1AN 930059: PCN | MDDS 931259: PCN | MDDS

| - Performance | |
|---|---------|
| # of Cores | б |
| # of Threads | 12 |
| Processor Base Frequency | 2.1 GHz |
| Max Turbo Frequency | 2.6 GHz |
| TDP | 80 W |
| | |
| Memory Specifications | |
| Many Many and City Adams and and an arrangement | 760 6D |



3.0

40

x4, x8, x16

| Package Specifications | |
|-------------------------------|---------------|
| Max CPU Configuration | 2 |
| TCASE | 71°C |
| Package Size | 52.5mm x 45mm |
| Sockets Supported | FCLGA2011 |
| Low Halogen Options Available | See MDDS |
| | |

| Travaricea recrinotogies | | |
|---|---|-----|
| Intel® Turbo Boost Technology ‡ | | 2.0 |
| Intel® vPro Technology ‡ | Q | Yes |
| Intel® Hyper-Threading Technology ‡ | Q | Yes |
| Intel® Virtualization Technology (VT-x)‡ | | Yes |
| Intel® Virtualization Technology for Directed I/O (VT-d) # | Q | Yes |
| Intel® VT-x with Extended Page Tables (EPT) ‡ | Q | Yes |
| Intel® TSX-NI | | No |
| Intel® 64 ‡ | Q | Yes |
| Idle States | | Yes |
| Enhanced Intel SpeedStep® Technology | Q | Yes |
| Intel® Demand Based Switching | Q | Yes |
| Thermal Monitoring Technologies | | Yes |
| Intel® Flex Memory Access | | No |
| Intel® Identity Protection Technology ‡ | | No |
| O lotal 8 Data Ducta ation Table and and | | |
| Intel® Data Protection Technology | | |
| Intel® AES New Instructions | Q | Yes |

| ntel® AES New Instructions | | Yes |
|--|-----|-----|
| Secure Key | | Yes |
| | | |
| Intel® Platform Protection Technol | ogy | |
| OS Guard | | Yes |
| rusted Execution Technology ‡ | Q | Yes |
| xecute Disable Bit ‡ | | Yes |
| | | |
| | | |

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Refer to Datasheet for formal definitions of product properties and features.

any item at the time of export.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability. Some products can support AES New Instructions with a Processor Configuration update, in particular, i7-2630QM/i7-2635QM, i7-2670QM/i7-2675QM, i5-2430M/i5-2435M, i5-2410M/i5-2415M. Please

contact OEM for the BIOS that includes the latest Processor configuration update. ‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications [motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIO5, drivers, virtual machine monitor-VMM, platform software, and for operating system) for feature compatibility. Functionality,

performance, and other benefits of this feature may vary depending on system configuration. *Conflict free" and *conflict-free" means *DRC conflict free", which is defined by the U.5. Securities and Exchange Commission rules to mean products that do not contain conflict minerals [tin, tantalum, tungsten]

and/or gold) that directly or indirectly finance or benefit armed groups in the Democratic Republic of the Congo (DRC) or adjoining countries. Intel also uses the term "conflict-free" in a broader sense to refer to suppliers, supply chains, smelters and refiners whose sources of conflict minerals do not finance conflict in the DRC or adjoining countries. Intel processors manufactured before January 1, 2013 are not confirmed conflict free. The conflict free designation refers only to product manufactured after that date. For Intel Boxed Processors, the conflict free designation refers to the processor only, not to any additional included accessories, such as heatsinks/coolers. See http://www.intel.com/content/www/us/en/architecture-and-technology/hyper-threading-threading-technology/hyper-threading-thread

processors support Intel® HT Technology. Max Turbo Frequency refers to the maximum single-core processor frequency that can be achieved with Intel® Turbo Boost Technology, See www.intel.com/technology/turboboost/ for more information.

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System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used. Low Halogen: Applies only to brominated and chlorinated flame retardants [BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet US-709

requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and for PVC may not be better for the environment. For benchmarking data see http://www.intel.com/performance.

Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families. See http://www.intel.com/content /www/us/en/processors/processor-numbers.html for details. Processors that support 64-bit computing on Intel® architecture require an Intel 64 architecture-enabled BIO5.

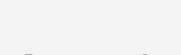
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