

Intel® Ethernet QSFP+ Optic

QSFP+ 40GBASE-SR4 and 40GBASE-LR4 Optics for Intel® Ethernet Network Adapters

Key Features

- Support for 40GBASE Ethernet
- Hot-swappable 40GbE I/O transceiver that plugs into a QSFP+ port
- Supports the 4x10 GbE mode to connect to four 10GBASE-SR or 10GBASE-LR optical interfaces
- Four channel, full duplex transceiver module
- Single MPO receptacle (SR)
- Single LC receptacle (LR)
- Maximum power dissipation
 1.5 W SR4;
 3.5 W LR4
- RoHS-6 compliant (lead-free)
- Commercial temperature range 0-70 $^{\circ}$ C
- Maximum link length 10 km on Single Mode Fiber (SMF)
- Maximum link length 100 m on Multimode Fiber (MMF)
- 1.06 Gb/s to 10.5 Gb/s per channel multi-rate capability
- Compatible with Intel® Ethernet Network Adapters

Overview

Intel® Ethernet QSFP+ Optics offer customers an efficient way to move to 40GbE for high bandwidth application requirements such as content distribution, high-end virtualization using multiple CPUs, network appliances, and Applications Delivery Controllers (ACD). When used with the Intel® Ethernet Network Adapter XL710, these optics support either 40GbE or 4x10GbE mode for four 10GBASE-SR or 10GBASE-LR optical interfaces.

To ensure maximum flexibility, Intel supports the ability to use either Intel® Ethernet QSFP+ Optics or direct attach twinaxial cables. This helps customers create the configuration that best meets the needs of their data center environment, while ensuring compatibility between adapter and accessories.

GENERAL SPECIFICATIONS	
Module Form Factor	QSFP+
Network Standards Physical Layer Interface	40GBASE-SR4 and 40GBASE-LR4 (4 x 10 GbE and 1 x 40 GbE)
QSFP+ Module Specifications	 INF-8438i Specification for QSFP (Quad Small Form factor Pluggable) Transceiver SFF-8436 – Specification for QSFP+ Copper and Optical Transceiver IEEE 802.3ba – PMD Type 40GBASE-SR4 or 40GBASE-LR4
Number of Lanes	4 Tx and 4 Rx
Airflow and Temperature Guidelines	Refer to adapter product brief for specific airflow and temperature requirements ¹

NOTE: When two Intel® Ethernet Network Adapter X520 and XL710 Series QSFP+ devices are connected back to back, they should be configured with the same Speed/Duplex setting. Results may vary if speed settings are mixed.

PRODUCT ORDER CODE

E40GQSFPSR E40GQSFPLR

SR4 OPTICAL CHARACTERISTICS

 $(T_{OP} = 0 \, ^{\circ}\text{C to } 70 \, ^{\circ}\text{C}, \, \text{VCC} = 3.15 \, \text{to } 3.45 \, \text{V})$

Parameter	Symbol	Min	Тур	Max	Unit	Note
Transmitter (per Lane)						
Signaling Speed per Lane			10.5		Gb/s	1
Center Wavelength		840		860	nm	
RMS Spectral Width	SW			0.65	nm	
Average Launch Power per Lane	TXP _x	-7.6		-1.0	dBm	
Transmit OMA per Lane	TxOMA	-5.6		3.0	dBm	2
Difference in Power between any two lanes (OMA)	DP _x			4.0	dB	
Peek Power per Lane	PP _x			4.0	dBm	
Launch Power (OMA) minus TDP per Lane	P-TDP	-6.5			dBm	
TDP per Lane	TDP			3.5	dBm	
Optical Extinction Ratio	ER	3.0			dB	
Optical Return Loss Tolerance	ORL			12	dB	
Encircled Flux	FLX		> 86% at 19 um < 30% at 4.5 um		dBm	
Average launch power of OFF transmitter per lane				-30	dBm	
Relative Intensity Noise	RIN			-128	dB/Hz	
Transmitter eye mask definition (X1, X2, X3) (Y1, Y2, Y3)			0.23, 0.34, 0.43 0.27, 0.35, 0.4			
N .						

- Transmitter consists or four lasers operating at a maximum rate of 10.5 Gb/s each.
 Even if TDP is < 0.9 dB, the OMA min must exceed this value.

SR4 OPTICAL CHARACTERISTICS (CONTINUED)

 $(T_{OP} = 0 \, ^{\circ}\text{C to } 70 \, ^{\circ}\text{C}, \text{VCC} = 3.15 \text{ to } 3.45 \, \text{V})$

Symbol	Min	Тур	Max	Unit	Note
		10.5		GBd	1
	840		860	nm	
DT	3.4			dBm	
RXP_x	-9.5		2.4	dBm	
RxOMA			3.0	dBm	
SRS			-5.4	dBm	
PP_x			4	dBm	
Rfl			-12	dB	
LOS _D			-12	dBm	
LOS _A	-30			dBm	
	0.5			dBm	
	DT RXP _x RxOMA SRS PP _x Rfl LOS _D	840 DT 3.4 RXP _x -9.5 RXOMA SRS PP _x Rfl LOS _b LOS _A -30	10.5 840 DT 3.4 RXP _x -9.5 RxOMA SRS PP _x Rfl LOS _D LOS _A -30	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	10.5 GBd 840 860 nm DT 3.4 dBm RXP _x -9.5 2.4 dBm RXOMA 3.0 dBm SRS -5.4 dBm PP _x 4 dBm Rfl -12 dB LOS _D -12 dBm LOS _A -30 dBm

^{1.} Receiver consists of four photodetectors operating at a maximum rate of 10.5 Gb/s each.

SR4 ELECTRICAL CHARACTERISTICS						
(T _{op} = 0 °C to 70 °C, VCC=3.15 to 3.45 V)						
Parameter	Symbol	Min	Тур	Max	Unit	Note
Supply Voltage	Vcc1 VccTx VccRx	3.15		3.45	V	
Supply Current	lcc			350	mA	
Link Turn-On Time						
Transmit turn-on time				2000	ms	1
Transmitter (per Lane)						
Single-ended input voltage tolerance	VinT	-0.3		4.0	V	
Differential data input swing	Vin,pp	180		1200	mVpp	2
Differential input threshold			50		mV	
AC common mode input voltage tolerance (RMS)		15			mV	
Differential imput return loss		Per IEEE P802.3ba, Section 86A.4.1.1		dB	3	
J2 Jitter Tolerance	Jt2	0.17			UI	
J9 Jitter Tolerance	Jt9	0.29			UI	
Data Dependent Pulse Width Shrinkage	DDPWS	0.07			UI	
Eye mask coordinates (X1, X2) (Y1, Y2)			0.11, 0.31 95, 350		UI mV	4

Notes:

- From power-on and end of any fault conditions.
 After internal AC coupling. Self-biasing 100 Ω differential input.
 10 MHz to 11.1 GHz range.
 Hit ratio = 5 x 10E-5. Valid for all settings in Figure 1.

SR4 ELECTRICAL CHARACTERISTICS (CONTINUED)

 $(T_{_{\mathrm{OP}}} = 0 \,^{\circ}\mathrm{C} \text{ to } 70 \,^{\circ}\mathrm{C}, \mathrm{VCC} = 3.15 \,^{\circ}\mathrm{to } 3.45 \,^{\vee}\mathrm{V})$

Paramet	er	Symbol	Min	Тур	Max	Unit	Note
Receiver (per Lane)							
Single-ended output voltage			-0.3		4.0	V	
Differential data output swing		Vout,pp	0		800	mVpp	1, 2
AC common mode output voltage				7.5	mV		
Termination mismatch at 1 MHz					5	%	
Differential output return loss		Per IEEE P802.3ba, Section 86A.4.2.1			dB	3	
Common mode output return los		Per IEEE P802.3ba, Section 86A.4.2.2			dB	3	
Output transition time, 20% to 80		28			ps		
J2 Jitter output Jo2					0.42	UI	
J9 Jitter output		Jo9			0.65	UI	
Eye mask coordinates #1	(X1, X2) (Y1, Y2)			0.29, 0.5 150, 425		UI mV	4
Eye mask coordinates #2	(X1, X2) (Y1, Y2)			0.29, 0.5 125, 500		UI mV	5
Power Supply Ripple Tolerance		PSR	50			mVpp	

Notes:

- 1. AC coupled with 100Ω differential output impedence. 2. Settable in four diecrete steps via the I^2C interface. See Figure 1 for Vout setting. 3. 10 MHz to 11.1 GHz range.
- 4. Hit ratio = 5 x 10E-5. Valid only for the shaded setting in Figure 1. 5. Hit ratio = 5 x 10E-5. Valid for all settings in Figure 1.

Power (mW)		Pre-Emphasis into 100 Ohms (mV)					
		0	125	175	325		
	0	599					
Volt (mV)	317	751	935	971	1075		
/olt	422	787	971	1007	1111		
	739	883	1055	1103	1190		

Figure 1 - Power Dissipation (mW, maximum) vs. Rx Output Conditions

LR4 OPTICAL CHARACTERISTICS $(T_{OR} = 0 \, ^{\circ}\text{C to } 70 \, ^{\circ}\text{C}, \text{VCC}=3.1 \text{ to } 3.47 \, \text{V})$ Symbol Min Typ Max Unit Note **Parameter** Transmitter (per Lane) Signaling Speed per Lane 10.3125 1 Gb/s 1264.5 - 1277.5 1284.5 - 1297.5 Lane Center Wavelengths (Range) nm 1304.5 - 1317.5 1324.5 - 1337.5 Total Average Launch Power P_{OUT} 8.3 dBm Transmit OMA per Lane **TxOMA** -4.0 3.5 dBm TPX, -7.0 2.3 2 Average Launch Power per Lane dBm Optical Extinction Ratio ER 3.5 dB Sidemode Suppression Ratio 30 dB $\mathsf{SSRP}_{\mathsf{MIN}}$ Average Launch Power of OFF Transmitter per Lane -30 dBm Relative Intensity Noise RIN -128 dB/Hz 3 Optical Return Loss Tolerance 20 dΒ Transmitter Reflectance -12 dB (X1, X2, X3)0.25, 0.4, 0.45 Transmitter Eye Mask Definition (Y1, Y2, Y3) 0.25, 0.28, 0.4 Receiver (per Lane) Signaling Speed per Lane 10.3125 GBd 1264.5 - 1277.5 1284.5 - 1297.5 Lane Center Wavelengths (Range) nm 1304.5 - 1317.5 1324.5 - 1337.5 Receive Power (OMA) per Lane **RxOMA** 3.5 dBm Average Receive Power per Lane RXP, -13.7 2.3 dBm 5 Receive Sensitivity (OMA) per Lane **Rxsens** -11.5 dBm Stressed Receiver Sensitivity (OMA) per Lane SRS -9.6 dBm Damage Threshold per Lane P_{MAX} 3.4 dBm RL -26 dΒ Return Loss Vertical Eye Closure Penalty per Lane 1.9 dB Receive Electrical 3 dB Upper Cutoff Frequency per Lane 12.3 GHz Loss of Optic Signal (LOS) De-Assert LOS -12 dBm

- 1. Transmitter consists or four lasers operating at 10.3 Gb/s each.
- 2. Minimum value is informative.

Loss of Optic Signal (LOS) Assert

Loss of Optic Signal (LOS) Hysteresis

- 3. RIN is scaled by 10*log(10/4) to maintain SNR outside of transmitter.
- 4. Receiver consists of four photodetectors operating at 10.3 Gb/s each.
 5. Minimum value is informative, equals min TxOMA with infinite ER and maximum channel insertion loss.

LOS

-280

1

dBm

dΒ

$(T_{OR} = 0 \, ^{\circ}\text{C to } 70 \, ^{\circ}\text{C}, \text{VCC}=3.1 \text{ to } 3.47 \, \text{V})$ Symbol Min Max Unit Note Typ **Parameter** Supply Voltage ٧ Vcc1, 3.1 3.47 VccTx, VccRx **Supply Current** lcc 1.13 Α Link Turn-on Time Transmit turn-on time 2000 1 ms Transmitter (per Lane) Single Ended Input Voltage Tolerance VinT ٧ -0.3 4.0 Differential Data Input Swing Vin,pp 120 1200 mVpp 2 Differential Input Threshold RIN 50 mV AC Common Mode Input Voltage Tolerance (RMS) 15 mV Differential Input Return Loss Per IEEE P802.3ba, Section 86A.4.1.1 dB 3 UI J2 Jitter Tolerance Jt2 0.17 J9 Jitter Tolerance 0.29 UI Tj9 Data Dependent Pulse Width Shrinkage **DDPWS** 0.07 UI 0.11, 0.31 UI (X1, X2)Eye Mask Coordinates 4 (Y1, Y2)95, 350 mV Receiver (per Lane) Single Ended Output Voltage -0.3 4 V 200 400 300 600 Differential Data Output Swing 550 5,6 Vout,pp mVpp 400 800 1200 600 AC Common Mode Output Voltage (RMS) 7.5 m۷ Termination Mismatch at 1 MHx % Differential Output Return Loss Per IEEE P802.3ba, Section 86A.4.2.1 dΒ Common Mode Output Return Loss Per IEEE P802.3ba, Section 86A.4.2.2 dB Output Transition Time, 20%-to-80% 28 ps J2 Jitter Output Jo2 0.42 UI Jo9 0.65 UI J9 Jitter Output 0.29, 0.5 (X1, X2)UI Eye Mask Coordinates #1 (Y1, Y2)150, 425 mV

PSR

50

mVpp

Notes

1. From power on and end of any fault conditions.

LR4 ELECTRICAL CHARACTERISTICS

Power Supply Ripple Tolerance

5. AC coupled with 100 $\boldsymbol{\Omega}$ differential output impedance.

^{2.} After internal AC coupling. Self-biasing 100 Ω differential input.

^{3. 10} MHz-to-11.1 GHz range.

^{4.} Hit ratio = 5 x 10E-5.

^{6.} Output voltage can be set using four discrete steps via I²C. Default is 400-800 mV.

Regulatory Compliance

Transceivers are Class 1 Laser Products and comply with US FDA regulations. These products are certified to meet the Class 1 eye safety requirements of EN (IEC) 60825 and the electrical safety requirements of EN (IEC) 60950. Copies of certificates are available from Intel Corporation upon request.

For Product Information

For information about all Intel® Ethernet Products, visit: intel.com/ethernet

Warranty

Intel limited lifetime hardware warranty, 90-day money-back guarantee (U.S. and Canada) and worldwide support.

Customer Support

For customer support options in North America visit: intel.com/content/www/us/en/support/contact-support.html

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¹ Optical Module Requirements for Intel® Ethernet Network Adapters with QSFP+ Open Optics Support

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