| Parameter | Rating | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 30 | $\mathrm{~V}_{\mathrm{P}}$ |
| Load Current | 1.2 | $\mathrm{~A}_{\mathrm{DC}}$ |
| On-Resistance (max) | 0.25 | $\Omega$ |

Features

- $1500 \mathrm{~V}_{\text {rms }}$ Input/Output Isolation
- Small 4-Pin SOP Package
- Low Drive Power Requirements
- High Reliability
- No EMI/RFI Generation
- Tape \& Reel Version Available
- Flammability Rating UL 94 V-0


## Applications

- Sensor Circuitry
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Industrial Controls


## Description

The CPC1020N is a 30 V , single-pole, normally-open (1-Form-A) Solid State Relay. The ultra-low on-resistance of this relay, $0.25 \Omega$, enables high-current operation.

IXYS Integrated Circuits' patented OptoMOS architecture makes available the optically coupled technology necessary to activate the output's efficient MOSFET switches while providing $1500 \mathrm{~V}_{\text {rms }}$ input to output isolation. Control of the isolated output is accomplished by means of the highly efficient infrared LED at the input.

## Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1172007
- TUV EN 62368-1: Certificate \# B 0826670008

Ordering Information

| Part \# | Description |
| :--- | :--- |
| CPC1020N | 4-Pin SOP (100/tube) |
| CPC1020NTR | 4-Pin SOP (2000/reel) |

## Pin Configuration



## Switching Characteristics of Normally-Open Devices


e3

Absolute Maximum Ratings @ $25^{\circ} \mathrm{C}$

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 30 | $\mathrm{~V}_{\mathrm{p}}$ |
| Input Power Dissipation ${ }^{1}$ | 75 | mW |
| Input Control Current <br> Peak (10ms) | 50 | mA |
| Reverse Input Voltage | 1 | A |
| Total Power Dissipation ${ }^{2}$ | 5 | V |
| Isolation voltage, Input to Output | 400 | mW |
| Operational Temperature, Ambient | -40 to +85 | $\mathrm{~V}_{\text {rs }}$ |
| Storage Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

${ }^{1}$ Derate linearly $1.33 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$
${ }^{2}$ Derate output power linearly $3.33 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at $+25^{\circ} \mathrm{C}$, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @ $25^{\circ} \mathrm{C}$

| Parameter | Conditions | Symbol | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Characteristics |  |  |  |  |  |  |
| Blocking Voltage | $\mathrm{I}_{\mathrm{L}}=1 \mu \mathrm{~A}$ | $\mathrm{V}_{\text {DRM }}$ | 30 | - | - | V |
| Load Current <br> Continuous ${ }^{1}$ <br> Peak | $\mathrm{I}_{\mathrm{F}}=2 \mathrm{~mA}$ | $I_{L}$ | - | - | 1.2 | $A_{D C}$ |
|  | $\mathrm{t} \leq 10 \mathrm{~ms}$ | L LPK | - | - | 3 | A |
| On-Resistance ${ }^{2}$ | $\mathrm{I}_{\mathrm{L}}=1 \mathrm{~A}$ | $\mathrm{R}_{\text {ON }}$ | - | 0.116 | 0.25 | $\Omega$ |
| Off-State Leakage Current | $\mathrm{V}_{L}=30 \mathrm{~V}_{P}$ | $\mathrm{I}_{\text {LEAK }}$ | - | - | 1 | $\mu \mathrm{A}$ |
| Switching Speeds <br> Turn-On <br> Turn-Off | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=10 \mathrm{~V}$ | $t_{\text {on }}$ | - | 0.48 | 3 | ms |
|  | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=10 \mathrm{~V}$ | $\mathrm{t}_{\text {off }}$ | - | 0.65 | 3 |  |
| Output Capacitance | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=25 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {OUT }}$ | - | 33 | - | pF |
| Input Characteristics |  |  |  |  |  |  |
| Input Control Current to Activate | $\mathrm{I}_{\mathrm{L}}=1 \mathrm{~A}$ | $I_{\text {F }}$ | - | 0.13 | 2 | mA |
| Input Control Current to Deactivate | - | $\mathrm{I}_{\mathrm{F}}$ | 0.1 | - | - | mA |
| Input Voltage Drop | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $V_{F}$ | 0.9 | 1.36 | 1.5 | V |
| Reverse Input Current | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | $\mathrm{I}_{\text {R }}$ | - | - | 10 | $\mu \mathrm{A}$ |
| Input/Output Characteristics |  |  |  |  |  |  |
| Capacitance, Input to Output | $\mathrm{V}_{10}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{10}$ | - | 3 | - | pF |

${ }^{1}$ Load current derates linearly from $1.2 \mathrm{~A} @ 25^{\circ} \mathrm{C}$ to $0.58 \mathrm{~A} @ 85^{\circ} \mathrm{C}$.
2 Measurement taken within 1 second of on-time.

## PERFORMANCE DATA*



## PERFORMANCE DATA*




## Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard IPC/JEDEC J-STD-033.

| Device | Moisture Sensitivity Level (MSL) Classification |
| :---: | :---: |
| CPC1020N | MSL 3 |

## ESD Sensitivity

This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

## Soldering Profile

Provided in the table below is the IPC/JEDEC J-STD-020 Classification Temperature ( $\mathrm{T}_{\mathrm{C}}$ ) and the maximum dwell time the body temperature of these surface mount devices may be $\left(T_{C}-5\right)^{\circ} \mathrm{C}$ or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

| Device | Classification Temperature $\left(\mathrm{T}_{\mathrm{d}}\right)$ | Dwell Time $\left(\mathrm{t}_{\mathrm{d}}\right)$ | Max Reflow Cycles |
| :---: | :---: | :---: | :---: |
| CPC1020N | $260^{\circ} \mathrm{C}$ | 30 seconds | 3 |

## Board Wash

IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.
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MECHANICAL DIMENSIONS

## CPC1020N



Note:

1. Lead dimensions do not include plating: 1000 microinches max.
2. Controlling dimension: mm

## CPC1020NTR Tape \& Reel



NOTE: All dimensional tolerances per Standard EIA-481-2 except as noted

For additional information please visit our website at: https://www.ixysic.com

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