CPC1302

| Parameter | Rating | Units |
| :--- | :---: | :---: |
| Breakdown Voltage $-\mathrm{BV}_{\text {CEO }}$ | 350 | $\mathrm{~V}_{\mathrm{P}}$ |
| Current Transfer Ratio - CTR | $1000-8000$ | $\%$ |

## Features

- $350 \mathrm{~V}_{\mathrm{P}}$ Breakdown Voltage
- $3750 \mathrm{~V}_{\text {rms }}$ Input/Output Isolation
- Machine Insertable, Wave Solderable
- Surface Mount Tape \& Reel Version Available


## Applications

- Telecom Switching
- Tip/Ring Circuits
- Hook Switch
- Modem Switching (Laptop, Notebook, Pocket Size)
- Loop Detect
- Ringing Detect
- Current Sensing


## Description

The CPC1302 is a dual optocoupler with two identical, independent channels, each having a unidirectional input and a high-voltage Darlington output. Light output from the highly efficient GaAIAs infrared LED activates its associated, optically coupled silicon NPN photo-Darlington output transistor. The input LED and the output transistor are separated by a $3750 \mathrm{~V}_{\mathrm{rms}}$ isolation barrier.
With a LED current of only 1 mA , a current transfer ratio of $1000 \%$ to $8000 \%$ is guaranteed at the collector of the 350 V Darlington output transistor.
The CPC1302's low input current capability with high current transfer ratios, output voltage capability, and isolation barrier rating make it ideal for many applications such as telecom, industrial, and power control.

## Approvals

- UL 1577 Approved Component: File E76270
- CSA Certified Component: Certificate 1172007
- EN 60950 Certified Component: TUV Certificate B 100549410006


## Ordering Information

| Part Number | Description |
| :--- | :--- |
| CPC1302G | 8-Pin DIP (50/Tube) |
| CPC1302GS | 8-Pin Surface Mount (50/Tube) |
| CPC1302GSTR | 8-Pin Surface Mount (1000/Reel) |

## Pin Configuration



## Absolute Maximum Ratings @ $25^{\circ} \mathrm{C}$

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Breakdown Voltage, $\mathrm{BV}_{\text {CEO }}$ | 350 | $\mathrm{~V}_{\mathrm{p}}$ |
| Reverse Input Voltage | 5 | V |
| Input Control Current <br> Peak (10ms) | 50 | mA |
|  | 1 | A |
| Input Power Dissipation ${ }^{1}$ (Each) | 150 | mW |
| Phototransistor Power Dissipation ${ }^{2}$ (Each) | 150 | mW |
| Isolation Voltage, Input to Output | 3750 | $\mathrm{~V}_{\text {rms }}$ |
| Operational Temperature | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

${ }^{1}$ Derate linearly $1.33 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$
${ }^{2}$ Derate linearly $1.5 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics @ $\mathbf{2 5}^{\circ} \mathrm{C}$

| Parameters | Conditions | Symbol | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Characteristics |  |  |  |  |  |  |
| Phototransistor Breakdown Voltage | $\mathrm{I}_{\text {CEO }}=100 \mu \mathrm{~A}$ | $\mathrm{BV}_{\text {CEO }}$ | 350 | - | - | $V_{P}$ |
| Emitter-Collector Breakdown Voltage | $\mathrm{I}_{\mathrm{E}}=0.1 \mathrm{~mA}$ | $\mathrm{BV}_{\mathrm{ECO}}$ | 0.3 | - | - | V |
| Phototransistor Output (Dark) Current | $\mathrm{V}_{\text {CEO }}=200 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}$ | $\mathrm{I}_{\text {CEO }}$ | - | - | 100 | nA |
| Saturation Voltage | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{F}}=1 \mathrm{~mA}$ | $\mathrm{V}_{\text {CE(sat) }}$ | - | - | 1 | V |
|  | $\mathrm{I}_{\mathrm{C}}=100 \mathrm{~mA}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ |  | - | - | 1.2 | V |
| Current Transfer Ratio | $\mathrm{I}_{\mathrm{F}}=1 \mathrm{~mA}, \mathrm{~V}_{\text {CE }}=1 \mathrm{~V}$ | CTR | 1000 | 5500 | 8000 | \% |
| Output Capacitance | $\mathrm{V}_{\text {CEO }}=50 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {OUT }}$ | - | 13 | - | pF |
| Input Characteristics |  |  |  |  |  |  |
| Input Control Current | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{~V}_{\mathrm{CE}}=1 \mathrm{~V}$ | $\mathrm{I}_{\mathrm{F}}$ | - | 0.07 | 1 | mA |
| Input Voltage Drop | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $V_{F}$ | 0.9 | 1.2 | 1.4 | V |
| Input Reverse Current | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | $I_{R}$ | - | - | 10 | $\mu \mathrm{A}$ |
| Common Characteristics |  |  |  |  |  |  |
| Input to Output Capacitance | - | $\mathrm{C}_{10}$ | - | 3 | - | pF |

Switching Characteristics @ $25^{\circ} \mathrm{C}$

| Characteristic | Symbol | Test Condition | Typ | Units |
| :---: | :---: | :---: | :---: | :---: |
| Rise Time | $\mathrm{t}_{\mathrm{R}}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{cc}}=10 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA} \\ & \mathrm{R}_{\mathrm{L}}=100 \Omega \end{aligned}$ | 40 | $\mu \mathrm{s}$ |
| Fall Time | $\mathrm{t}_{\mathrm{F}}$ |  | 5 |  |
| Turn-On Time | $\mathrm{t}_{\text {on }}$ |  | 5 |  |
| Storage Time | $\mathrm{t}_{5}$ |  | 20 |  |
| Turn-Off Time | $\mathrm{t}_{\text {off }}$ |  | 60 |  |
| Turn-On Time | $\mathrm{t}_{\text {on }}$ | $\mathrm{V}_{\text {cc }}=10 \mathrm{~V}$ | 1 |  |
| Storage Time | $\mathrm{t}_{\text {s }}$ | $\mathrm{I}_{\mathrm{F}}=16 \mathrm{~mA}$ | 40 |  |
| Turn-Off Time | $\mathrm{t}_{\text {off }}$ | $\mathrm{R}_{L}=180 \Omega$ | 80 |  |



Switching Time Test Circuit $\quad V_{c c}$


## PERFORMANCE DATA*



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) rating as shown below, and should be handled according to the requirements of the latest version of the joint industry standard IPC/JEDEC J-STD-033.

| Device | Moisture Sensitivity Level (MSL) Rating |
| :---: | :---: |
| CPC1302G / CPC1302GS | MSL 1 |

## ESD Sensitivity

This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

## Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of J-STD-020 must be observed.

| Device | Maximum Temperature x Time |
| :---: | :---: |
| CPC1302G $/$ CPC1302GS | $250^{\circ} \mathrm{C}$ for 30 seconds |

## Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.


MECHANICAL DIMENSIONS

## CPC1302G



## PCB Hole Pattern



Dimensions mm (inches)

## CPC1302GS



PCB Land Pattern


$\frac{\text { Dimensions }}{\mathrm{mm}}$

## MECHANICAL DIMENSIONS

## CPC1302GSTR Tape \& Reel



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