

Preliminary data

V_{RSM}	$V_{(BR)min}$	V_{RRM}	Туре
V	V	V	
1300	1300	1200	DSA 1-12D
1700	1750	1600	DSA 1-16D
1900	1950	1800	DSA 1-18D





V_{RBM} = 1200-1800 V



A = Anode, C = Cathode

Features

- Plastic standard package
- Planar passivated chips

Applications

- · Low power rectifiers
- Field supply for DC motors
- Power supplies
- High voltage rectifiers

Advantages

- Space and weight savings
- Simple PCB mounting
- Improved temperature & power cycling
- Reduced protection circuits

Symbol Conditions **Characteristic Values** typ. max. $V_{R} = V_{RRM}$ $T_{VJ} = T_{VJM}$ 0.7 I_{R} mΑ VF $I_{\rm F} = 7 \, \text{A}$ $T_{v,j} = 25^{\circ}C$ 1.34 V V V_{T0} For power-loss calculations only 0.8 67 mΩ r_T $T_{VJ} = T_{VJM}$ \mathbf{R}_{thJA} Forced air cooling with 1.5 m/s, $T_{amb} = 45^{\circ}C$ 38 K/W Soldered on to PC board, $T_{amb} = 45^{\circ}C$ 80 K/W 8.5 ds Creepage distance on surface mm Strike distance through air \mathbf{d}_{A} 6.7 mm 100 а Max. allowable acceleration m/s²

Data according to IEC 60747

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20191128c

7 A



Symbol	Conditions	Maximum Ratings	Maximum Ratings	
I _{frms} I _{favm}	$ \begin{array}{l} {{T_{{_{VJ}}}} = {T_{{_{VJM}}}} \\ {{T_{{_{amb}}}} = 45^\circ C;{R_{{_{thJA}}}} = 38 \text{ K/W};180^\circ \sin \alpha \\ {{T_{{_{amb}}}} = 45^\circ C;{R_{{_{thJA}}}} = 80 \text{ K/W};180^\circ \sin \alpha \end{array} } \end{array} $	7 2.3 9 1.3	A A A	
P _{RSM}	$T_{VJM}, t_p = 10 \ \mu s$	1.6 kV	W	
I _{FSM}	$T_{vJ} = 45^{\circ}C;$ t = 10 ms (50 Hz), si t = 8.3 ms (60 Hz), si	ne 110 ne 118	A	
	$T_{vJ} = 150^{\circ}C; t = 10 \text{ ms}$ (50 Hz), si t = 8.3 ms (60 Hz), si	ne 100 ne 104	A	
l²t	$T_{vJ} = 45^{\circ}C;$ t = 10 ms (50 Hz), si t = 8.3 ms (60 Hz), si	ne 60 A ³ ne 58	² S	
	$T_{vJ} = 150^{\circ}C; t = 10 \text{ ms}$ (50 Hz), si t = 8.3 ms (60 Hz), si	ne 50 A ³ ne 45	² S	
T _{VJ} T _{VJM} T _{stg}		-40+150 ° 150 ° -40+150 °	0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0	
Weight	typical	0.8	g	

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