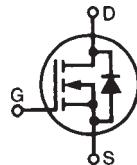


**X-Class HiPerFET™
Power MOSFET**
**IXFA30N60X
IXFP30N60X**

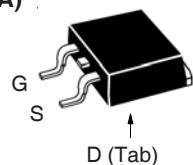
V_{DSS} = 600V
I_{D25} = 30A
R_{DS(on)} ≤ 155mΩ



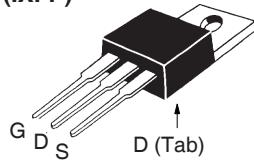
N-Channel Enhancement Mode
Avalanche Rated
Fast Intrinsic Diode

Symbol	Test Conditions	Maximum Ratings	
V _{DSS}	T _J = 25°C to 150°C	600	V
V _{DGR}	T _J = 25°C to 150°C, R _{GS} = 1MΩ	600	V
V _{GSS}	Continuous	±30	V
V _{GSM}	Transient	±40	V
I _{D25}	T _C = 25°C	30	A
I _{DM}	T _C = 25°C, Pulse Width Limited by T _{JM}	60	A
I _A	T _C = 25°C	10	A
E _{AS}	T _C = 25°C	1	J
dv/dt	I _S ≤ I _{DM} , V _{DD} ≤ V _{DSS} , T _J ≤ 150°C	50	V/ns
P _D	T _C = 25°C	500	W
T _J		-55 ... +150	°C
T _{JM}		150	°C
T _{stg}		-55 ... +150	°C
T _L	Maximum Lead Temperature for Soldering	300	°C
T _{SOLD}	1.6 mm (0.062in.) from Case for 10s	260	°C
F _c	Mounting Force (TO-263)	10.65 / 2.2..14.6	N/lb
M _d	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in
Weight	TO-263	2.5	g
	TO-220	3.0	g

TO-263 (IXFA)



TO-220 (IXFP)



G = Gate D = Drain
 S = Source Tab = Drain

Features

- International Standard Packages
- Low R_{DS(ON)} and Q_G
- Avalanche Rated
- Low Package Inductance

Advantages

- High Power Density
- Easy to Mount
- Space Savings

Applications

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- PFC Circuits
- AC and DC Motor Drives
- Robotics and Servo Controls

Symbol	Test Conditions (T _J = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV _{DSS}	V _{GS} = 0V, I _D = 1mA	600		V
V _{GS(th)}	V _{DS} = V _{GS} , I _D = 4mA	2.5		4.5 V
I _{GSS}	V _{GS} = ±30V, V _{DS} = 0V			±100 nA
I _{DSS}	V _{DS} = V _{DSS} , V _{GS} = 0V T _J = 125°C			25 μA 750 μA
R _{DS(on)}	V _{GS} = 10V, I _D = 0.5 • I _{D25} , Note 1			155 mΩ

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
g_{fs}	$V_{DS} = 10\text{V}$, $I_D = 0.5 \cdot I_{D25}$, Note 1	10	17	S
R_{Gi}	Gate Input Resistance		2.6	Ω
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0\text{V}$, $V_{DS} = 25\text{V}$, $f = 1\text{MHz}$	2270 1610 14		pF
Effective Output Capacitance				
$C_{o(er)}$	Energy related } $V_{GS} = 0\text{V}$	120		pF
$C_{o(tr)}$	Time related } $V_{DS} = 0.8 \cdot V_{DSS}$	375		pF
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	Resistive Switching Times $V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 0.5 \cdot I_{D25}$ $R_G = 5\Omega$ (External)	21 43 58 33		ns
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 0.5 \cdot I_{D25}$	56 12 28		nC
R_{thJC} R_{thCS}	TO-220	0.50	0.25	$^\circ\text{C}/\text{W}$
			0.50	$^\circ\text{C}/\text{W}$

Source-Drain Diode

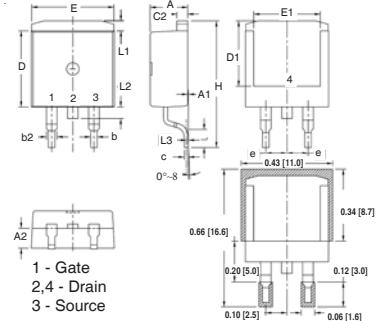
Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
I_s	$V_{GS} = 0\text{V}$		30	A
I_{SM}	Repetitive, pulse Width Limited by T_{JM}		120	A
V_{SD}	$I_F = I_s$, $V_{GS} = 0\text{V}$, Note 1		1.4	V
t_{rr} Q_{RM} I_{RM}	$I_F = 15\text{A}$, $-di/dt = 100\text{A}/\mu\text{s}$ $V_R = 100\text{V}$	145 860 12		ns nC A

Note 1. Pulse test, $t \leq 300\mu\text{s}$, duty cycle, $d \leq 2\%$.

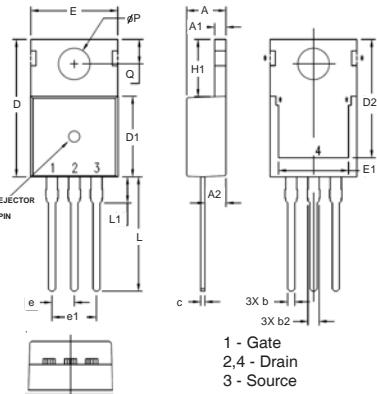
ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

TO-263 Outline



TO-220 Outline



IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065B1 6,683,344 6,727,585 7,005,734B2 7,157,338B2 4,860,072 5,017,508 5,063,307 5,381,025 6,259,123B1 6,534,343 6,710,405B2 6,759,692 7,063,975B2 4,881,106 5,034,796 5,187,117 5,486,715 6,306,728B1 6,583,505 6,710,463 6,771,478B2 7,071,537

Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

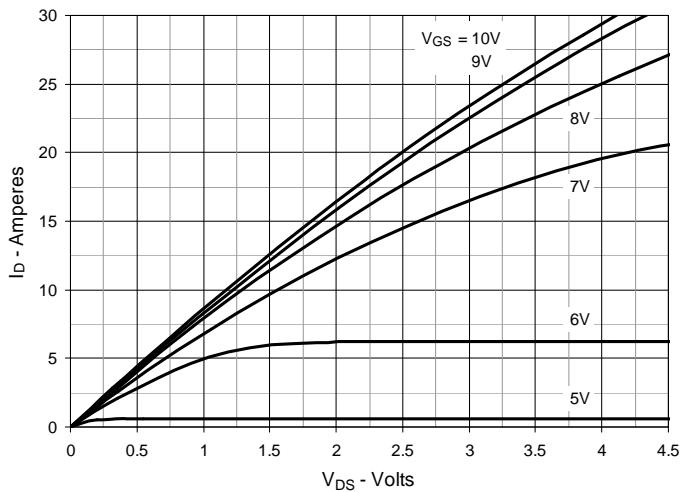


Fig. 3. Output Characteristics @ $T_J = 125^\circ\text{C}$

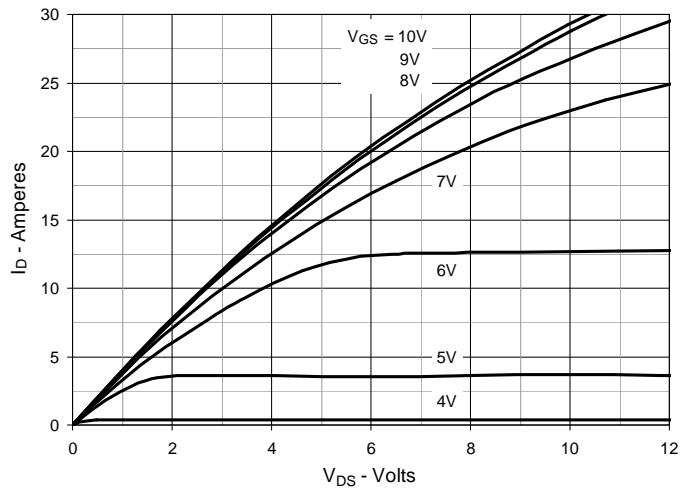


Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 15\text{A}$ Value vs. Drain Current

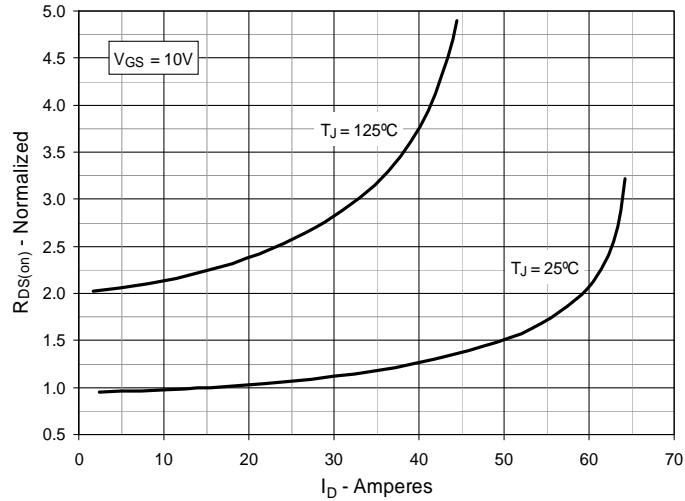


Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

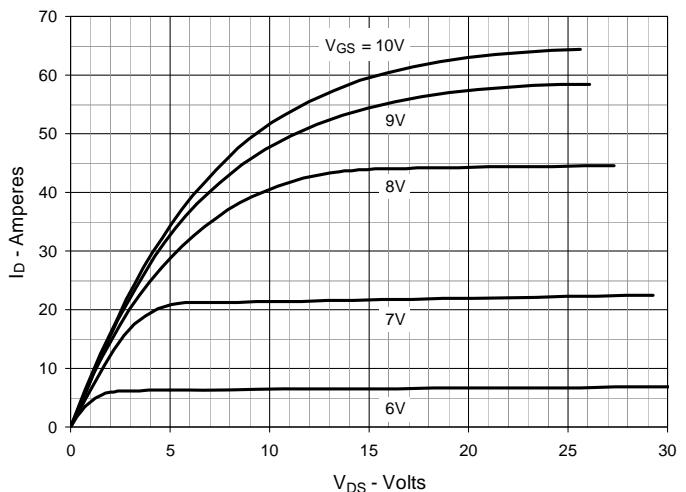


Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 15\text{A}$ Value vs. Junction Temperature

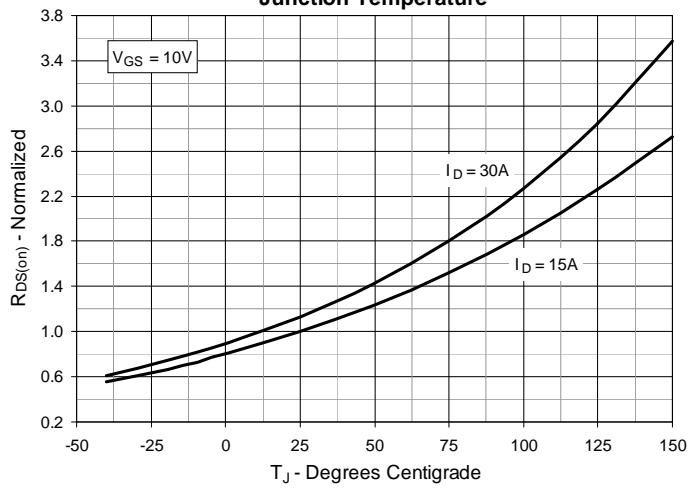
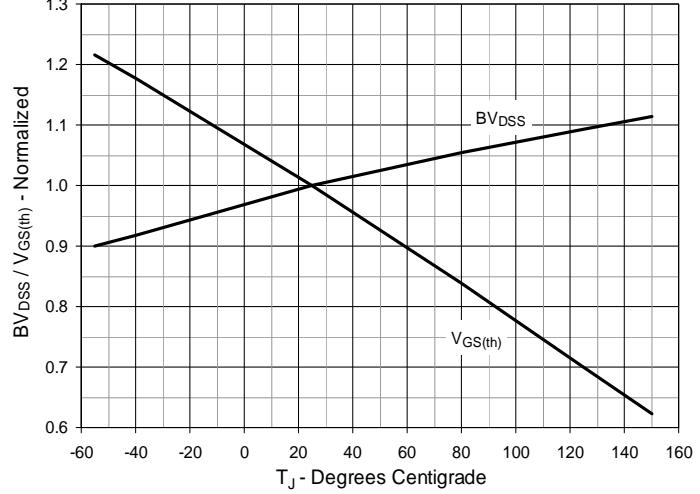


Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature



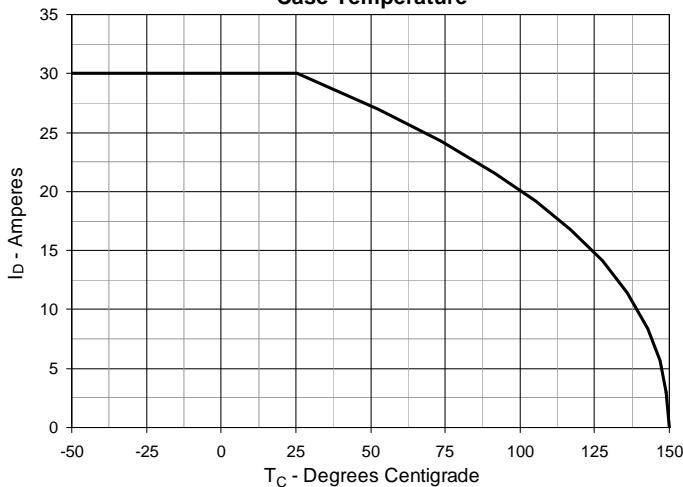
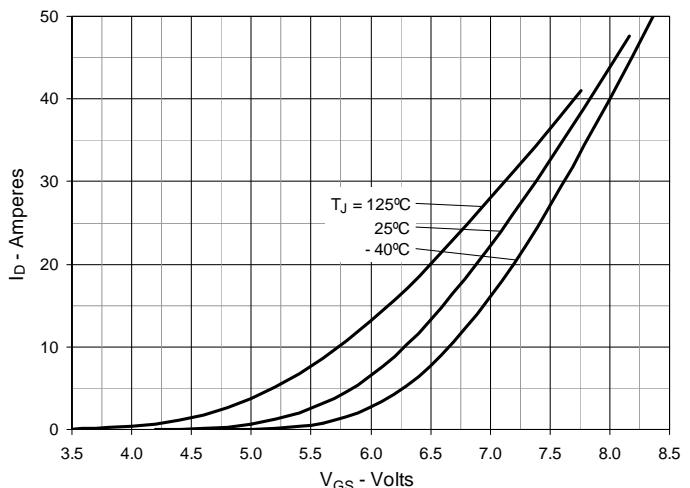
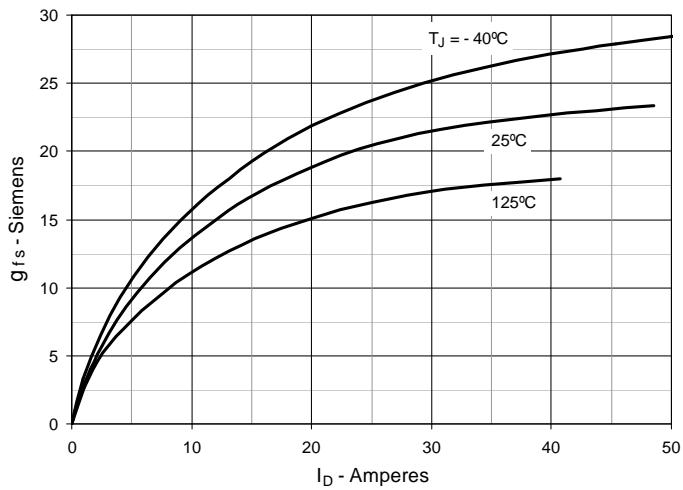
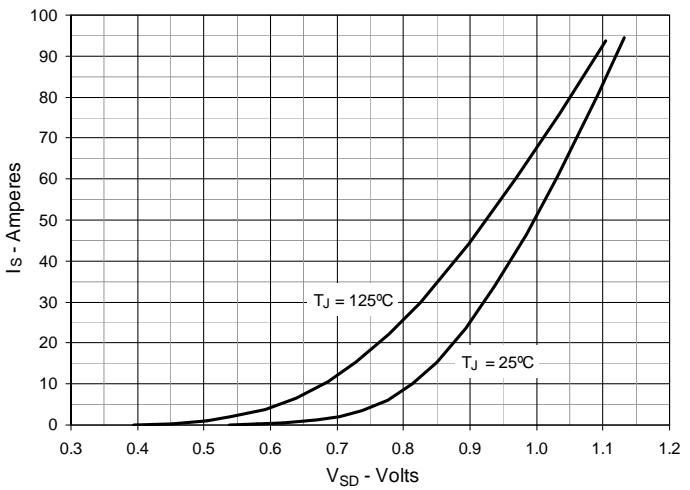
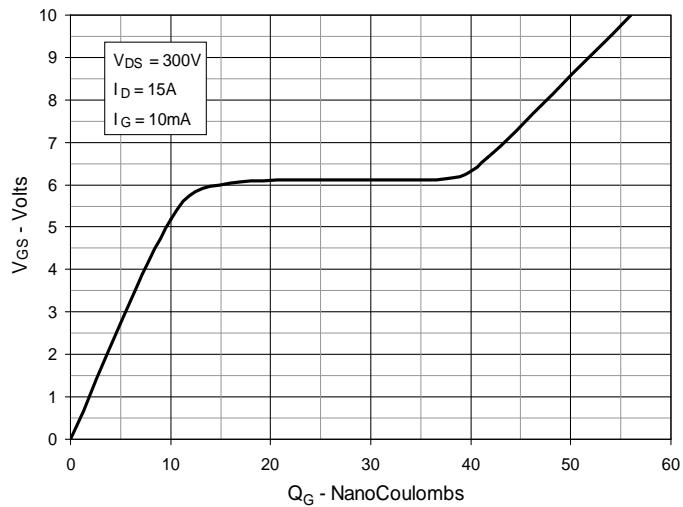
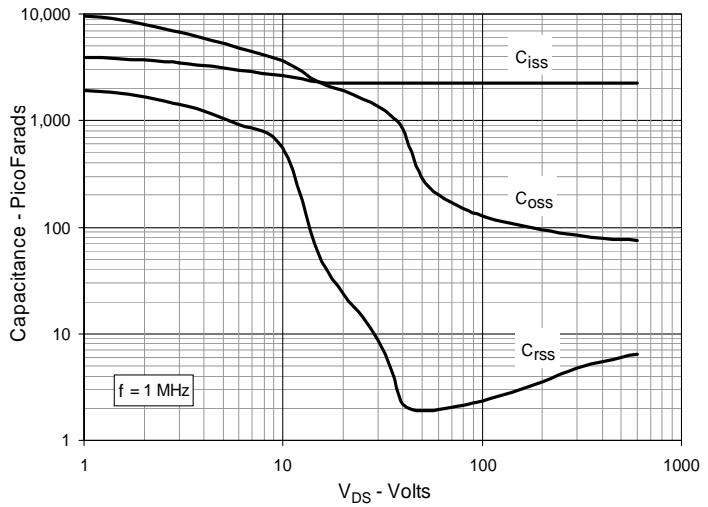
**Fig. 7. Maximum Drain Current vs.
Case Temperature**

Fig. 8. Input Admittance

Fig. 9. Transconductance

Fig. 10. Forward Voltage Drop of Intrinsic Diode

Fig. 11. Gate Charge

Fig. 12. Capacitance


Fig. 13. Output Capacitance Stored Energy

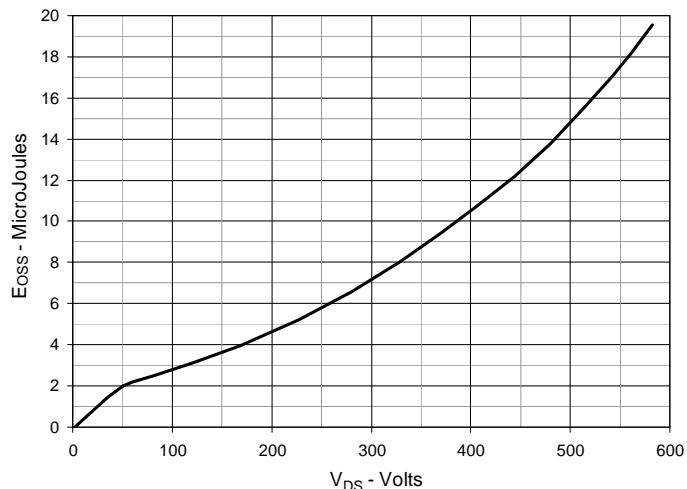


Fig. 14. Forward-Bias Safe Operating Area

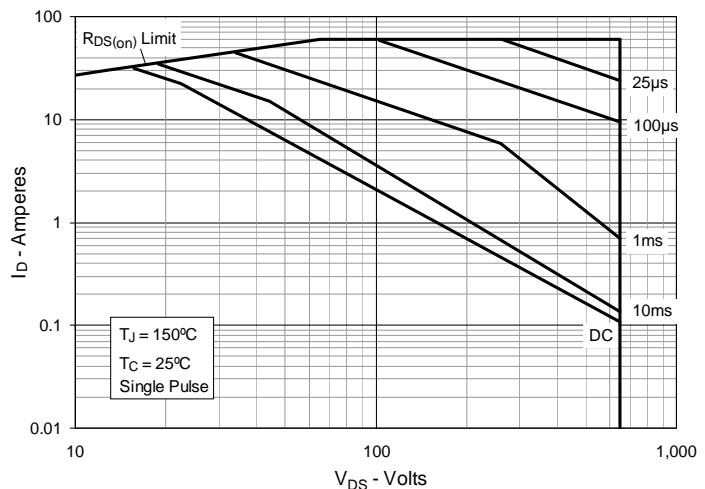
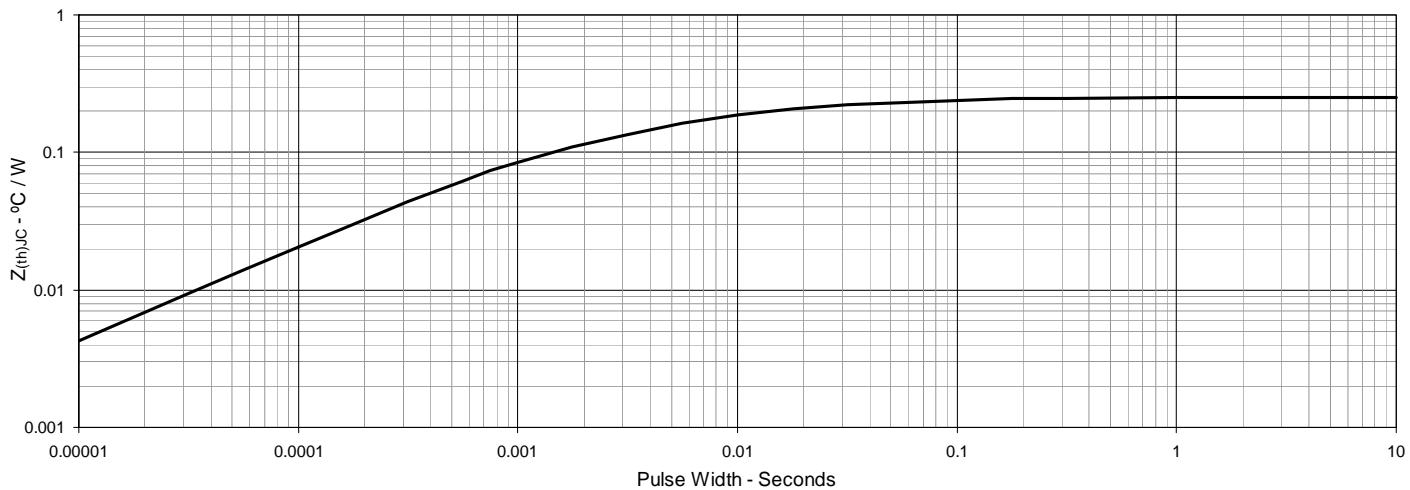


Fig. 15. Maximum Transient Thermal Impedance



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