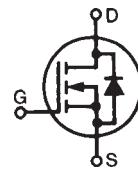


PolarHT™ HiPerFET Power MOSFET

N-Channel Enhancement Mode
Fast Intrinsic Diode
Avalanche Rated

IXFH 110N10P
IXFV 110N10P
IXFV 110N10PS

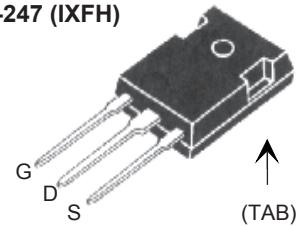
$V_{DSS} = 100 \text{ V}$
 $I_{D25} = 110 \text{ A}$
 $R_{DS(on)} \leq 15 \text{ m}\Omega$
 $t_{rr} \leq 150 \text{ ns}$



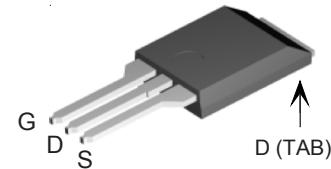
Symbol	Test Conditions	Maximum Ratings		
V_{DSS}	$T_J = 25^\circ\text{C}$ to 175°C	100		V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 175°C ; $R_{GS} = 1 \text{ M}\Omega$	100		V
V_{GSS}	Continuous	± 20		V
V_{GSM}	Transient	± 30		V
I_{D25}	$T_c = 25^\circ\text{C}$	110		A
$I_{D(\text{RMS})}$	External lead current limit	75		A
I_{DM}	$T_c = 25^\circ\text{C}$, pulse width limited by T_{JM}	250		A
I_{AR}	$T_c = 25^\circ\text{C}$	60		A
E_{AR}	$T_c = 25^\circ\text{C}$	40		mJ
E_{AS}	$T_c = 25^\circ\text{C}$	1.0		J
dv/dt	$I_s \leq I_{DM}$, $di/dt \leq 100 \text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$, $R_G = 4 \Omega$	10		V/ns
P_D	$T_c = 25^\circ\text{C}$	480		W
T_J		-55 ... +175		$^\circ\text{C}$
T_{JM}		175		$^\circ\text{C}$
T_{stg}		-55 ... +150		$^\circ\text{C}$
T_L	1.6 mm (0.062 in.) from case for 10 s	300		$^\circ\text{C}$
T_{SOLD}	Plastic body for 10 s	260		$^\circ\text{C}$
M_d	Mounting torque (TO-247)	1.13/10		Nm/lb.in.
F_c	Mounting Force (PLUS220)	11..65 / 2.5..15		N/lb
Weight	TO-247 PLUS220	6 4		g g

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
BV_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	100		V
$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 4 \text{ mA}$	2.5		5.0 V
I_{GSS}	$V_{GS} = \pm 20 \text{ V}_{DC}$, $V_{DS} = 0$		± 100	nA
I_{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$		25 250	μA
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$, $I_D = 0.5 I_{D25}$ Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$		15	$\text{m}\Omega$

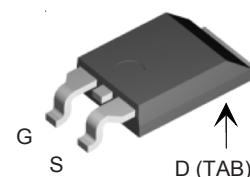
TO-247 (IXFH)



PLUS220 (IXFV)



PLUS220SMD (IXFV...S)



G = Gate
S = Source
D = Drain
TAB = Drain

Features

- Fast intrinsic diode
- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
 - easy to drive and to protect

Advantages

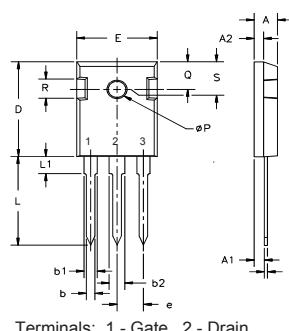
- Easy to mount
- Space savings
- High power density

Symbol **Test Conditions**
Characteristic Values
 $(T_J = 25^\circ C, \text{ unless otherwise specified})$
Min. **Typ.** **Max.**

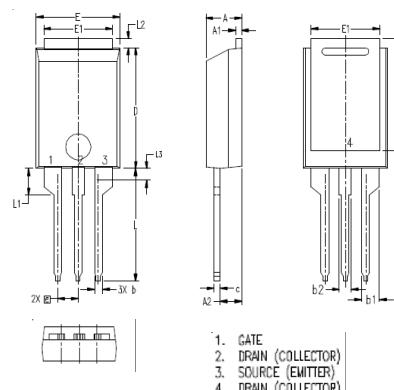
g_{fs}	$V_{DS} = 10 \text{ V}; I_D = 0.5 I_{D25}$, pulse test	30	40	S
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	3550	pF	
		1370	pF	
		440	pF	
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 60 \text{ A}$ $R_G = 4 \Omega$ (External)	21	ns	
		25	ns	
		65	ns	
		25	ns	
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$	110	nC	
		25	nC	
		62	nC	
R_{thJC}			0.31	$^\circ\text{C}/\text{W}$
R_{thcs}	(TO-247)	0.21		$^\circ\text{C}/\text{W}$

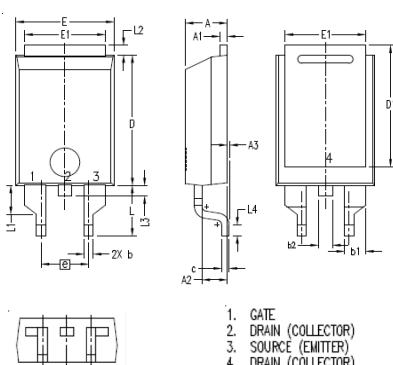
Source-Drain Diode
Characteristic Values
 $(T_J = 25^\circ C, \text{ unless otherwise specified})$
Symbol **Test Conditions**
Min. **Typ.** **Max.**

I_s	$V_{GS} = 0 \text{ V}$		110	A
I_{SM}	Repetitive		250	A
V_{SD}	$I_F = I_s, V_{GS} = 0 \text{ V}$ Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2\%$		1.5	V
t_{rr} Q_{RM}	$I_F = 25 \text{ A}, -di/dt = 100 \text{ A}/\mu\text{s}$ $V_R = 50 \text{ V}, V_{GS} = 0 \text{ V}$		150	ns
		0.6		μC

TO-247 (IXFH) Outline

 Terminals: 1 - Gate 2 - Drain
3 - Source TAB - Drain

Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.7	5.3	.185	.209
A ₁	2.2	2.54	.087	.102
A ₂	2.2	2.6	.059	.098
b	1.0	1.4	.040	.055
b ₁	1.65	2.13	.065	.084
b ₂	2.87	3.12	.113	.123
C	.4	.8	.016	.031
D	20.80	21.46	.819	.845
E	15.75	16.26	.610	.640
e	5.20	5.72	.205	.225
L	19.81	20.32	.780	.800
L ₁		4.50		.177
ØP	3.55	3.65	.140	.144
Q	5.89	6.40	.232	.252
R	4.32	5.49	.170	.216
S	6.15	BSC	242	BSC

PLUS220 (IXFV) Outline

 1. GATE
2. DRAIN (COLLECTOR)
3. SOURCE (EMITTER)
4. DRAIN (COLLECTOR)

PLUS220SMD (IXFV_S) Outline

 1. GATE
2. DRAIN (COLLECTOR)
3. SOURCE (EMITTER)
4. DRAIN (COLLECTOR)

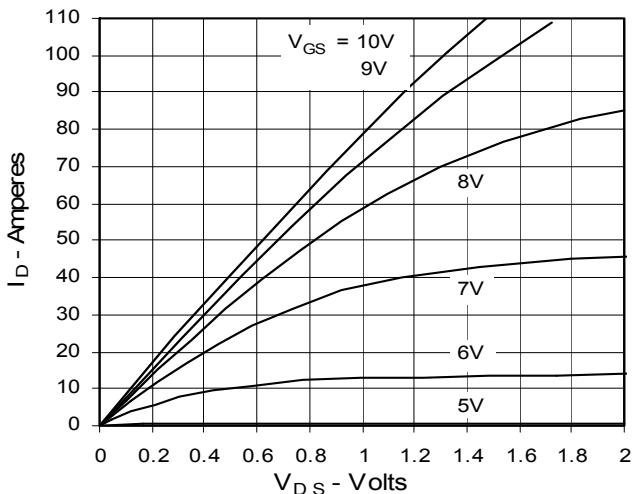
SYM	INCHES		MILLIMETER	
	MIN	MAX	MIN	MAX
A	.169	.185	4.30	4.70
A ₁	.028	.035	.70	0.90
A ₂	.098	.118	2.50	3.00
A ₃	.000	.010	0.00	0.25
b	.035	.047	0.90	1.20
b ₁	.080	.095	2.03	2.41
b ₂	.054	.064	1.37	1.63
c	.028	.035	0.70	0.90
D	.551	.591	14.00	15.00
D ₁	.512	.539	13.00	13.70
E	.394	.433	10.00	11.00
E ₁	.331	.346	8.40	8.80
e	.200	BSC	5.08	BSC
L	.209	.228	5.30	5.80
L ₁	.118	.138	3.00	3.50
L ₂	.035	.051	0.90	1.30
L ₃	.047	.059	1.20	1.50
L ₄	.039	.059	1.00	1.50

SYM	INCHES		MILLIMETER	
	MIN	MAX	MIN	MAX
A	.169	.185	4.30	4.70
A ₁	.028	.035	.70	0.90
A ₂	.098	.118	2.50	3.00
b	.035	.047	0.90	1.20
b ₁	.080	.095	2.03	2.41
b ₂	.054	.064	1.37	1.63
c	.028	.035	0.70	0.90
D	.551	.591	14.00	15.00
D ₁	.512	.539	13.00	13.70
E	.394	.433	10.00	11.00
E ₁	.331	.346	8.40	8.80
e	.100	BSC	2.54	BSC
L	.512	.551	13.00	14.00
L ₁	.118	.138	3.00	3.50
L ₂	.035	.051	0.90	1.30
L ₃	.047	.059	1.20	1.50

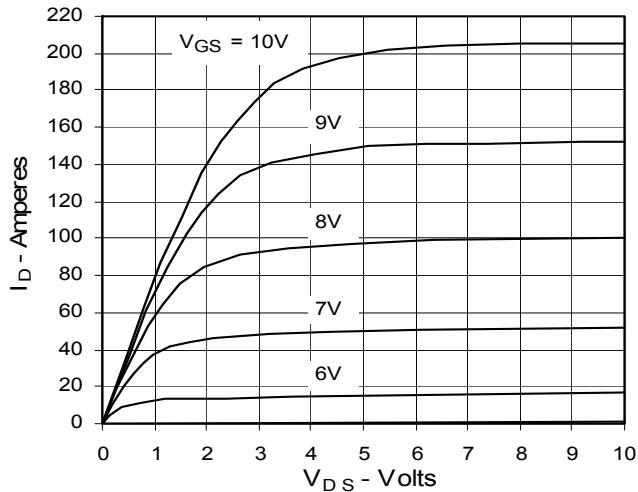
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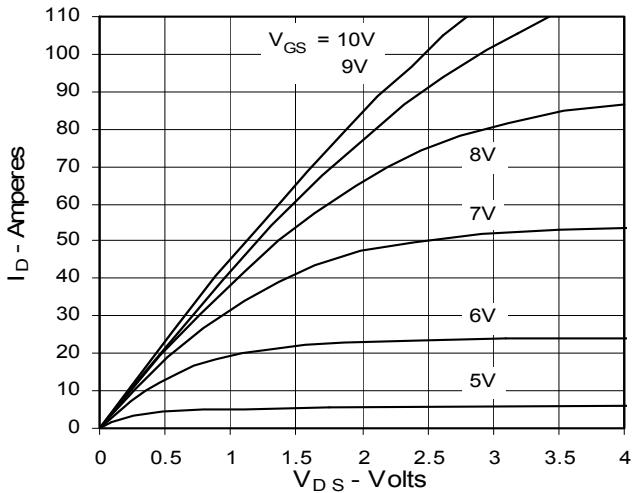
**Fig. 1. Output Characteristics
@ 25°C**



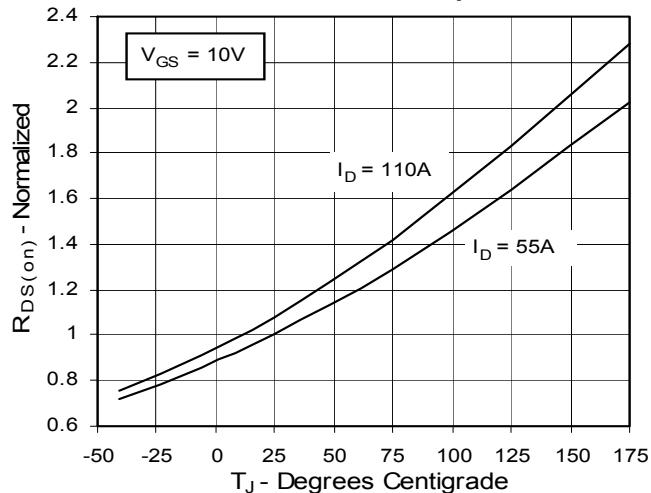
**Fig. 2. Extended Output Characteristics
@ 25°C**



**Fig. 3. Output Characteristics
@ 150°C**



**Fig. 4. $R_{DS(on)}$ Normalized to 0.5 I_{D25}
Value vs. Junction Temperature**



**Fig. 5. $R_{DS(on)}$ Normalized to 0.5 I_{D25}
Value vs. Drain Current**

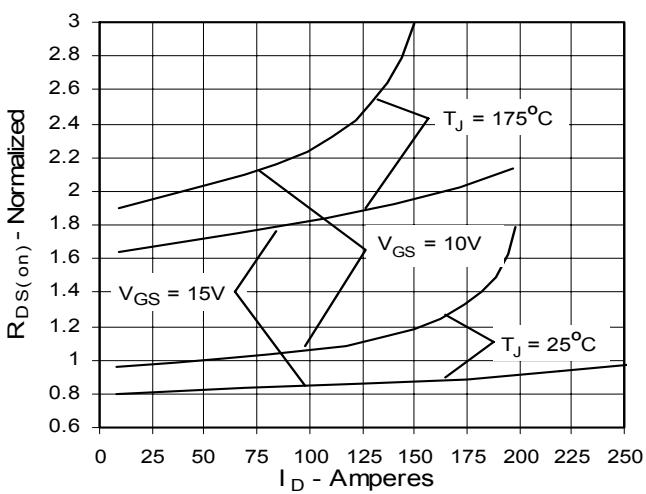


Fig. 6. Drain Current vs. Case Temperature

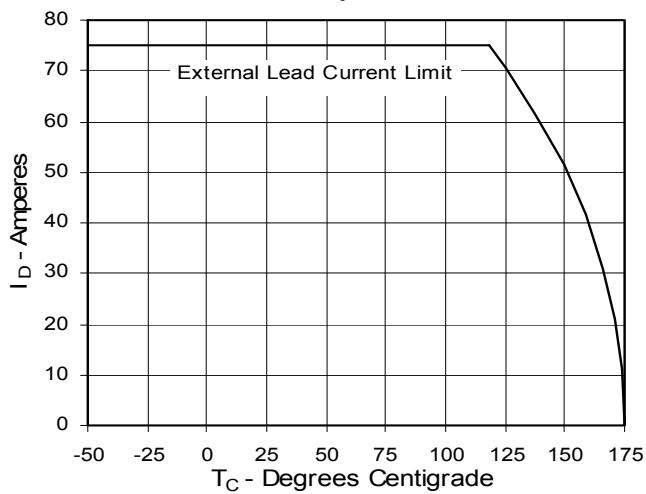


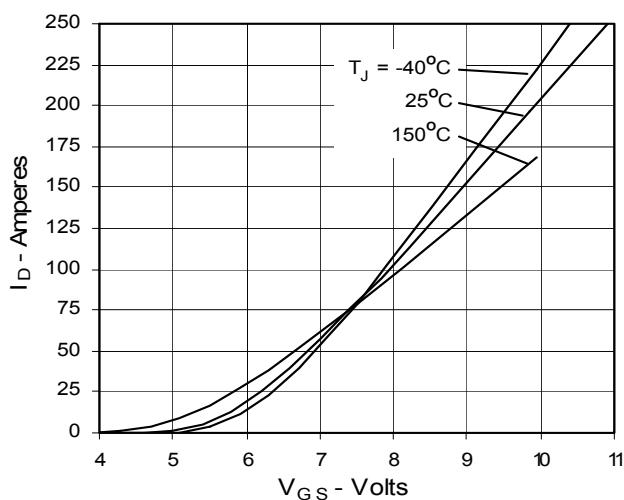
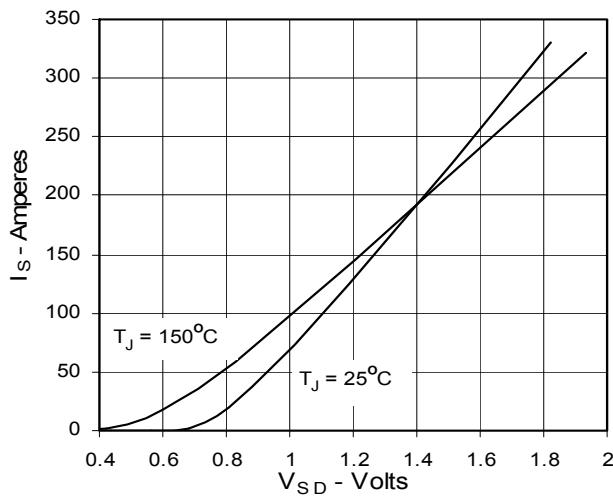
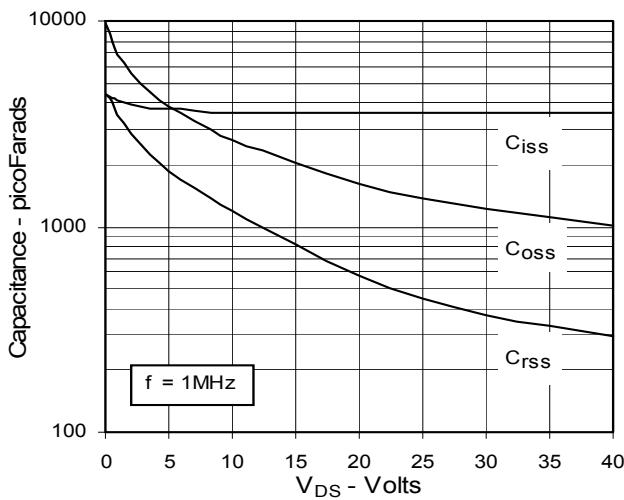
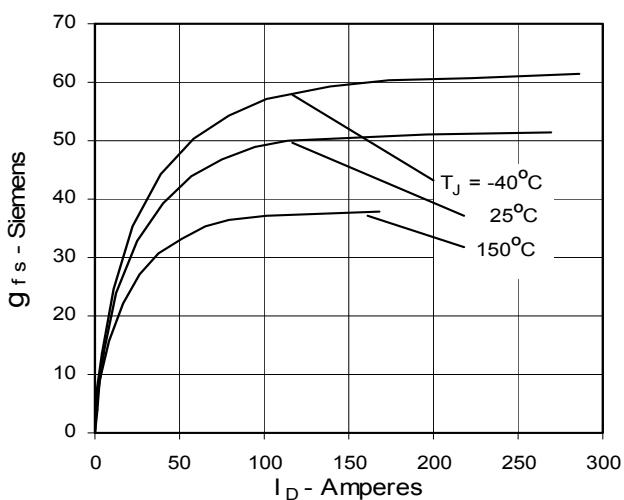
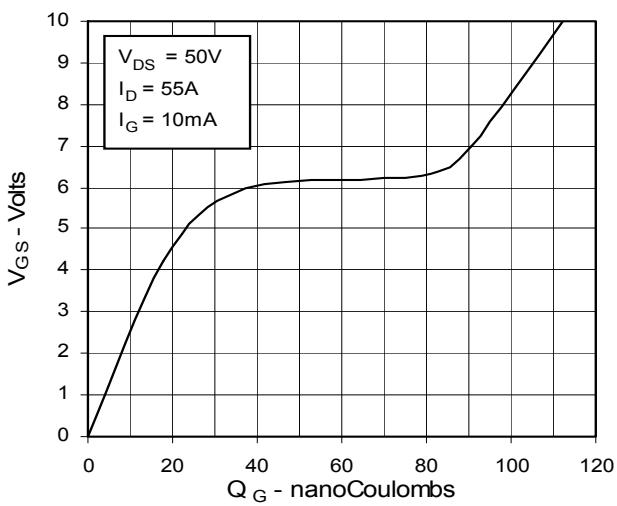
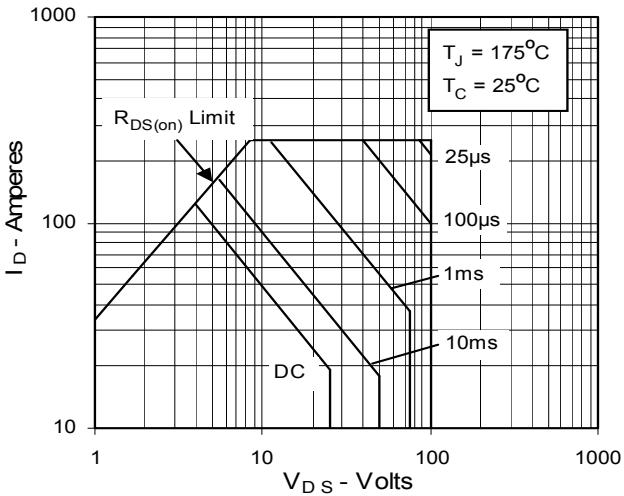
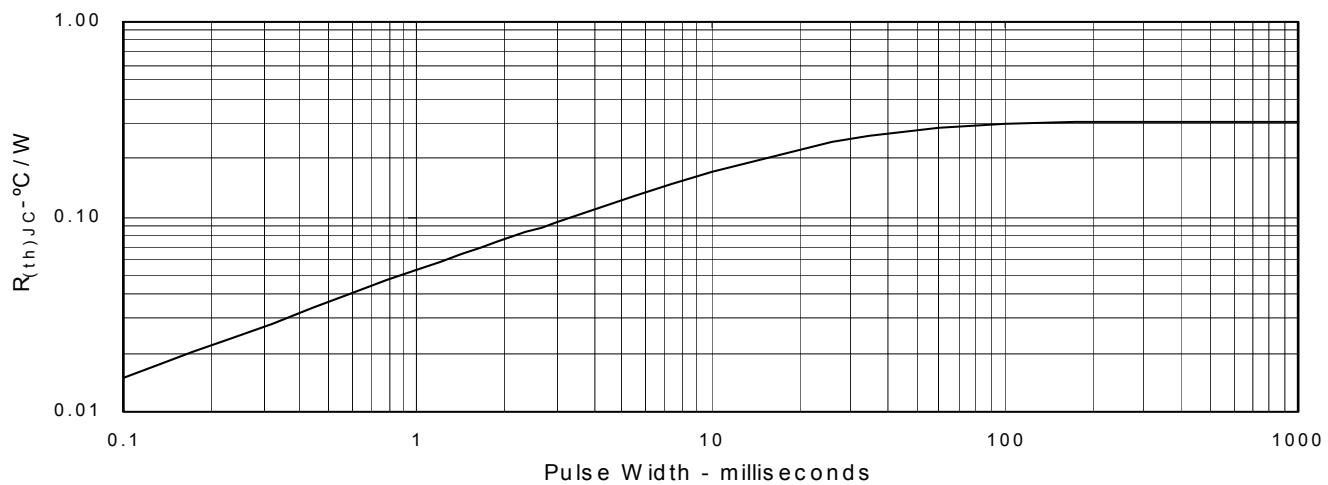
Fig. 7. Input Admittance

**Fig. 9. Source Current vs.
Source-To-Drain Voltage**

Fig. 11. Capacitance

Fig. 8. Transconductance

Fig. 10. Gate Charge

**Fig. 12. Forward-Bias
Safe Operating Area**


Fig. 13. Maximum Transient Thermal Resistance



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