

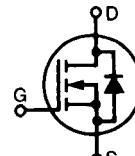
# HiPerFET™ Power MOSFETs Q-Class

N-Channel Enhancement Mode  
Avalanche Rated Low Q<sub>g</sub>, High dv/dt

**IXFH 9N80Q**  
**IXFT 9N80Q**

V<sub>DSS</sub> = 800 V  
I<sub>D25</sub> = 9 A  
R<sub>DS(on)</sub> = 1.1 Ω

t<sub>rr</sub> ≤ 250 ns

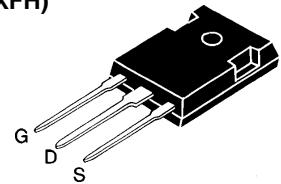


## Maximum Ratings

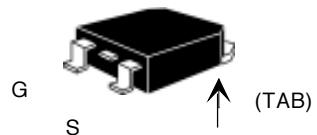
<b>V<sub>DSS</sub></b>	T <sub>J</sub> = 25°C to 150°C	800	V
<b>V<sub>DGR</sub></b>	T <sub>J</sub> = 25°C to 150°C; R <sub>GS</sub> = 1 MΩ	800	V
<b>V<sub>GS</sub></b>	Continuous	±20	V
<b>V<sub>GSM</sub></b>	Transient	±30	V
<b>I<sub>D25</sub></b>	T <sub>C</sub> = 25°C	9	A
<b>I<sub>DM</sub></b>	T <sub>C</sub> = 25°C, pulse width limited by T <sub>JM</sub>	36	A
<b>I<sub>AR</sub></b>	T <sub>C</sub> = 25°C	9	A
<b>E<sub>AR</sub></b>	T <sub>C</sub> = 25°C	20	mJ
<b>E<sub>AS</sub></b>		700	mJ
<b>dv/dt</b>	I <sub>S</sub> ≤ I <sub>DM</sub> , di/dt ≤ 100 A/μs, V <sub>DD</sub> ≤ V <sub>DSS</sub> , T <sub>J</sub> ≤ 150°C, R <sub>G</sub> = 2 Ω	5	V/ns
<b>P<sub>D</sub></b>	T <sub>C</sub> = 25°C	180	W
<b>T<sub>J</sub></b>		-55 ... +150	°C
<b>T<sub>JM</sub></b>		150	°C
<b>T<sub>stg</sub></b>		-55 ... +150	°C
<b>T<sub>L</sub></b>	1.6 mm (0.063 in) from case for 10 s	300	°C
<b>M<sub>d</sub></b>	Mounting torque	1.13/10	Nm/lb.in.
<b>Weight</b>	TO-247	6	g
	TO-268	4	g

Symbol	Test Conditions	Characteristic Values		
		(T <sub>J</sub> = 25°C, unless otherwise specified)	min.	typ.
<b>V<sub>DSS</sub></b>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA	800		V
<b>V<sub>GS(th)</sub></b>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 2.5 mA	3.0		5.0 V
<b>I<sub>GSS</sub></b>	V <sub>GS</sub> = ±20 V <sub>DC</sub> , V <sub>DS</sub> = 0		±100	nA
<b>I<sub>DSS</sub></b>	V <sub>DS</sub> = 0.8 V <sub>DSS</sub> V <sub>GS</sub> = 0 V	T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C	50 1	μA mA
<b>R<sub>DS(on)</sub></b>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 I <sub>D25</sub> Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %		1.1	Ω

TO-247 AD (IXFH)



TO-268 (D3) (IXFT)



G = Gate      D = Drain  
S = Source      TAB = Drain

## Features

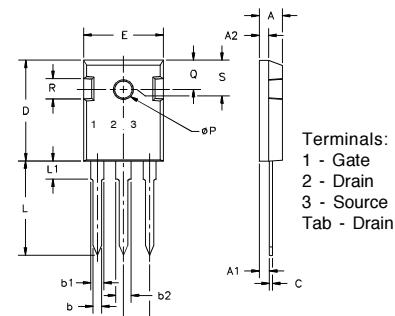
- IXYS advanced low Q<sub>g</sub> process
- Low gate charge and capacitances
  - easier to drive
  - faster switching
- International standard packages
- Low R<sub>DS(on)</sub>
- Unclamped Inductive Switching (UIS) rated
- Molding epoxies meet UL 94 V-0 flammability classification

## Advantages

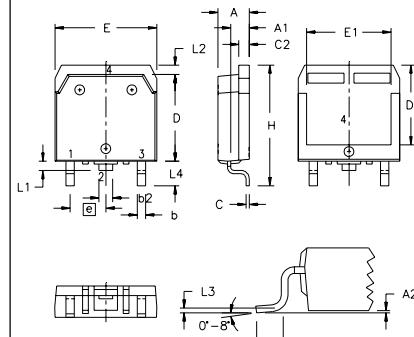
- Easy to mount
- Space savings
- High power density

Symbol	Test Conditions	Characteristic Values		
		( $T_J = 25^\circ\text{C}$ , unless otherwise specified)	min.	typ.
$g_{fs}$	$V_{DS} = 10 \text{ V}; I_D = 0.5 \cdot I_{D25}$ , pulse test	3	5	S
$C_{iss}$		2200		pF
$C_{oss}$		240		pF
$C_{rss}$		41		pF
$t_{d(on)}$		20		ns
$t_r$		20		ns
$t_{d(off)}$	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 0.5 \cdot I_{D25}$	42		ns
$t_f$	$R_G = 4.7 \Omega$ (External),	13		ns
$Q_{g(on)}$		56		nC
$Q_{gs}$		17		nC
$Q_{gd}$		22		nC
$R_{thJC}$			0.7	K/W
$R_{thCK}$	(TO-247)	0.25		K/W

Symbol	Test Conditions	Characteristic Values			
		( $T_J = 25^\circ\text{C}$ , unless otherwise specified)	min.	typ.	max.
$I_s$	$V_{GS} = 0 \text{ V}$			9	A
$I_{SM}$	Repetitive; pulse width limited by $T_{JM}$			36	A
$V_{SD}$	$I_F = I_S, V_{GS} = 0 \text{ V}$ , Pulse test, $t \leq 300 \mu\text{s}$ , duty cycle $d \leq 2 \%$			1.5	V
$t_{rr}$			250	ns	
$Q_{RM}$			0.75	$\mu\text{C}$	
$I_{RM}$	$I_F = I_S, -di/dt = 100 \text{ A}/\mu\text{s}, V_R = 100 \text{ V}$	7.5		A	

**TO-247 AD (IXFH) Outline**


Dim.	Millimeter Min.	Max.	Inches Min.	Max.
A	4.7	5.3	.185	.209
A <sub>1</sub>	2.2	2.54	.087	.102
A <sub>2</sub>	2.2	2.6	.059	.098
b	1.0	1.4	.040	.055
b <sub>1</sub>	1.65	2.13	.065	.084
b <sub>2</sub>	2.87	3.12	.113	.123
C	4	8	.016	.031
D	20.80	21.46	.819	.845
E	15.75	16.26	.610	.640
e	5.20	5.72	.205	.225
L	19.81	20.32	.780	.800
L <sub>1</sub>		4.50		.177
QP	3.55	3.65	.140	.144
Q	5.89	6.40	0.232	0.252
R	4.32	5.49	.170	.216
S	6.15	BSC	.242	BSC

**TO-268 Outline**


Terminals:

- 1 - Gate
- 2 - Drain
- 3 - Source
- Tab - Drain

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.193	.201	4.90	5.10
A <sub>1</sub>	.106	.114	2.70	2.90
A <sub>2</sub>	.001	.010	0.02	0.25
b	.045	.057	1.15	1.45
b <sub>2</sub>	.075	.083	1.90	2.10
C	.016	.026	0.40	0.65
C <sub>2</sub>	.057	.063	1.45	1.60
D	.543	.551	13.80	14.00
D <sub>1</sub>	.488	.500	12.40	12.70
E	.624	.632	15.85	16.05
E <sub>1</sub>	.524	.535	13.30	13.60
e	.215	BSC	5.45	BSC
H	.736	.752	18.70	19.10
L	.094	.106	2.40	2.70
L <sub>1</sub>	.047	.055	1.20	1.40
L <sub>2</sub>	.039	.045	1.00	1.15
L <sub>3</sub>	.010	BSC	0.25	BSC
L <sub>4</sub>	.150	.161	3.80	4.10

IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,881,106  
4,850,072 4,931,844

5,017,508 5,049,961 5,187,117 5,486,715  
5,034,796 5,063,307 5,237,481 5,381,025

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