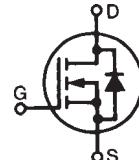


# PolarHV™ HiPerFET Power MOSFET

**IXFK 80N50P**  
**IXFX 80N50P**

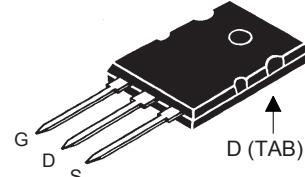
**V<sub>DSS</sub>** = 500 V  
**I<sub>D25</sub>** = 80 A  
**R<sub>DS(on)</sub>** ≤ 65 mΩ  
**t<sub>rr</sub>** ≤ 200 ns

N-Channel Enhancement Mode  
Avalanche Rated  
Fast Intrinsic Diode

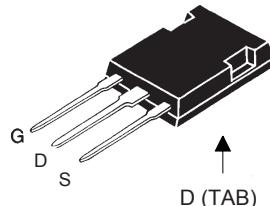


Symbol	Test Conditions	Maximum Ratings		
V <sub>DSS</sub>	T <sub>J</sub> = 25°C to 150°C	500	V	
V <sub>DGR</sub>	T <sub>J</sub> = 25°C to 150°C; R <sub>GS</sub> = 1 MΩ	500	V	
V <sub>GSM</sub>	Transient	± 40	V	
V <sub>GSM</sub>	Continuous	± 30	V	
I <sub>D25</sub>	T <sub>C</sub> = 25°C	80	A	
I <sub>L</sub>	Lead Current Limit, RMS	75	A	
I <sub>DM</sub>	T <sub>C</sub> = 25°C, pulse width limited by T <sub>JM</sub>	200	A	
I <sub>AR</sub>	T <sub>C</sub> = 25°C	80	A	
E <sub>AR</sub>	T <sub>C</sub> = 25°C	80	mJ	
E <sub>AS</sub>	T <sub>C</sub> = 25°C	3.5	J	
dv/dt	I <sub>S</sub> ≤ I <sub>DM</sub> , di/dt ≤ 100 A/μs, V <sub>DD</sub> ≤ V <sub>DSS</sub> , T <sub>J</sub> ≤ 150°C, R <sub>G</sub> = 2 Ω	20	V/ns	
P <sub>D</sub>	T <sub>C</sub> = 25°C	1040	W	
T <sub>J</sub>		-55 ... +150	°C	
T <sub>JM</sub>		150	°C	
T <sub>stg</sub>		-55 ... +150	°C	
T <sub>L</sub>	1.6 mm (0.062 in.) from case for 10 s	300	°C	
T <sub>SOLD</sub>	Plastic body for 10 seconds	260	°C	
F <sub>c</sub>	Mounting force (PLUS247)	20..120/4.5..25	N/lb	
M <sub>d</sub>	Mounting torque (TO-264)	1.13/10	Nm/lb.in.	
Weight	TO-264	10	g	
	PLUS247	6	g	

TO-264 (IXFK)



PLUS247 (IXFX)



G = Gate      S = Source  
D = Drain      Tab = Collector

Symbol Test Conditions  
(T<sub>J</sub> = 25°C unless otherwise specified)

Characteristic Values  
Min.    Typ.    Max.

BV <sub>DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 500 μA	500		V
V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 8 mA	3.0		5.0 V
I <sub>GSS</sub>	V <sub>GS</sub> = ± 30 V <sub>DC</sub> , V <sub>DS</sub> = 0		± 200	nA
I <sub>DSS</sub>	V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0 V		25	μA
		T <sub>J</sub> = 125°C	2	mA
R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 I <sub>D25</sub>		65	mΩ

## Features

- International standard package
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect

## Advantages

- Easy to mount
- Space savings
- High power density

**Symbol**
**Test Conditions**
**Characteristic Values**
 $(T_J = 25^\circ C \text{ unless otherwise specified})$ 

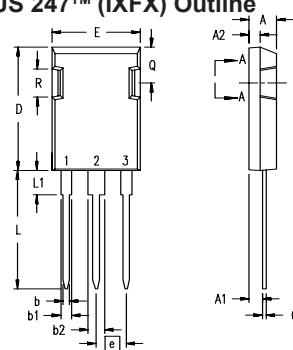
		<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>
<b><math>g_{fs}</math></b>	$V_{DS} = 20 V; I_D = 0.5 I_{D25}$ , Note 1	45	70	S
<b><math>C_{iss}</math></b>		12.7	nF	
<b><math>C_{oss}</math></b>	$V_{GS} = 0 V, V_{DS} = 25 V, f = 1 \text{ MHz}$	1280	pF	
<b><math>C_{rss}</math></b>		120	pF	
<b><math>t_{d(on)}</math></b>		25	ns	
<b><math>t_r</math></b>	$V_{GS} = 10 V, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$	27	ns	
<b><math>t_{d(off)}</math></b>	$R_G = 1 \Omega$ (External)	70	ns	
<b><math>t_f</math></b>		16	ns	
<b><math>Q_{g(on)}</math></b>		197	nC	
<b><math>Q_{gs}</math></b>	$V_{GS} = 10 V, V_{DS} = 0.5 V_{DSS}, I_D = 0.5 I_{D25}$	70	nC	
<b><math>Q_{gd}</math></b>		64	nC	
<b><math>R_{thJC}</math></b>			0.12 $^\circ\text{CW}$	
<b><math>R_{thCS}</math></b>		0.15	$^\circ\text{C/W}$	

**Source-Drain Diode**
**Characteristic Values**
 $(T_J = 25^\circ C \text{ unless otherwise specified})$ 

		<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>
<b><math>I_s</math></b>	$V_{GS} = 0 V$		80	A
<b><math>I_{SM}</math></b>	Repetitive		200	A
<b><math>V_{SD}</math></b>	$I_F = I_S, V_{GS} = 0 V$ ,		1.5	V
<b><math>t_{rr}</math></b>	$I_F = 25 A, -di/dt = 100 A/\mu s$		200	ns
<b><math>Q_{RM}</math></b>	$V_R = 100 V, V_{GS} = 0 V$	0.6	$\mu\text{C}$	
<b><math>I_{RM}</math></b>		6	A	

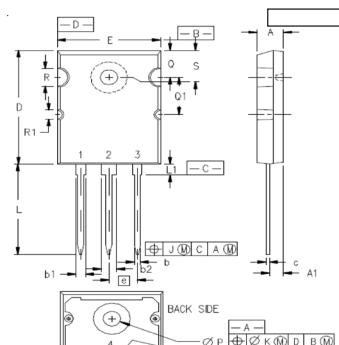
**Notes:**

1. Pulse test,  $t \leq 300 \mu\text{s}$ , duty cycle  $d \leq 2 \%$

**PLUS 247™ (IXFX) Outline**


Terminals:  
 1 - Gate  
 2 - Drain (Collector)  
 3 - Source (Emitter)  
 4 - Drain (Collector)

Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.83	5.21	.190	.205
A <sub>1</sub>	2.29	2.54	.090	.100
A <sub>2</sub>	1.91	2.16	.075	.085
b	1.14	1.40	.045	.055
b <sub>1</sub>	1.91	2.13	.075	.084
b <sub>2</sub>	2.92	3.12	.115	.123
C	0.61	0.80	.024	.031
D	20.80	21.34	.819	.840
E	15.75	16.13	.620	.635
e	5.45	BSC	.215	BSC
L	19.81	20.32	.780	.800
L1	3.81	4.32	.150	.170
Q	5.59	6.20	.220	.244
R	4.32	4.83	.170	.190

**TO-264 (IXFK) Outline**


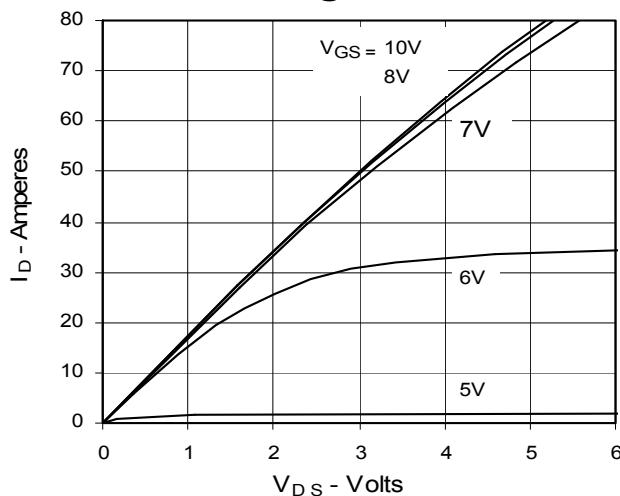
1 - GATE  
 2, 4 - DRAIN (COLLECTOR)  
 3 - SOURCE (EMITTER)

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.185	.209	4.70	5.31
A1	.102	.118	2.59	3.00
b	.037	.055	0.94	1.40
b1	.087	.102	2.21	2.59
b2	.110	.126	2.79	3.20
c	.017	.029	0.43	0.74
D	1.007	1.047	25.58	26.59
E	.760	.799	19.30	20.29
e	.215BSC		5.46 BSC	
J	.000	.010	0.00	0.25
K	.000	.010	0.00	0.25
L	.779	.842	19.79	21.39
L1	.087	.102	2.21	2.59
ØP	.122	.138	3.10	3.51
Q	.240	.256	6.10	6.50
Q1	.330	.346	8.38	8.79
ØR	.155	.187	3.94	4.75
ØR1	.085	.093	2.16	2.36
S	.243	.253	6.17	6.43

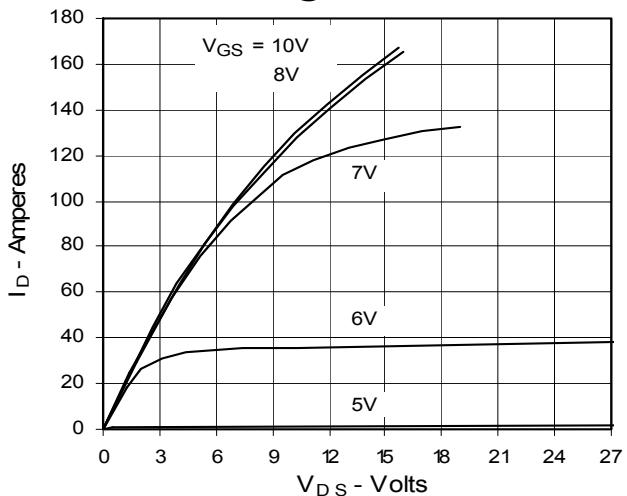
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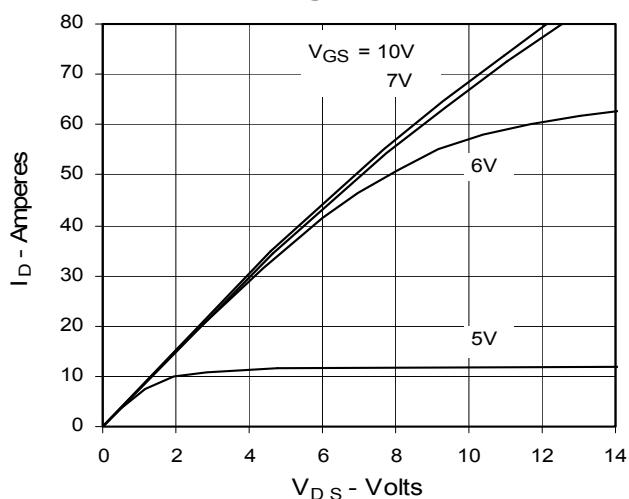
**Fig. 1. Output Characteristics  
@ 25°C**



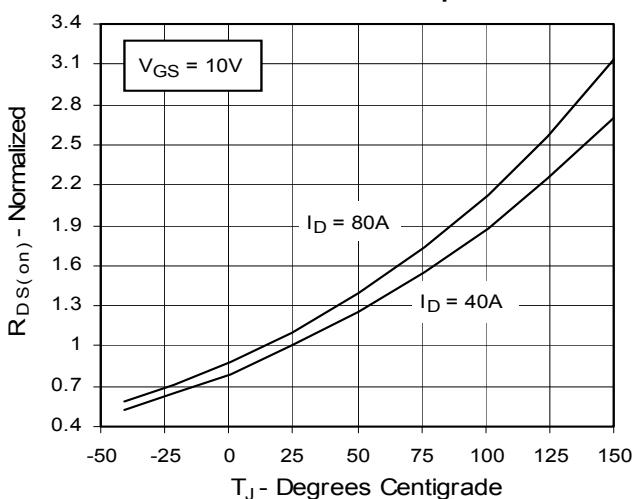
**Fig. 2. Extended Output Characteristics  
@ 25°C**



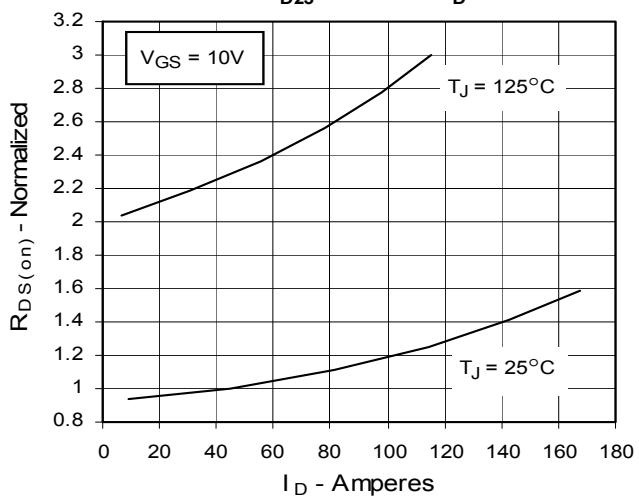
**Fig. 3. Output Characteristics  
@ 125°C**



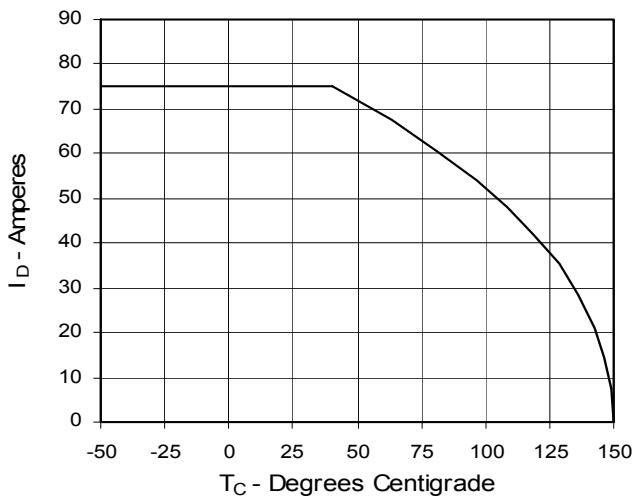
**Fig. 4.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs. Junction Temperature**

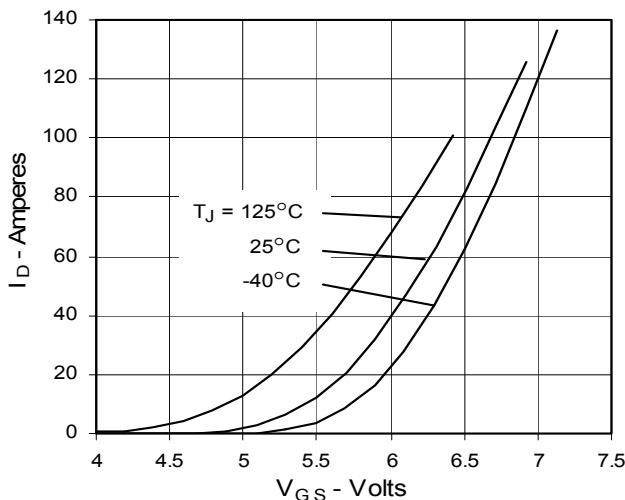
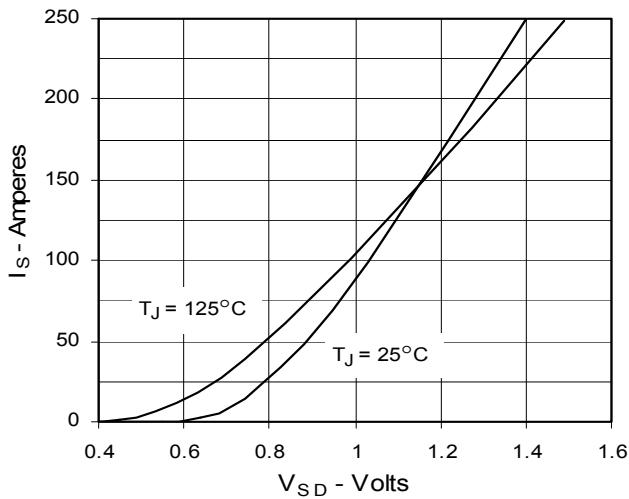
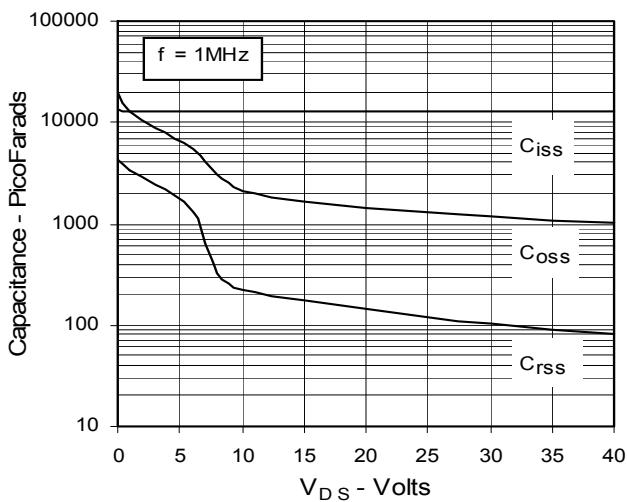
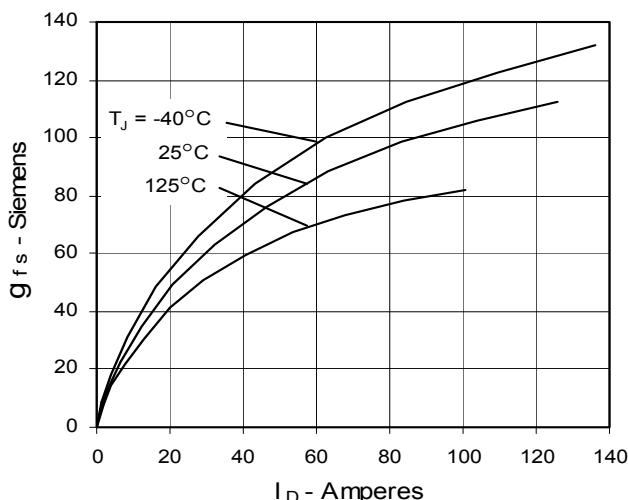
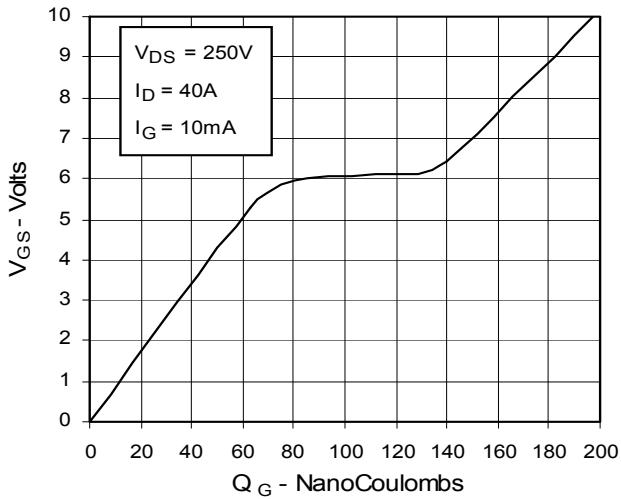
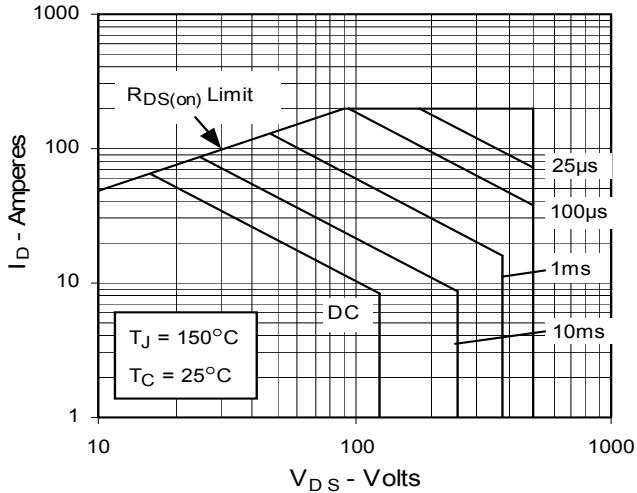


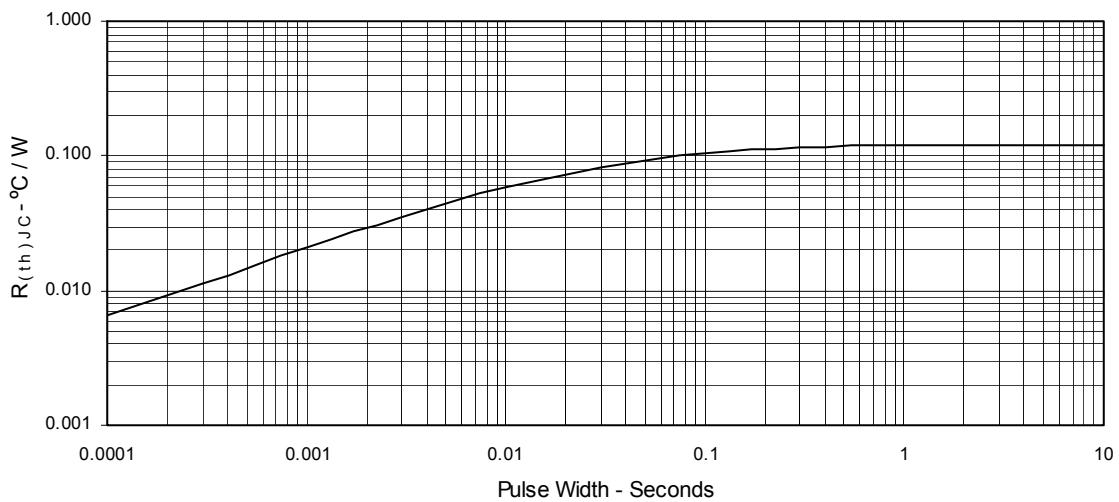
**Fig. 5.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs.  $I_D$**



**Fig. 6. Drain Current vs. Case Temperature**



**Fig. 7. Input Admittance**

**Fig. 9. Source Current vs.  
Source-To-Drain Voltage**

**Fig. 11. Capacitance**

**Fig. 8. Transconductance**

**Fig. 10. Gate Charge**

**Fig. 12. Forward-Bias  
Safe Operating Area**


**Fig. 13. Maximum Transient Thermal Resistance**



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