# CoolMOS™ [1]) Power MOSFET

Electrically isolated back surface
2500 V electrical isolation
N-Channel Enhancement Mode
Low R_{DSon}, high V_{DSS} MOSFET
Ultra low gate charge
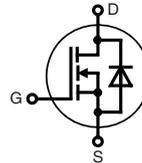
I_{D25} = 19 A
V_{DSS} = 600 V
$R_{DS(on) max}$ = 0.125 Ω

ISOPLUS220™

isolated back surface

ⅡL E72873
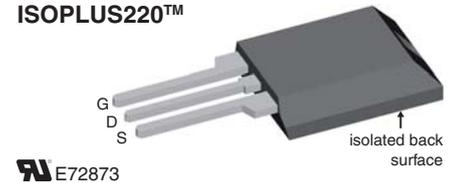
MOSFET			
Symbol	**Conditions**	**Maximum Ratings**	
V_{DSS}	T_{VJ} = 25°C	600	V
V_{GS}		± 20	V
I_{D25}	T_C = 25°C	19	A
I_{D90}	T_C = 90°C	15	A
E_{AS}	single pulse $\}$ I_D = 11 A; T_C = 25°C	708	mJ
E_{AR}	repetitive	1.2	mJ
dV/dt	MOSFET dV/dt ruggedness V_{DS} = 0...480 V	50	V/ns

Symbol	Conditions		min.	typ.	max.	
	Characteristic Values (T_{VJ} = 25°C, unless otherwise specified)					
R_{DSon}	V_{GS} = 10 V; I_D = 16 A			110	125	mΩ
$V_{GS(th)}$	V_{DS} = V_{GS}; I_D = 1.1 mA		2.5	3	3.5	V
I_{DSS}	V_{DS} = 600 V; V_{GS} = 0 V	T_{VJ} = 25°C			2	µA
		T_{VJ} = 125°C		20		µA
I_{GSS}	V_{GS} = ± 20 V; V_{DS} = 0 V				100	nA
C_{iss}	V_{GS} = 0 V; V_{DS} = 100 V			2500		pF
C_{oss}	f = 1 MHz			120		pF
Q_g				53	70	nC
Q_{gs}	V_{GS} = 0 to 10 V; V_{DS} = 400 V; I_D = 12 A			12		nC
Q_{gd}				18		nC
$t_{d(on)}$				15		ns
t_r	V_{GS} = 10 V; V_{DS} = 400 V			5		ns
$t_{d(off)}$	I_D = 16 A; R_G = 3.3 Ω			50		ns
t_f				5		ns
R_{thJC}					0.95	K/W

Features

• Silicon chip on Direct-Copper-Bond substrate
  - high power dissipation
  - isolated mounting surface
  - 2500 V electrical isolation
  - low drain to tab capacitance (< 30 pF)
• Fast CoolMOS™ [1]) power MOSFET 4th generation
  - high blocking capability
  - lowest resistance
  - avalanche rated for unclamped inductive switching (UIS)
  - low thermal resistance due to reduced chip thickness
• Enhanced total power density

Applications

• Switched mode power supplies (SMPS)
• Uninterruptible power supplies (UPS)
• Power factor correction (PFC)
• Welding
• Inductive heating
• PDP and LCD adapter

Advantages

• Easy assembly:
  no screws or isolation foils required
• Space savings
• High power density
• High reliability

[1]) CoolMOS™ is a trademark of Infineon Technologies AG.

IXKC 19N60C5

Source-Drain Diode

Symbol	Conditions	Characteristic Values			
		(T_{VJ} = 25°C, unless otherwise specified)			
		min.	typ.	max.	
I_S	V_{GS} = 0 V			16	A
V_{SD}	I_F = 16 A; V_{GS} = 0 V		0.9	1.2	V
t_{rr} Q_{RM} I_{RM}	I_F = 16 A; -di_F/dt = 100 A/µs; V_R = 400 V		430 9 42		ns µC A

Component

Symbol	Conditions	Maximum Ratings	
T_{VJ} T_{stg}	operating storage	-55...+150 -55...+150	°C °C
V_{ISOL}	RMS leads-to-tab, 50/60 Hz, f = 1 minute	2500	V~
F_C	mounting force	11-65 / 2.4-11	N/lb

Symbol	Conditions	Characteristic Values			
		min.	typ.	max.	
R_{thCH}	with heatsink compound		0.3		K/W
Weight			2.7		g

IXYS reserves the right to change limits, test conditions and dimensions.

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ISOPLUS220™ Outline
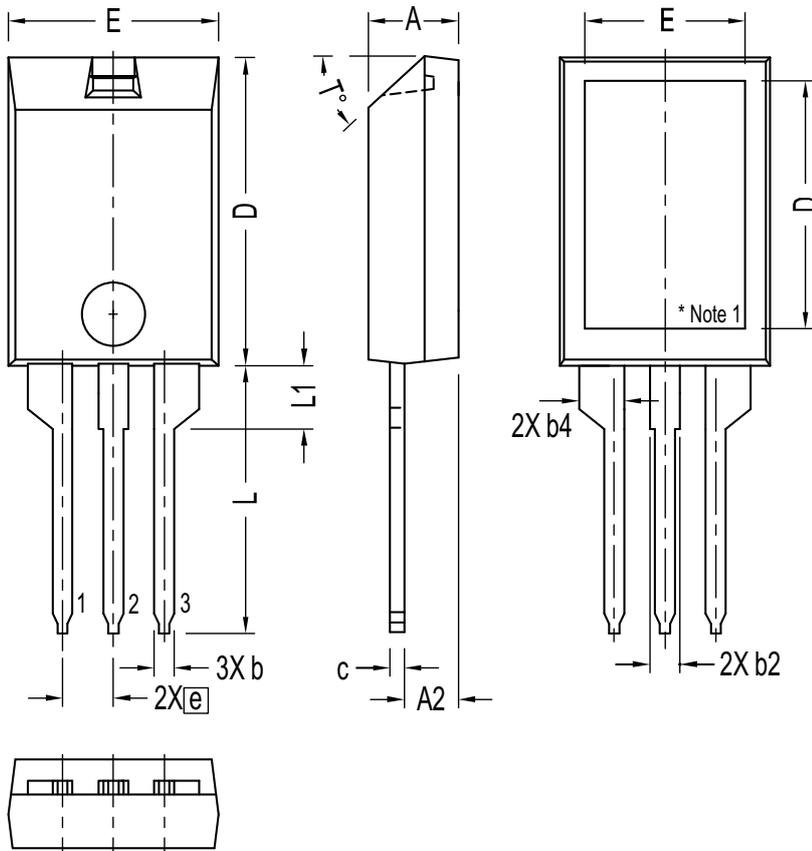
SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.157	.197	4.00	5.00
A2	.098	.118	2.50	3.00
b	.035	.051	0.90	1.30
b2	.049	.065	1.25	1.65
b4	.093	.100	2.35	2.55
c	.028	.039	0.70	1.00
D	.591	.630	15.00	16.00
D1	.472	.512	12.00	13.00
E	.394	.433	10.00	11.00
E1	.295	.335	7.50	8.50
e	.100 BASIC		2.55 BASIC	
L	.512	.571	13.00	14.50
L1	.118	.138	3.00	3.50
T°			42.5°	47.5°

NOTE:
1. Bottom heatsink is electrically isolated from Pin 1, 2, or 3.
2. This drawing will meet dimensional requirement of JEDEC SS Product Outline TO-273 except D and D1 dimension.
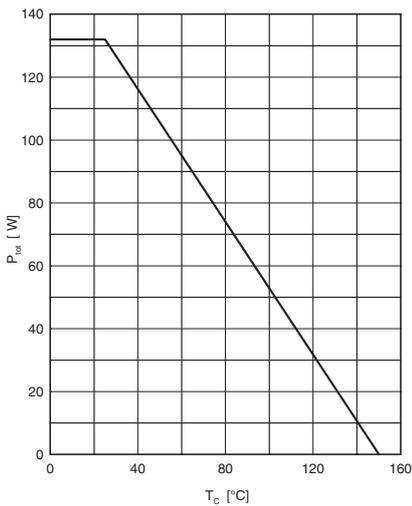
Fig. 1 Power dissipation
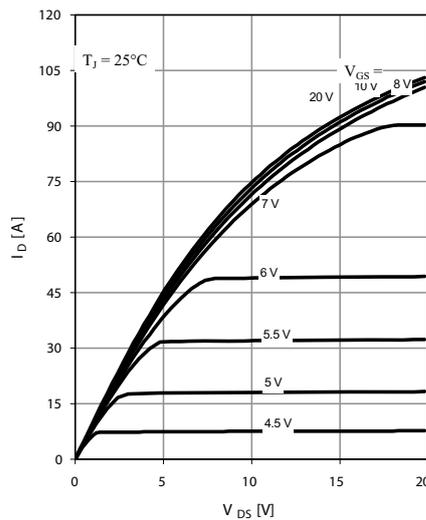
Fig. 2 Typ. output characteristics

Fig. 3 Typ. output characteristics
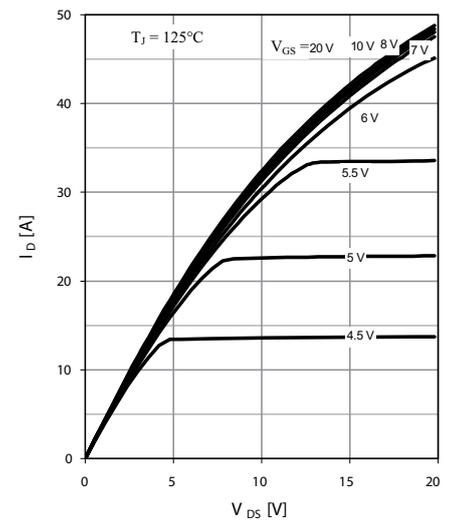
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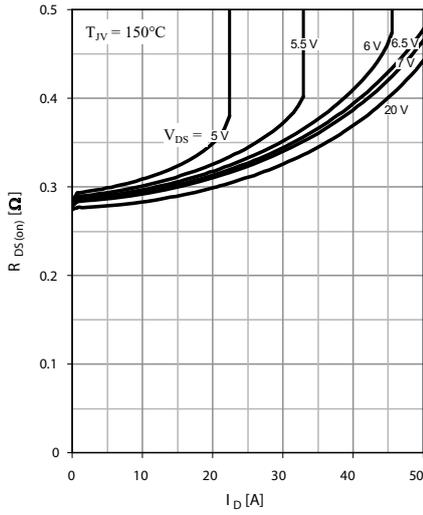
Fig. 4 Typ. drain-source on-state resistance
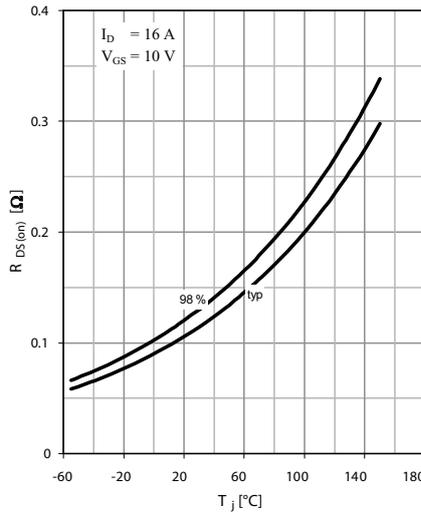
Fig. 5 Drain-source on-state resistance
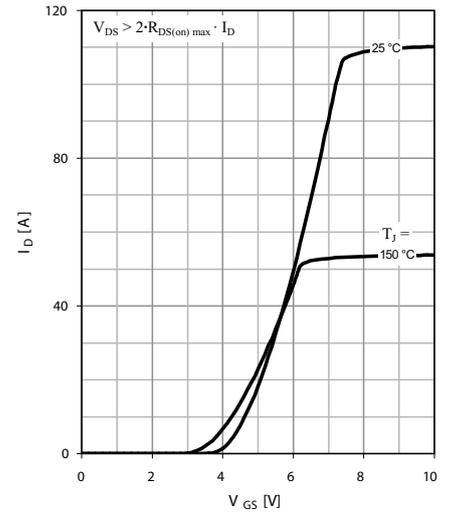

Fig. 6 Typ. transfer characteristics
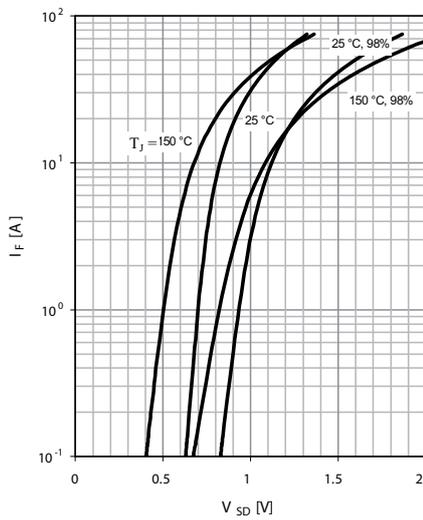

Fig. 7 Forward characteristic
of reverse diode
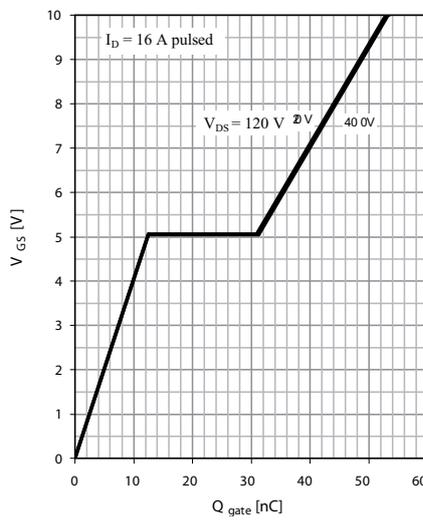
Fig. 8 Typ. gate charge
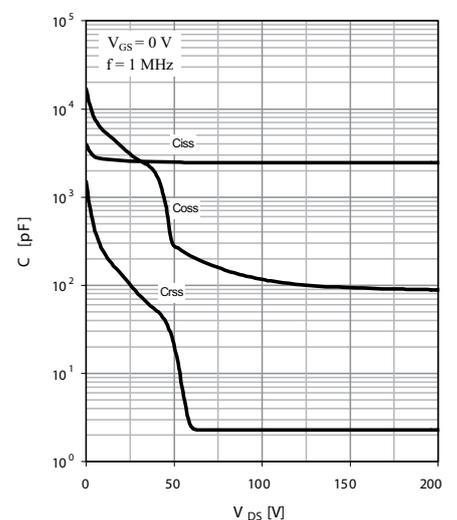
Fig. 9 Typ. capacitances
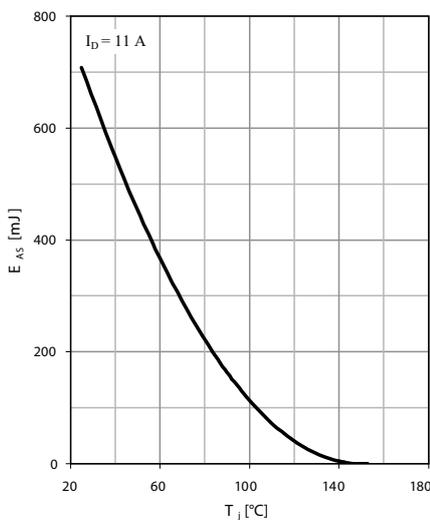
Fig. 10 Avalanche energy
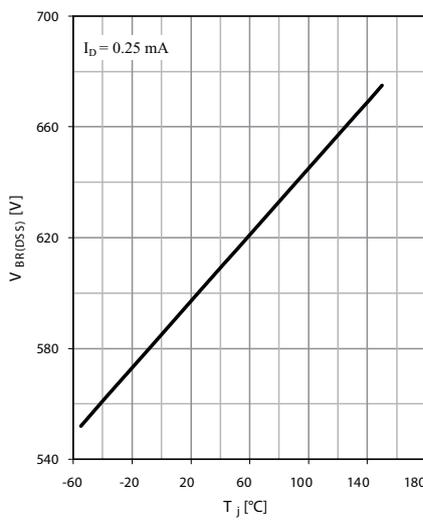
Fig. 11 Drain-source breakdown voltage

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