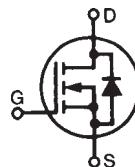


# High Voltage Power MOSFETs

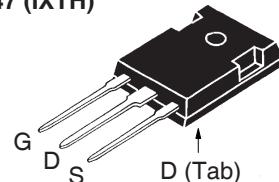
## IXTH02N250 IXTV02N250S

**V<sub>DSS</sub>** = 2500V  
**I<sub>D25</sub>** = 200mA  
**R<sub>DS(on)</sub>** ≤ 450Ω

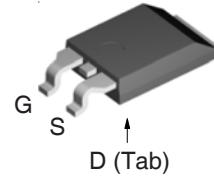
N-Channel Enhancement Mode  
Fast Intrinsic Diode



TO-247 (IXTH)



PLUS220SMD (IXTV\_S)



G = Gate      D = Drain  
 S = Source    Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
V <sub>DSS</sub>	T <sub>J</sub> = 25°C to 150°C	2500	V
V <sub>DGR</sub>	T <sub>J</sub> = 25°C to 150°C, R <sub>GS</sub> = 1MΩ	2500	V
V <sub>GSS</sub>	Continuous	±20	V
V <sub>GSM</sub>	Transient	±30	V
I <sub>D25</sub>	T <sub>C</sub> = 25°C	200	mA
I <sub>DM</sub>	T <sub>C</sub> = 25°C, Pulse Width Limited by T <sub>JM</sub>	600	mA
P <sub>D</sub>	T <sub>C</sub> = 25°C	83	W
T <sub>J</sub>		- 55 ... +150	°C
T <sub>JM</sub>		150	°C
T <sub>stg</sub>		- 55 ... +150	°C
T <sub>L</sub>	1.6mm (0.062 in.) from Case for 10s	300	°C
T <sub>SOLD</sub>	Plastic Body for 10s	260	°C
M <sub>d</sub>	Mounting Torque (TO-247)	1.13 / 10	Nm/lb.in
F <sub>c</sub>	Mounting Force (PLUS220)	11..65 / 25..14.6	N/lb.
Weight	TO-247	6	g
	PLUS220	4	g

### Features

- Fast Intrinsic Diode
- Low Package Inductance

Symbol	Test Conditions (T <sub>J</sub> = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	2500		V
V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.5		4.5 V
I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100 nA
I <sub>DSS</sub>	V <sub>DS</sub> = 0.8 • V <sub>DSS</sub> , V <sub>GS</sub> = 0V T <sub>J</sub> = 125°C			5 μA 500 μA
R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 50mA, Note 1			450 Ω

### Advantages

- Easy to Mount
- Space Savings

### Applications

- High Voltage Power Supplies
- Capacitor Discharge
- Pulse Circuits

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$g_{fs}$	$V_{DS} = 100\text{V}$ , $I_D = 0.5 \cdot I_{D25}$ , Note 1	88	145	mS
$C_{iss}$		116		pF
$C_{oss}$	$V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$	8		pF
$C_{rss}$		3		pF
$t_{d(on)}$	<b>Resistive Switching Times</b>	19		ns
$t_r$		19		ns
$t_{d(off)}$		32		ns
$t_f$		33		ns
$Q_{g(on)}$		7.4		nC
$Q_{gs}$	$V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$	0.7		nC
$Q_{gd}$		5.3		nC
$R_{thJC}$			1.5	°C/W
$R_{thCS}$		0.25		°C/W

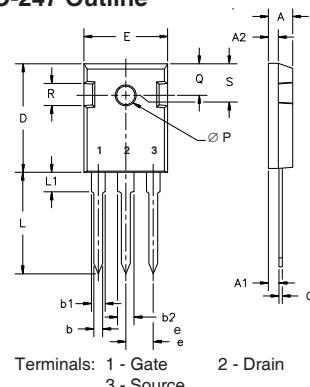
### Source-Drain Diode

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$I_s$	$V_{GS} = 0\text{V}$		200	mA
$I_{SM}$	Repetitive, Pulse Width Limited by $T_{JM}$		800	mA
$V_{SD}$	$I_F = 100\text{mA}$ , $V_{GS} = 0\text{V}$ , Note 1		2.0	V
$t_{rr}$	$I_F = 200\text{mA}$ , $-di/dt = 50\text{A}/\mu\text{s}$ , $V_R = 100\text{V}$	1.5		μs

Note 1. Pulse test,  $t \leq 300\mu\text{s}$ , duty cycle,  $d \leq 2\%$ .

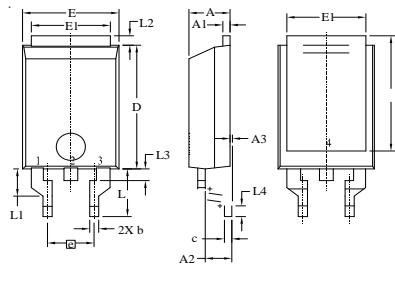
\*Additional provisions for lead to lead voltage isolation are required at  $V_{DS} > 1200\text{V}$ .

### TO-247 Outline



Dim.	Millimeter Min.	Millimeter Max.	Inches Min.	Inches Max.
A	4.7	5.3	.185	.209
A <sub>1</sub>	2.2	2.54	.087	.102
A <sub>2</sub>	2.2	2.6	.059	.098
b	1.0	1.4	.040	.055
b <sub>1</sub>	1.65	2.13	.065	.084
b <sub>2</sub>	2.87	3.12	.113	.123
C	.4	.8	.016	.031
D	20.80	21.46	.819	.845
E	15.75	16.26	.610	.640
e	5.20	5.72	.205	.225
L	19.81	20.32	.780	.800
L <sub>1</sub>		4.50		.177
ØP	3.55	3.65	.140	.144
Q	5.89	6.40	.232	.252
R	4.32	5.49	.170	.216
S	6.15	BSC	242	BSC

### PLUS220SMD Outline

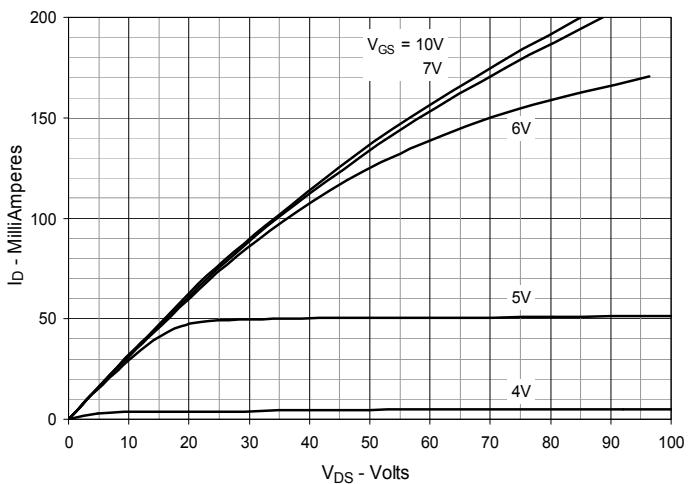
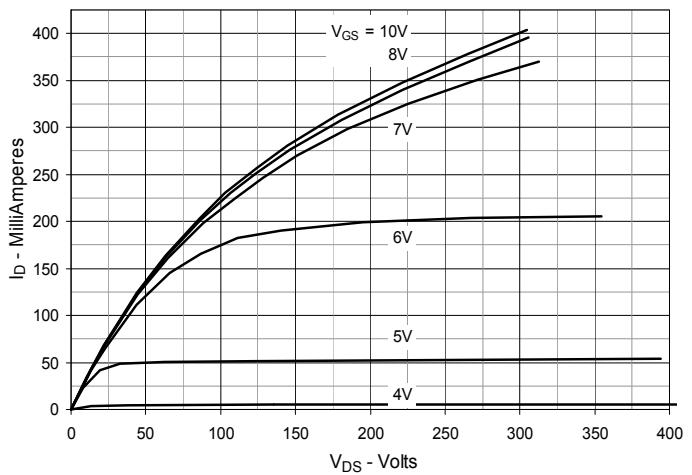
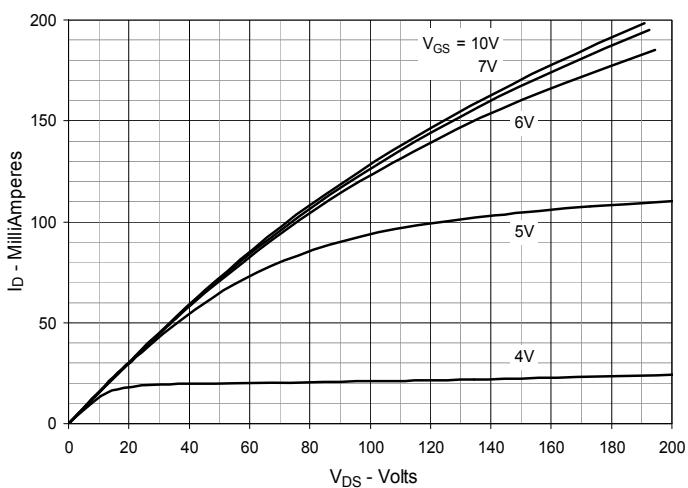
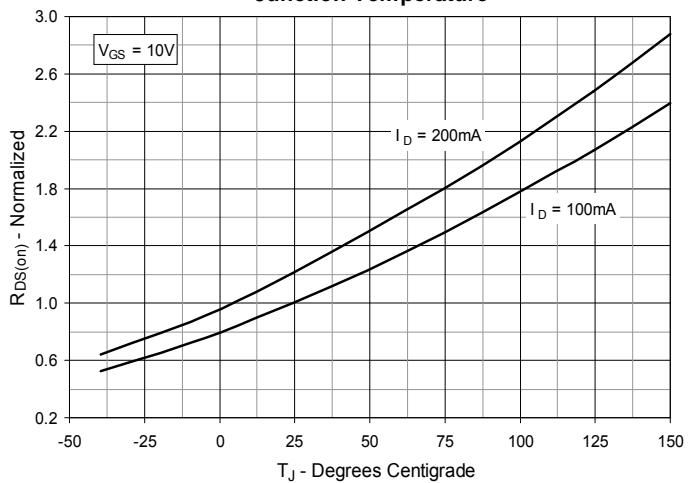
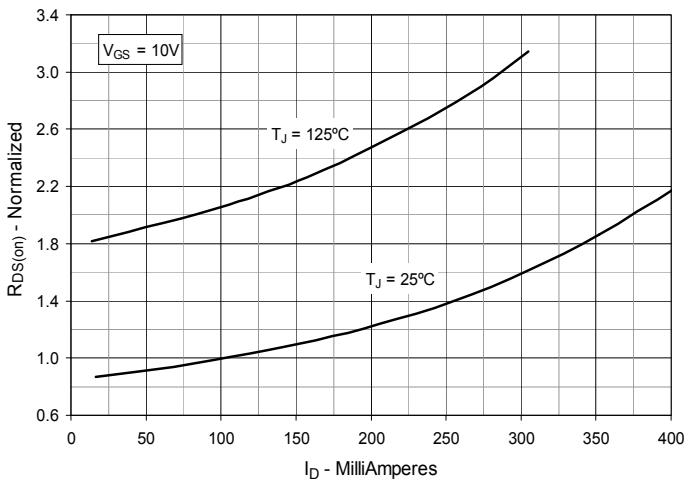
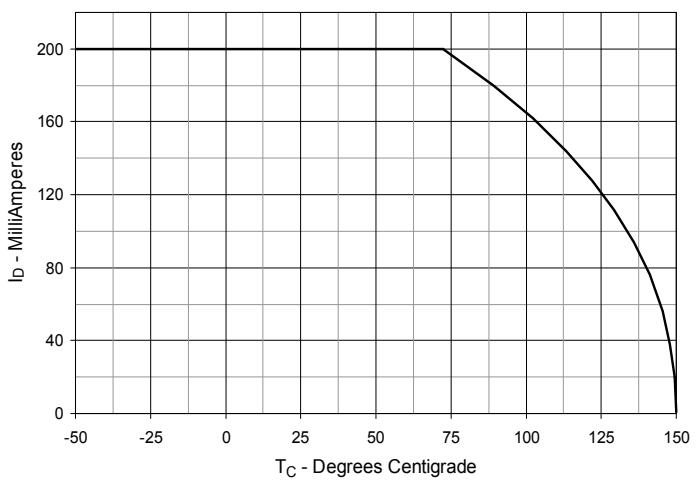


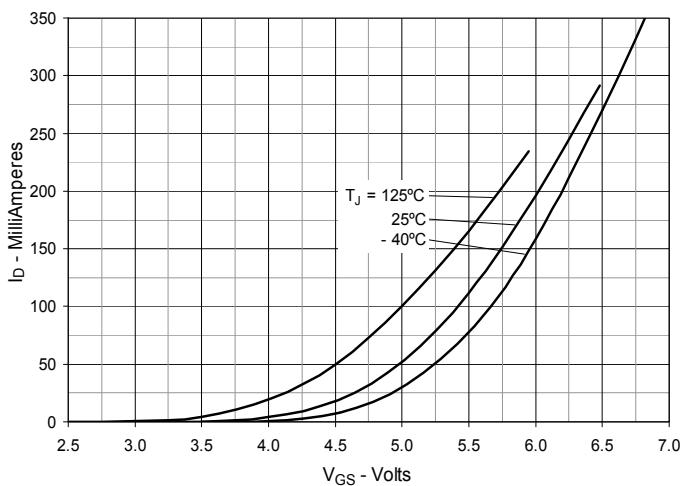
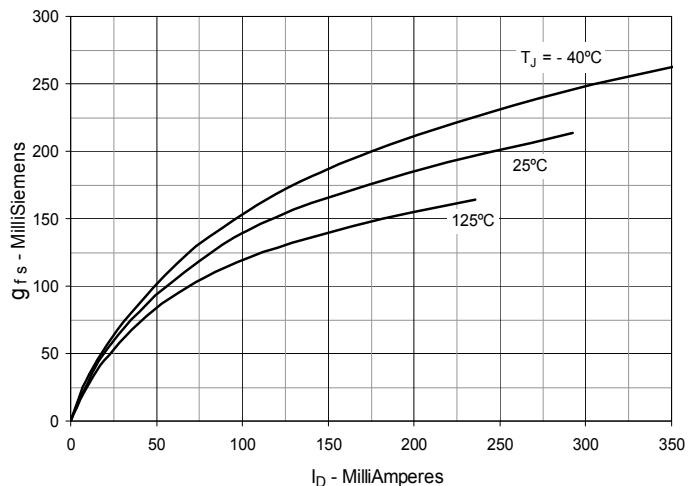
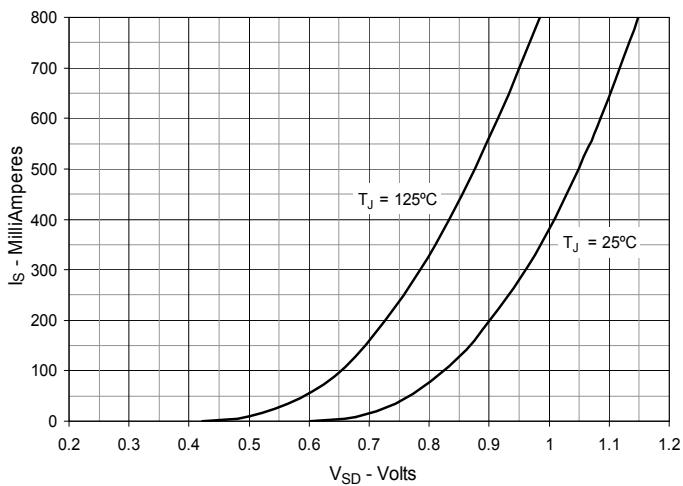
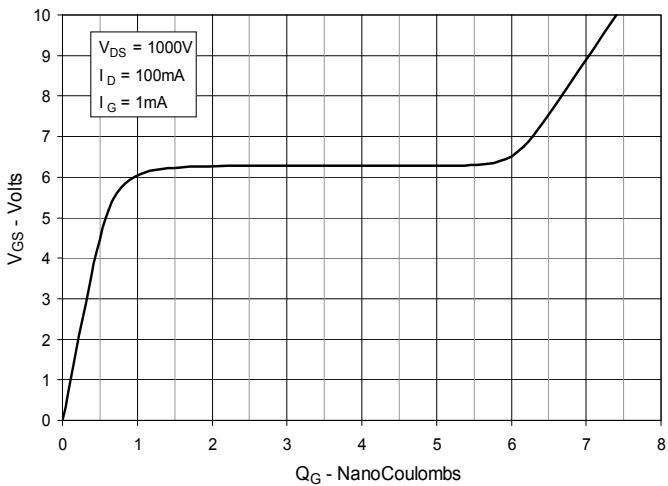
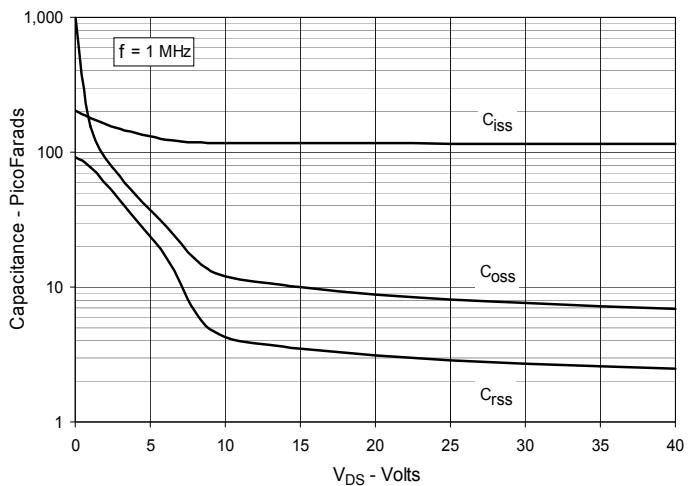
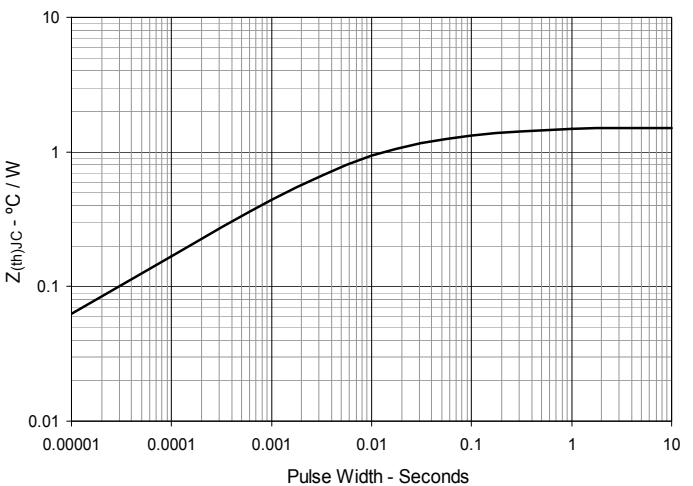
1. Gate    2. Drain  
3. Source    4. Drain

SYM	INCHES		MILLIMETER	
	MIN	MAX	MIN	MAX
A	.169	.185	4.30	4.70
A1	.028	.035	0.70	0.90
A2	.098	.118	2.50	3.00
A3	.000	.010	0.00	0.25
b	.035	.047	0.90	1.20
b1	.080	.095	2.03	2.41
b2	.054	.064	1.37	1.63
c	.028	.035	0.70	0.90
D	.551	.591	14.00	15.00
D1	.512	.539	13.00	13.70
E	.394	.433	10.00	11.00
E1	.331	.346	8.40	8.80
e	.200	BSC	5.08	BSC
L	.209	.228	5.30	5.80
L1	.118	.138	3.00	3.50
L2	.035	.051	0.90	1.30
L3	.045	.053	1.25	1.35
L4	.039	.059	1.00	1.50

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

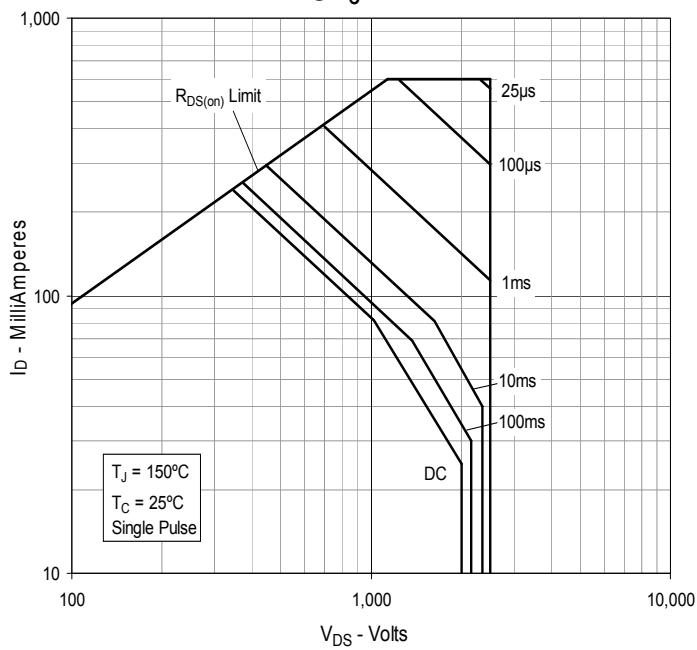
IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065 B1 6,683,344 6,727,585 7,005,734 B2 7,157,338B2 5,017,508 5,063,307 5,381,025 6,259,123 B1 6,534,343 6,710,405 B2 6,759,692 7,063,975 B2 4,881,106 5,034,796 5,187,117 5,486,715 6,306,728 B1 6,583,505 6,710,463 6,771,478 B2 7,071,537

**Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$** 

**Fig. 2. Extended Output Characteristics @  $T_J = 25^\circ\text{C}$** 

**Fig. 3. Output Characteristics @  $T_J = 125^\circ\text{C}$** 

**Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 100\text{mA}$  Value vs. Junction Temperature**

**Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 100\text{mA}$  Value vs. Drain Current**

**Fig. 6. Maximum Drain Current vs. Case Temperature**


**Fig. 7. Input Admittance**

**Fig. 8. Transconductance**

**Fig. 9. Forward Voltage Drop of Intrinsic Diode**

**Fig. 10. Gate Charge**

**Fig. 11. Capacitance**

**Fig. 12. Maximum Transient Thermal Impedance**


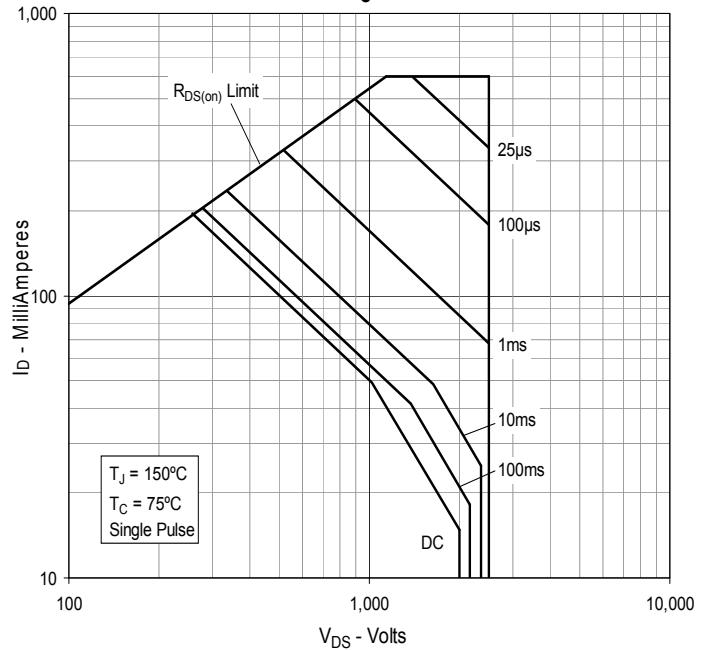
**Fig. 13. Forward-Bias Safe Operating Area**

@  $T_C = 25^\circ\text{C}$



**Fig. 14. Forward-Bias Safe Operating Area**

@  $T_C = 75^\circ\text{C}$



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