IXYS

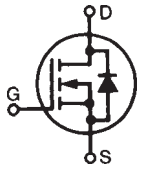
Advance Technical Information

LinearL2™ Power MOSFET w/ Extended FBSOA

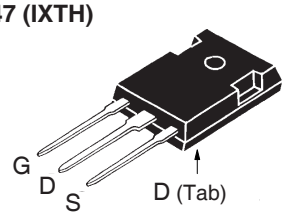
IXTH50N30L2

V_{DSS} = 300V
I_{D25} = 50A
$R_{DS(on)}$ ≤ 72mΩ

N-Channel Enhancement Mode



TO-247 (IXTH)



G = Gate D = Drain
S = Source Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C	300	V
V_{DGR}	T_J = 25°C to 150°C, R_{GS} = 1MΩ	300	V
V_{GSS}	Continuous	± 20	V
V_{GSM}	Transient	± 30	V
I_{D25}	T_C = 25°C	50	A
I_{DM}	T_C = 25°C, Pulse Width Limited by T_{JM}	120	A
I_A	T_C = 25°C	25	A
E_{AS}	T_C = 25°C	2.5	J
P_D	T_C = 25°C	540	W
T_J		-55 ... +150	°C
T_{JM}		150	°C
T_{stg}		-55 ... +150	°C
T_L	Maximum Lead Temperature for Soldering	300	°C
T_{SOLD}	Plastic Body for 10s	260	°C
M_d	Mounting Torque	1.13 / 10	Nm/lb.in
Weight	TO-247	6	g

Symbol	Test Conditions (T_J = 25°C Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0V, I_D = 250μA	300			V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 250μA	2.5		4.5	V
I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
I_{DSS}	$V_{DS} = V_{DSS}$, V_{GS} = 0V			10	μA
	T_J = 125°C			350	μA
$R_{DS(on)}$	V_{GS} = 10V, $I_D = 0.5 \bullet I_{D25}$, Note 1			72	mΩ

Features

- Designed for Linear Operation
- International Standard Package
- Avalanche Rated
- Guaranteed FBSOA at 75°C

Advantages

- Easy to Mount
- Space Savings
- High Power Density

Applications

- Solid State Circuit Breakers
- Soft Start Controls
- Linear Amplifiers
- Programmable Loads
- Current Regulators

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Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max.	
g_{fs}	V_{DS} = 10V, I_D = 0.5 • I_{D25}, Note 1	13	22	31	S
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		9300		pF
C_{oss}			900		pF
C_{rss}			260		pF
$t_{d(on)}$	**Resistive Switching Times**		36		ns
t_r	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 0.5 • I_{D25}		100		ns
$t_{d(off)}$	R_G = 2Ω (External)		140		ns
t_f			50		ns
$Q_{g(on)}$	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 0.5 • I_{D25}		330		nC
Q_{gs}			52		nC
Q_{gd}			180		nC
R_{thJC}				0.23	°C/W
R_{thCS}			0.21		°C/W

Safe Operating Area Specification

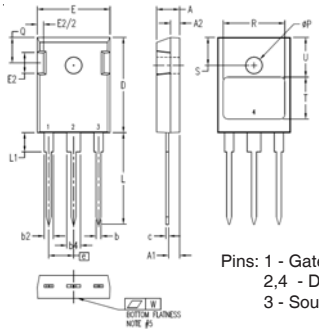
Symbol	Test Conditions	Characteristic Values Min.	Typ.	Max.	
SOA	V_{DS} = 300V, I_D = 1.1A, T_C = 75°C, Tp = 2s	330			W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C, Unless Otherwise Specified)	Characteristic Values Min.	Typ.	Max	
I_S	V_{GS} = 0V			50	A
I_{SM}	Repetitive, pulse Width Limited by T_{JM}			200	A
V_{SD}	I_F = I_S, V_{GS} = 0V, Note 1			1.4	V
t_{rr}	I_F = 25A, -di/dt = 100A/μs		430		ns
Q_{RM}	V_R = 100V		11		μC
I_{RM}			52		A

Note 1: Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.

TO-247 Outline



Pins: 1 - Gate
2,4 - Drain
3 - Source

SYM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	.190	.205	4.83	5.21
A1	.087	.100	2.21	2.54
A2	.075	.085	1.91	2.16
b	.045	.055	1.14	1.40
b2	.075	.085	1.91	2.16
b4	.115	.126	2.92	3.20
c	.023	.033	0.58	0.84
D	.820	.840	20.83	21.34
E	.620	.635	15.75	16.13
E2	.175	.195	4.44	4.95
e	.215 BSC		5.45 BSC	
L	.780	.810	19.81	20.57
L1	.160	.177	4.06	4.50
Q	.220	.240	5.59	6.10
R	.520	.540	13.21	13.72
S	.242 BSC		6.15 BSC	
T	.355	.375	9.02	9.53
U	.345	.370	8.76	9.40
øP	.140	.144	3.55	3.66
W	.000	.004	0.00	0.10

ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents: 4,835,592 4,860,072 4,881,106 4,931,844 5,017,508 5,034,796 5,049,961 5,063,307 5,187,117 5,237,481 5,381,025 5,486,715 6,162,665 6,259,123 B1 6,306,728 B1 6,404,065 B1 6,534,343 6,583,505 6,683,344 6,710,405 B2 6,710,463 6,727,585 6,759,692 6,771,478 B2 7,005,734 B2 7,063,975 B2 7,071,537 7,157,338B2

Fig. 1. Output Characteristics @ T_J = 25°C

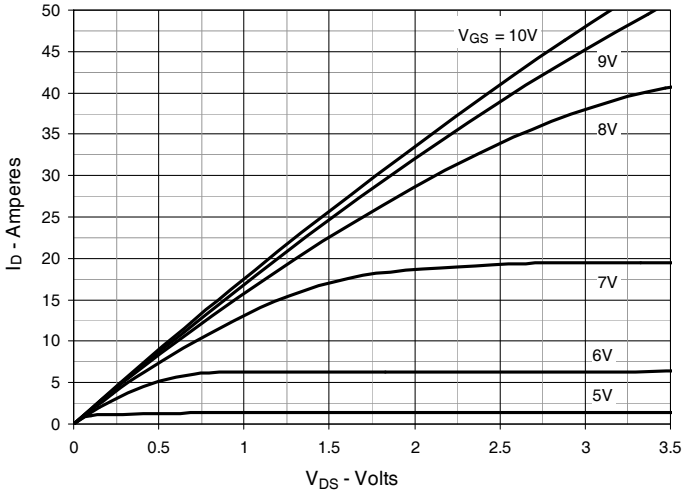


Fig. 2. Extended Output Characteristics @ T_J = 25°C

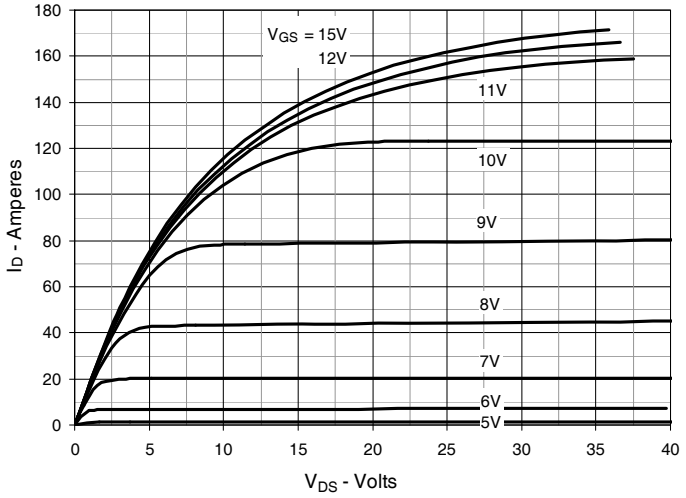


Fig. 3. Output Characteristics @ T_J = 125°C

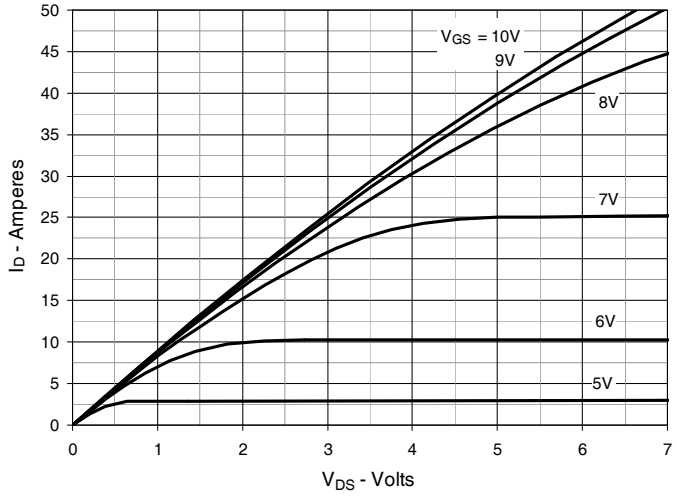


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 25A Value vs. Junction Temperature

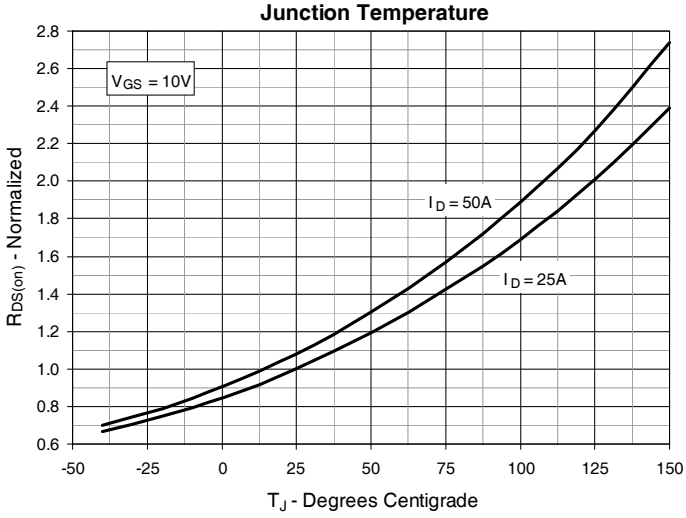


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 25A Value vs. Drain Current

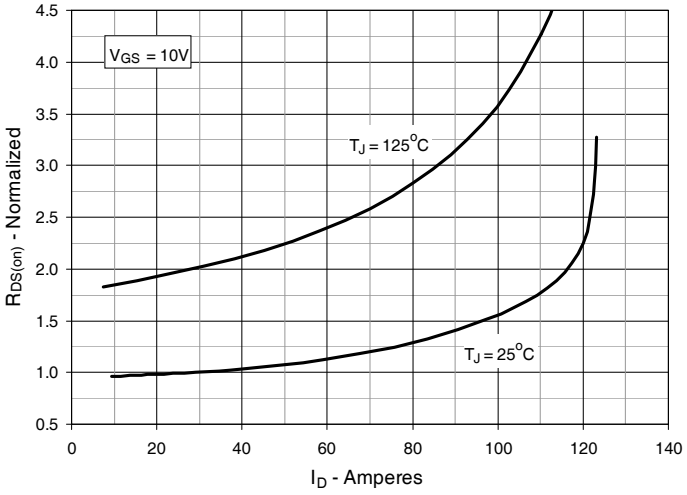


Fig. 6. Normalized Breakdown & Threshold Voltages vs. Junction Temperature

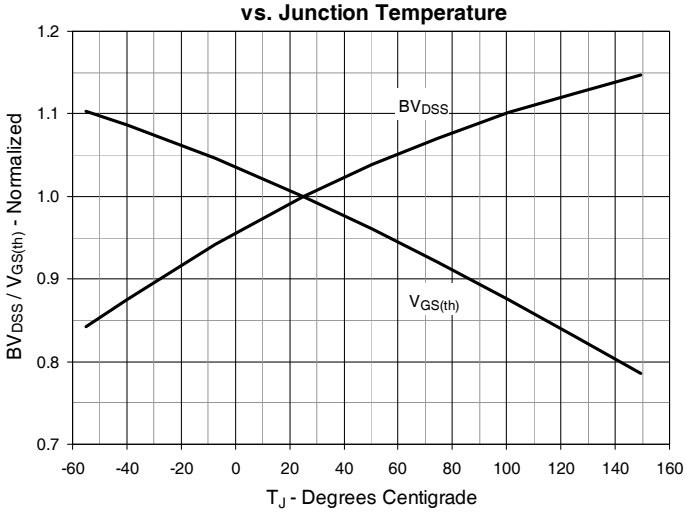


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Fig. 7. Maximum Drain Current vs. Case Temperature

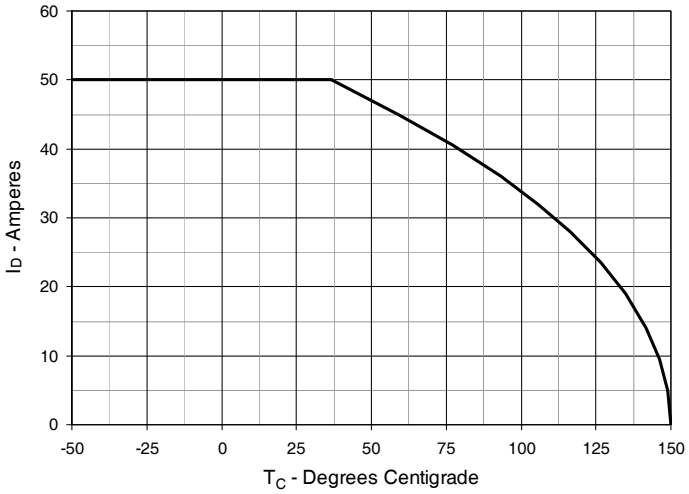

Fig. 8. Input Admittance

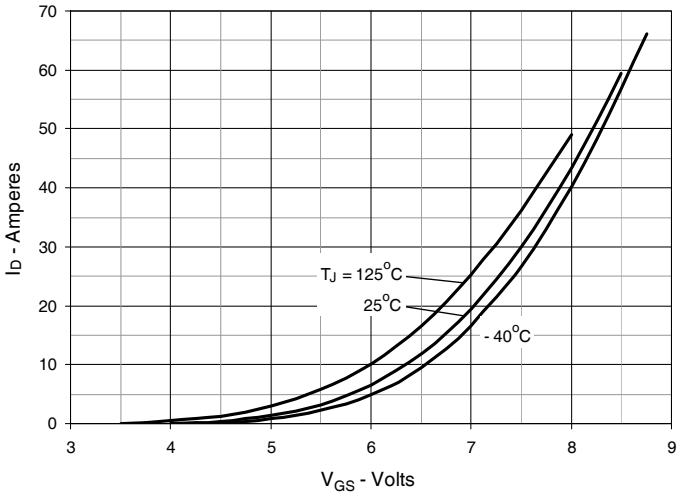

Fig. 9. Transconductance

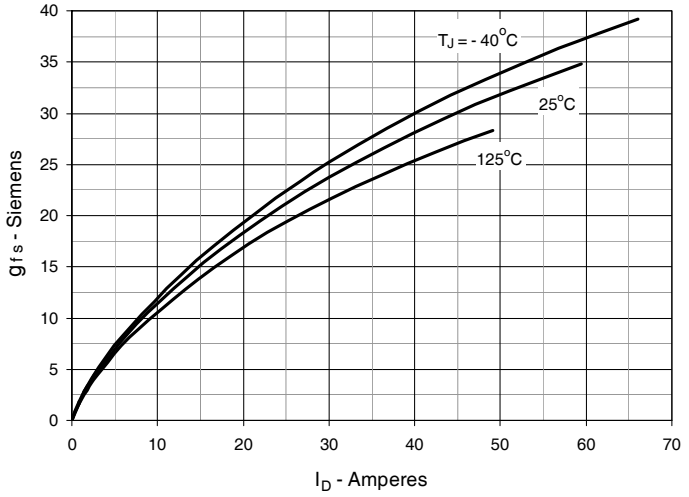

Fig. 10. Forward Voltage Drop of Intrinsic Diode

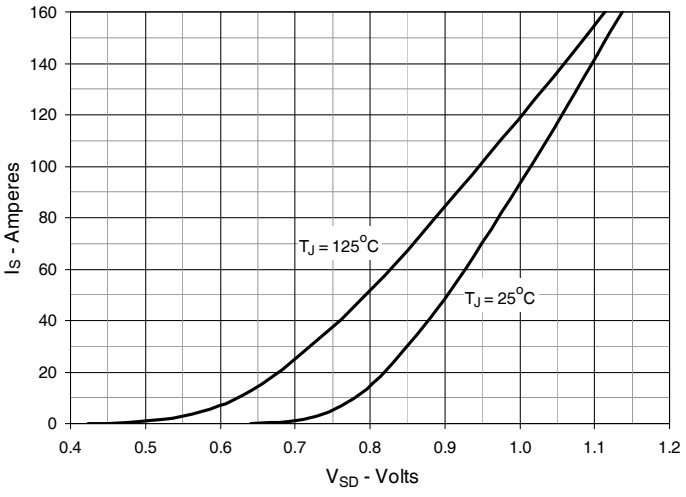

Fig. 11. Gate Charge

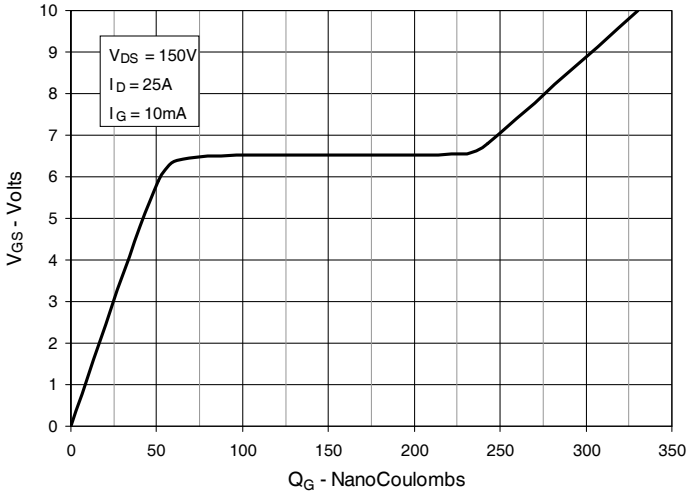

Fig. 12. Capacitance

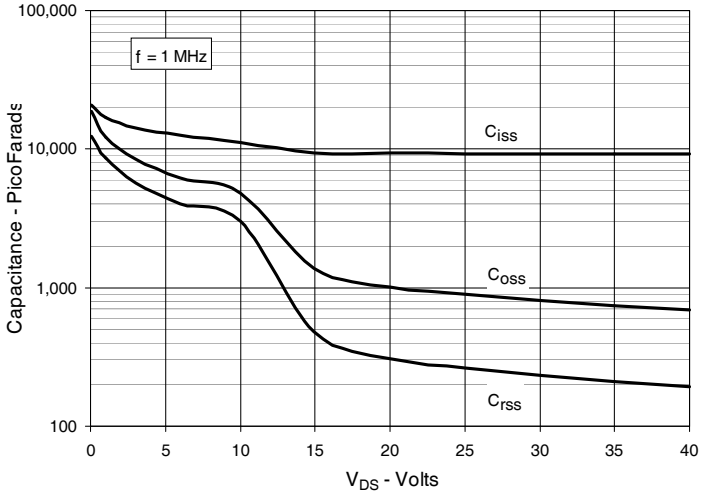

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Fig. 13. Forward-Bias Safe Operating Area @ T_C = 25°C

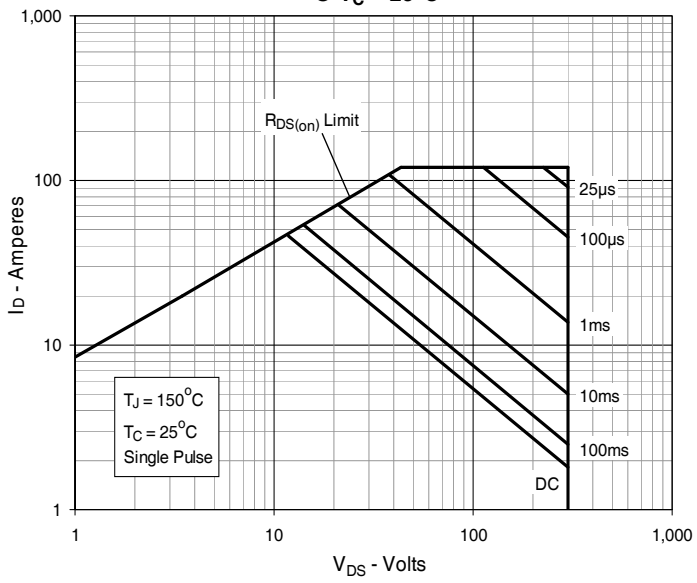


Fig. 14. Forward-Bias Safe Operating Area @ T_C = 75°C

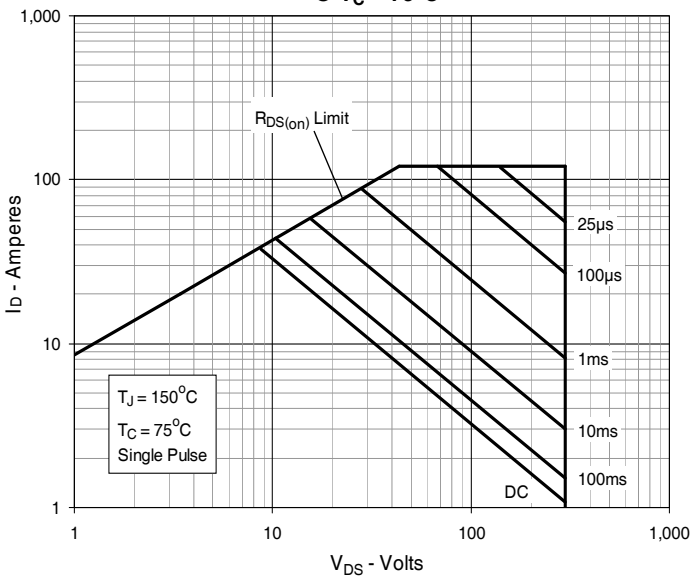
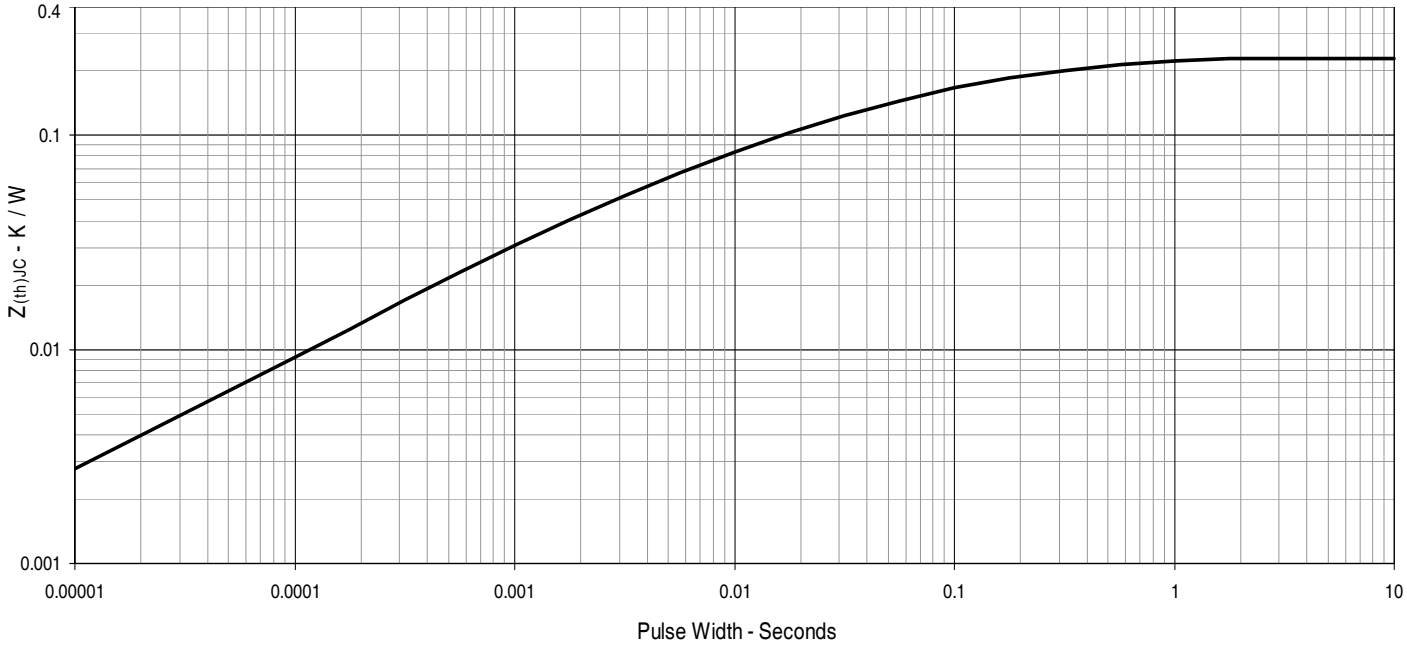


Fig. 15. Maximum Transient Thermal Impedance



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