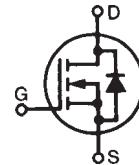


Polar™
Power MOSFET

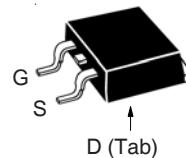
IXTA10N60P
IXTP10N60P

V_{DSS} = 600V
I_{D25} = 10A
R_{DS(on)} ≤ 740mΩ

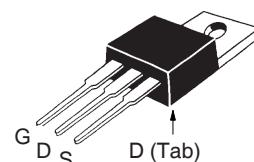


N-Channel Enhancement Mode
Avalanche Rated
Fast Intrinsic Rectifier

TO-263 AA (IXTA)



TO-220AB (IXTP)



G = Gate D = Drain
 S = Source Tab = Drain

Symbol	Test Conditions	Maximum Ratings	
V _{DSS}	T _J = 25°C to 150°C	600	V
V _{DGR}	T _J = 25°C to 150°C, R _{GS} = 1MΩ	600	V
V _{GSS}	Continuous	±30	V
V _{GSM}	Transient	±40	V
I _{D25}	T _C = 25°C	10	A
I _{DM}	T _C = 25°C, Pulse Width Limited by T _{JM}	25	A
I _A	T _C = 25°C	10	A
E _{AS}	T _C = 25°C	500	mJ
dv/dt	I _S ≤ I _{DM} , V _{DD} ≤ V _{DSS} , T _J ≤ 150°C	10	V/ns
P _D	T _C = 25°C	200	W
T _J		-55 ... +150	°C
T _{JM}		150	°C
T _{stg}		-55 ... +150	°C
T _L	1.6mm (0.062in.) from Case for 10s	300	°C
T _{sold}	Plastic Body for 10 Seconds	260	°C
M _d	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in.
Weight	TO-263	2.5	g
	TO-220	3.0	g

Symbol	Test Conditions (T _J = 25°C Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV _{DSS}	V _{GS} = 0V, I _D = 250μA	600		V
V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	3.0		5.5 V
I _{GSS}	V _{GS} = ± 30V, V _{DS} = 0V			±100 nA
I _{DSS}	V _{DS} = V _{DSS} , V _{GS} = 0V			5 μA
	T _J = 125°C			50 μA
R _{DS(on)}	V _{GS} = 10V, I _D = 0.5 • I _{D25} , Notes 1, 2			740 mΩ

Features

- International Standard Packages
- Dynamic dv/dt Rating
- Avalanche Rated
- Fast Intrinsic Rectifier
- Low Q_G
- Low R_{DS(on)}
- Low Drain-to-Tab Capacitance
- Low Package Inductance

Advantages

- Easy to Mount
- Space Savings

Applications

- DC-DC Converters
- Battery Chargers
- Switch-Mode and Resonant-Mode Power Supplies
- Uninterrupted Power Supplies
- AC Motor Drives
- High Speed Power Switching Applications

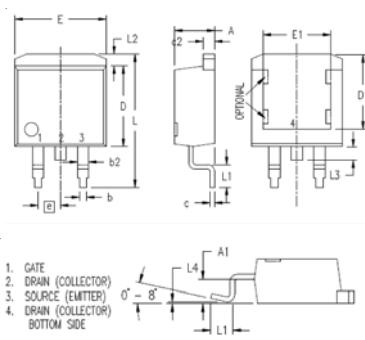
Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
g_{fs}	$V_{DS} = 10\text{V}$, $I_D = 0.5 \cdot I_{D25}$, Note 1	6	11	S
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0\text{V}$, $V_{DS} = 25\text{V}$, $f = 1\text{MHz}$	1720		pF
		160		pF
		14		pF
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	Resistive Switching Time $V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 0.5 \cdot I_{D25}$ $R_G = 10\Omega$ (External)	23		ns
		27		ns
		65		ns
		21		ns
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10\text{V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = 0.5 \cdot I_{D25}$	32		nC
		12		nC
		10		nC
R_{thJC}			0.62	$^\circ\text{C}/\text{W}$
R_{thCH}	TO-220	0.50		$^\circ\text{C}/\text{W}$

Source-Drain Diode

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
I_s	$V_{GS} = 0\text{V}$		10	A
I_{SM}	Repetitive, Pulse Width Limited by T_{JM}		30	A
V_{SD}	$I_F = I_S$, $V_{GS} = 0\text{V}$, Note 1		1.5	V
t_{rr}	$I_F = 10\text{A}$, $V_{GS} = 0\text{V}$ -di/dt = 100A/ μs , $V_R = 100\text{V}$	500		ns

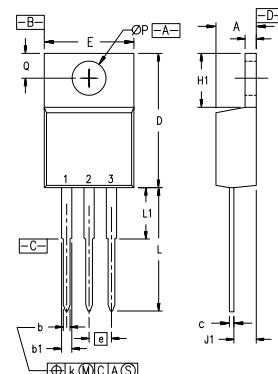
Notes: 1. Pulse test, $t \leq 300\mu\text{s}$, duty cycle, $d \leq 2\%$.
 2. On through-hole packages, $R_{DS(on)}$ Kelvin test contact location must be 5mm or less from the package body.

TO-263 Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.160	.190	4.06	4.83
A1	.080	.110	2.03	2.79
b	.020	.039	0.51	0.99
b2	.045	.055	1.14	1.40
c	.016	.029	0.40	0.74
c2	.045	.055	1.14	1.40
D	.340	.380	8.64	9.65
D1	.315	.350	8.00	8.89
E	.380	.410	9.65	10.41
E1	.245	.320	6.22	8.13
e	.100	BSC	2.54	BSC
L	.575	.625	14.61	15.88
L1	.090	.110	2.29	2.79
L2	.040	.055	1.02	1.40
L3	.050	.070	1.27	1.78
L4	0	.005	0	0.13

TO-220 Outline



Pins: 1 - Gate 2 - Drain
 3 - Source

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100	BSC	2.54	BSC
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
ØP	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

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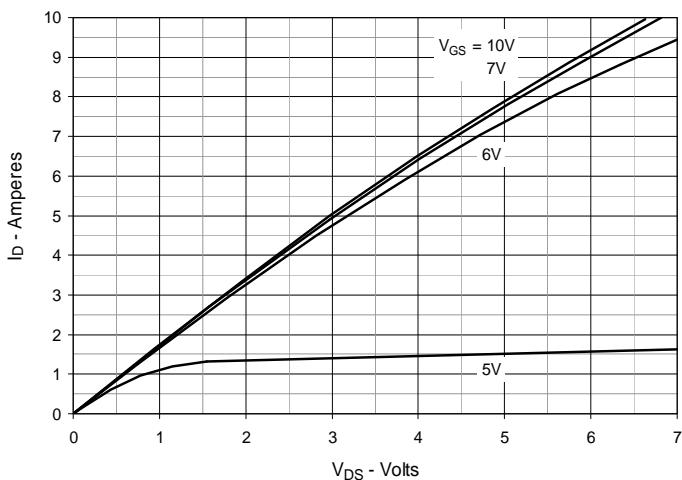
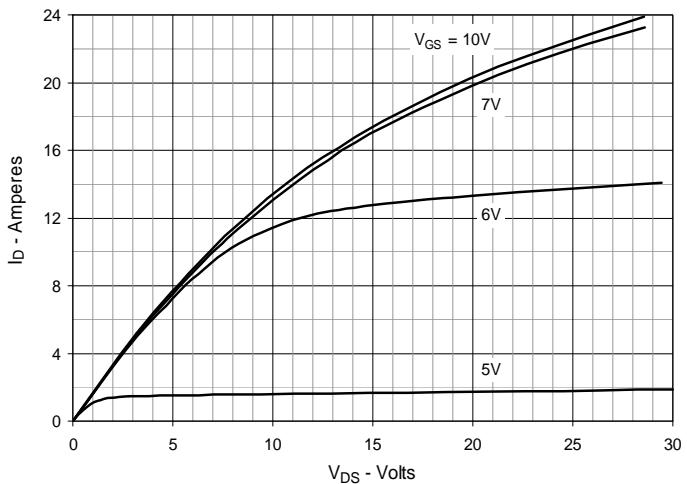
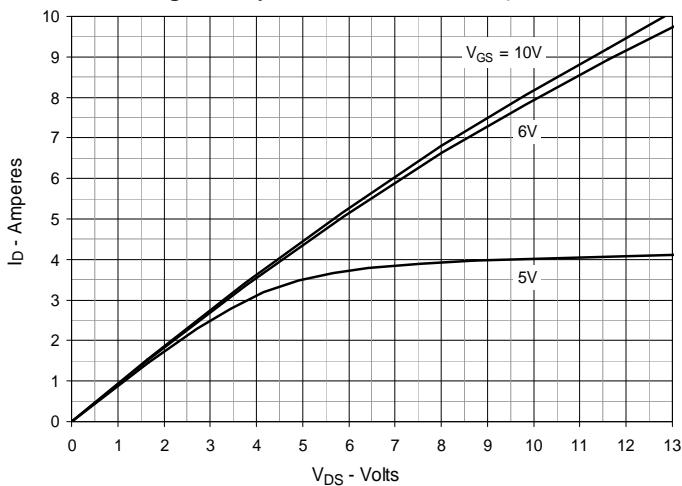
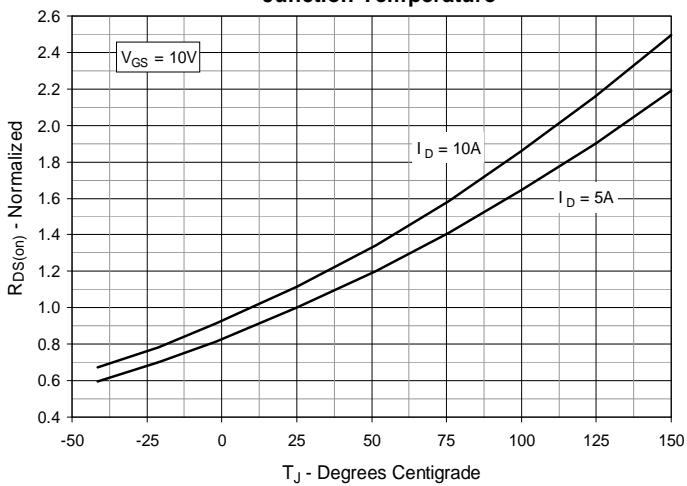
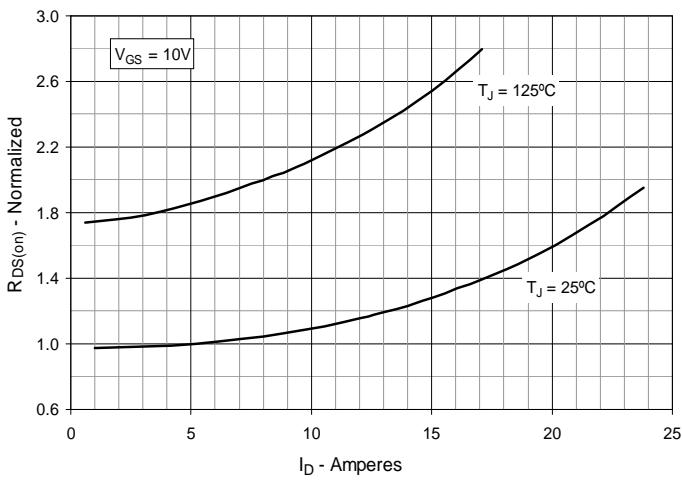
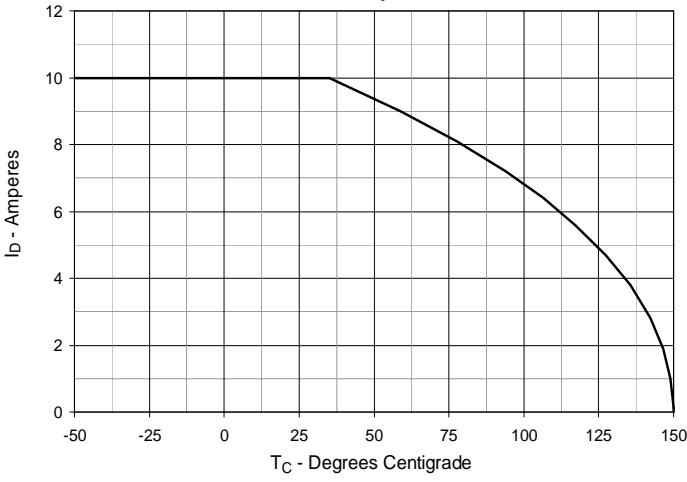
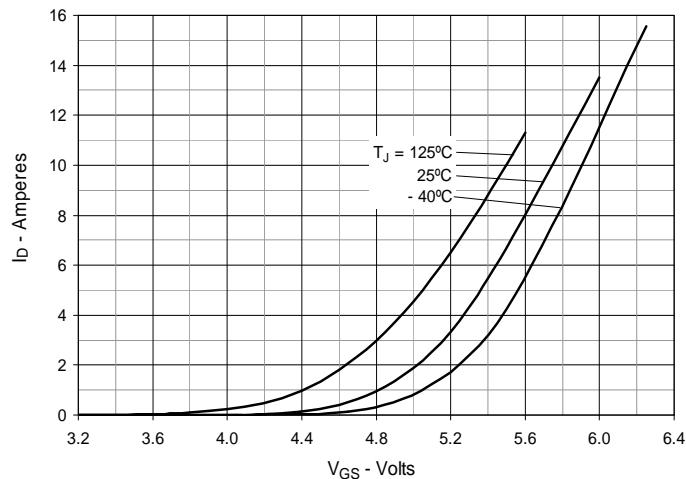
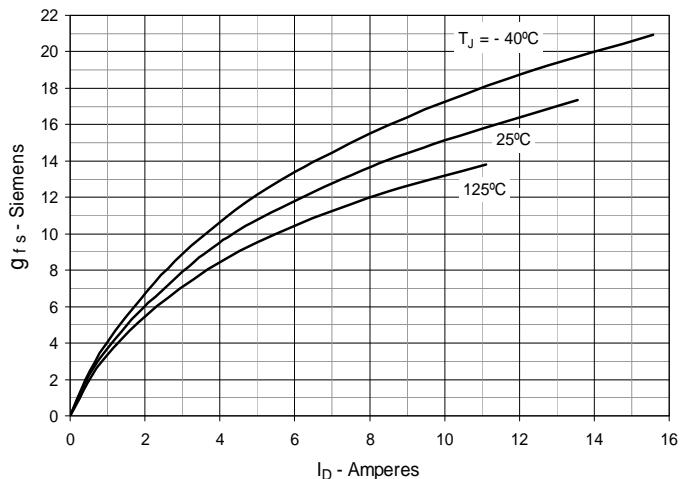
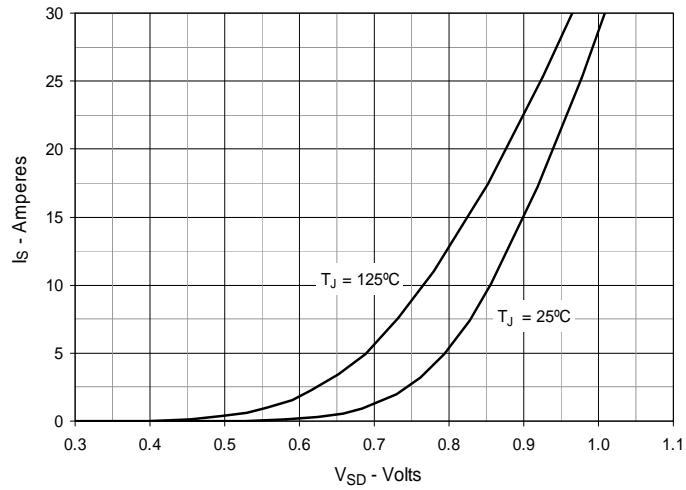
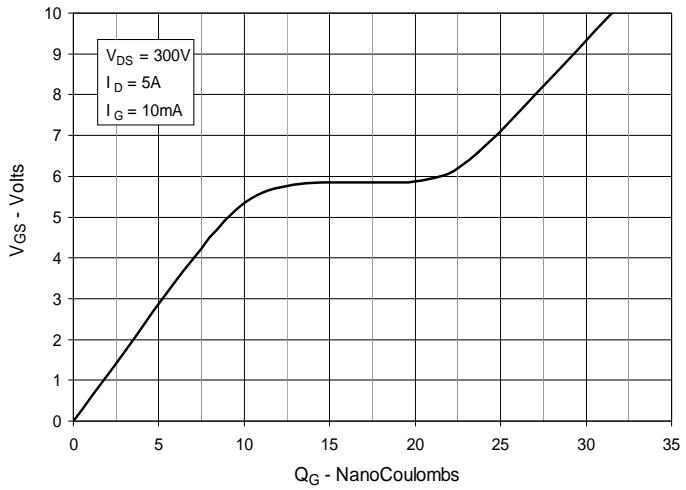
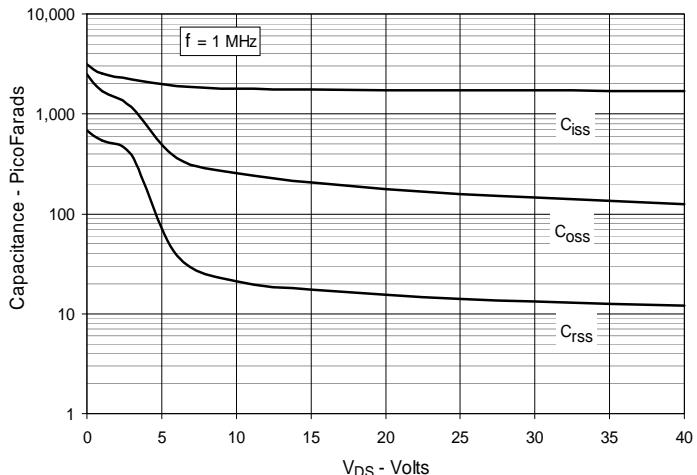
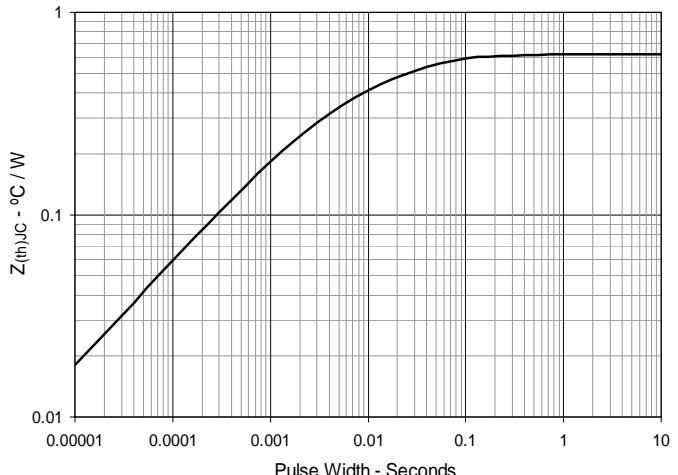
Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 3. Output Characteristics @ $T_J = 125^\circ\text{C}$

Fig. 4. $R_{DS(on)}$ Normalized to $I_D = 5\text{A}$ Value vs. Junction Temperature

Fig. 5. $R_{DS(on)}$ Normalized to $I_D = 5\text{A}$ Value vs. Drain Current

Fig. 6. Maximum Drain Current vs. Case Temperature


Fig. 7. Input Admittance

Fig. 8. Transconductance

Fig. 9. Forward Voltage Drop of Intrinsic Diode

Fig. 10. Gate Charge

Fig. 11. Capacitance

Fig. 12. Maximum Transient Thermal Impedance


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