## PolarP2 ${ }^{\text {TM }}$

## Power MOSFET

N-Channel Enhancement Mode
Avalanche Rated
Fast Intrinsic Diode

$\mathrm{V}_{\text {Dss }}=500 \mathrm{~V}$
$\mathrm{I}_{\mathrm{D25}}=42 \mathrm{~A}$
$\mathrm{R}_{\mathrm{DS}(\mathrm{on})} \leq 145 \mathrm{~m} \Omega$
$\mathrm{t}_{\mathrm{rr(ty)})}=400 \mathrm{~ns}$

## TO-3P



$$
\begin{array}{ll}
G=\text { Gate } & D=\text { Drain } \\
S=\text { Source } & T a b=\text { Drain }
\end{array}
$$

## Features

- Avalanche Rated
- Fast Intrinsic Diode
- Dynamic dv/dt Rated
- Low Package Inductance


## Advantages

- High Power Density
- Easy to Mount
- Space Savings


## Applications

- Switch-Mode and Resonant-Mode Power Supplies
- DC-DC Converters
- Laser Drivers
- AC and DC Motor Drives
- Robotics and Servo Controls

| Symbol Test Conditions <br> ( $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ Unless Otherwise Specified) |  | Characteristic Values |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Typ. | Max. |
| $\mathrm{g}_{\text {fs }}$ | $\mathrm{V}_{\mathrm{DS}}=20 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=0.5 \cdot \mathrm{I}_{\mathrm{D} 25}$, Note 1 | 23 | 36 | S |
| $\begin{aligned} & \mathrm{C}_{\text {iss }} \\ & \mathrm{C}_{\text {oss }} \\ & \mathrm{C}_{\mathrm{rss}} \end{aligned}$ | $\} V_{G S}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=25 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | $\begin{array}{r} 5400 \\ 545 \\ 44 \end{array}$ | pF pF pF |
| $\begin{aligned} & t_{d(o n)} \\ & t_{r} \\ & t_{d(\text { off })} \\ & t_{f} \\ & \hline \end{aligned}$ | Resistive Switching Times $\left\{\begin{array}{l} \mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0.5 \cdot \mathrm{~V}_{\mathrm{DSS}}, \mathrm{I}_{\mathrm{D}}=0.5 \cdot \mathrm{I}_{\mathrm{D} 25} \\ \mathrm{R}_{\mathrm{G}}=3 \Omega \text { (External) } \end{array}\right.$ |  | 23 12 42 9 | ns ns ns ns |
| $\begin{aligned} & \mathbf{Q}_{\mathrm{g}(\mathrm{on})} \\ & \mathbf{Q}_{\mathrm{gs}} \\ & \mathbf{Q}_{\mathrm{gd}} \\ & \hline \end{aligned}$ | $\} V_{G S}=10 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0.5 \cdot \mathrm{~V}_{\mathrm{DSS}}, \mathrm{I}_{\mathrm{D}}=0.5 \cdot \mathrm{I}_{\mathrm{D} 25}$ |  | $\begin{aligned} & 88 \\ & 30 \\ & 31 \end{aligned}$ | nC nC nC C |
| $\begin{aligned} & \mathbf{R}_{\mathrm{thuc}} \\ & \mathbf{R}_{\mathrm{thcs}} \\ & \hline \end{aligned}$ |  |  | 0.25 | $\begin{array}{r} 0.15{ }^{\circ} \mathrm{C} / \mathrm{W} \\ { }^{\circ} \mathrm{C} / \mathrm{W} \end{array}$ |

## Source-Drain Diode

| Symbol Test Conditions$\left(T_{j}=25^{\circ} \mathrm{C}\right.$ Unless Otherwise Specified) |  | Characteristic Values |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Typ. | Max |  |
| $\mathrm{I}_{\text {s }}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}$ |  |  | 42 | A |
| $\underline{\mathrm{I}_{\text {SM }}}$ | Repetitive, Pulse Width Limited by $\mathrm{T}_{\mathrm{JM}}$ |  |  | 168 | A |
| $\mathrm{V}_{\text {sD }}$ | $\mathrm{I}_{\mathrm{F}}=\mathrm{I}_{\mathrm{S}}, \mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}$, Note 1 |  |  | 1.5 | V |
| $\mathrm{t}_{\mathrm{rr}}$ | $\begin{aligned} & I_{F}=21 \mathrm{~A},-\mathrm{di} / \mathrm{dt}=100 \mathrm{~A} / \mu \mathrm{s} \\ & \mathrm{~V}_{\mathrm{R}}=100 \mathrm{~V}, \mathrm{~V}_{G S}=0 \mathrm{~V} \end{aligned}$ |  | 400 |  | ns |



Pins: 1 - Gate 2 - Drain 3-Source 4-Drain


Note 1. Pulse test, $\mathrm{t} \leq 300 \mu \mathrm{~s}$, duty cycle, $\mathrm{d} \leq 2 \%$.

## ADVANCE TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

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