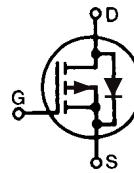


**PolarP™**  
**Power MOSFET**

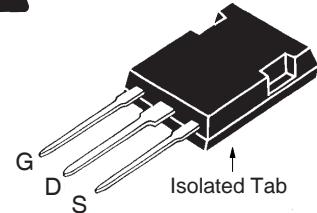
**IXTR32P60P**

P-Channel Enhancement Mode  
Avalanche Rated



**V<sub>DSS</sub>** = - 600V  
**I<sub>D25</sub>** = - 18A  
**R<sub>DS(on)</sub>** ≤ 385mΩ

ISOPLUS247  
 E153432



G = Gate      D = Drain  
S = Source

Symbol	Test Conditions	Maximum Ratings		
<b>V<sub>DSS</sub></b>	T <sub>J</sub> = 25°C to 150°C	- 600	V	
<b>V<sub>DGR</sub></b>	T <sub>J</sub> = 25°C to 150°C, R <sub>GS</sub> = 1MΩ	- 600	V	
<b>V<sub>GSS</sub></b>	Continuous	±20	V	
<b>V<sub>GSM</sub></b>	Transient	±30	V	
<b>I<sub>D25</sub></b>	T <sub>C</sub> = 25°C	-18	A	
<b>I<sub>DM</sub></b>	T <sub>C</sub> = 25°C, Pulse Width Limited by T <sub>JM</sub>	- 96	A	
<b>I<sub>A</sub></b>	T <sub>C</sub> = 25°C	- 32	A	
<b>E<sub>AS</sub></b>	T <sub>C</sub> = 25°C	3.5	J	
<b>dv/dt</b>	I <sub>S</sub> ≤ I <sub>DM</sub> , V <sub>DD</sub> ≤ V <sub>DSS</sub> , T <sub>J</sub> ≤ 150°C	10	V/ns	
<b>P<sub>D</sub></b>	T <sub>C</sub> = 25°C	310	W	
<b>T<sub>J</sub></b>		-55 ... +150	°C	
<b>T<sub>JM</sub></b>		150	°C	
<b>T<sub>stg</sub></b>		-55 ... +150	°C	
<b>T<sub>L</sub></b>	1.6mm (0.062 in.) from Case for 10s	300	°C	
<b>T<sub>SOLD</sub></b>	Plastic Body for 10s	260	°C	
<b>V<sub>ISOL</sub></b>	50/60 Hz, 1 Minute	2500	V~	
<b>M<sub>d</sub></b>	Mounting Force	20..120/4.5..27	N/lb.	
<b>Weight</b>		5	g	

Symbol	Test Conditions (T <sub>J</sub> = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
<b>BV<sub>DSS</sub></b>	V <sub>GS</sub> = 0V, I <sub>D</sub> = - 250μA	- 600		V
<b>V<sub>GS(th)</sub></b>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -1mA	- 2.0		V
<b>I<sub>GSS</sub></b>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100 nA
<b>I<sub>DSS</sub></b>	V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0V T <sub>J</sub> = 125°C			- 50 μA - 250 μA
<b>R<sub>DS(on)</sub></b>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -16A, Note 1			385 mΩ

### Features

- Silicon Chip on Direct-Copper Bond (DCB) Substrate
  - UL Recognized Package
  - Isolated Mounting Surface
  - 2500V~ Electrical Isolation
- Avalanche Rated
- The Rugged PolarP™ Process
- Low Q<sub>G</sub>
- Low Drain-to-Tab Capacitance
- Low Package Inductance

### Advantages

- Easy to Mount
- Space Savings
- High Power Density

### Applications

- High-Side Switches
- Push Pull Amplifiers
- DC Choppers
- Automatic Test Equipment
- Current Regulators

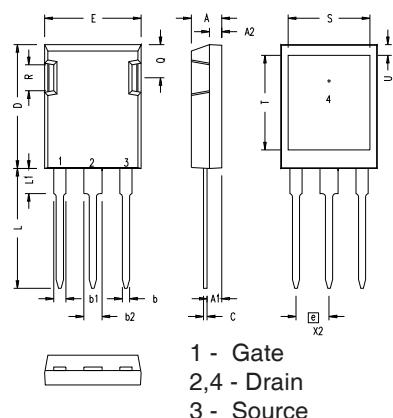
Symbol	Test Conditions (T <sub>J</sub> = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$g_{fs}$	V <sub>DS</sub> = -10V, I <sub>D</sub> = -16A, Note 1	21	32	S
C <sub>iss</sub>		11.1		nF
C <sub>oss</sub>		925		pF
C <sub>rss</sub>		77		pF
t <sub>d(on)</sub>	<b>Resistive Switching Times</b> V <sub>GS</sub> = -10V, V <sub>DS</sub> = 0.5 • V <sub>DSS</sub> , I <sub>D</sub> = -16A R <sub>G</sub> = 1Ω (External)	37		ns
t <sub>r</sub>		27		ns
t <sub>d(off)</sub>		95		ns
t <sub>f</sub>		33		ns
Q <sub>g(on)</sub>		196		nC
Q <sub>gs</sub>		54		nC
Q <sub>gd</sub>		58		nC
R <sub>thJC</sub>			0.40 °C/W	
R <sub>thCS</sub>		0.15		°C/W

### Source-Drain Diode

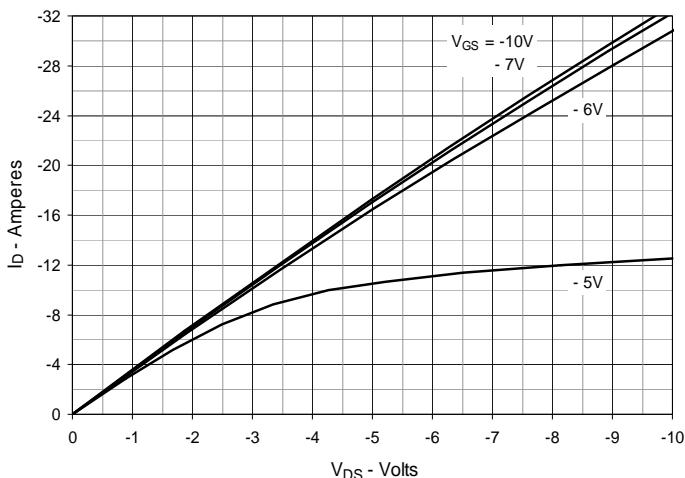
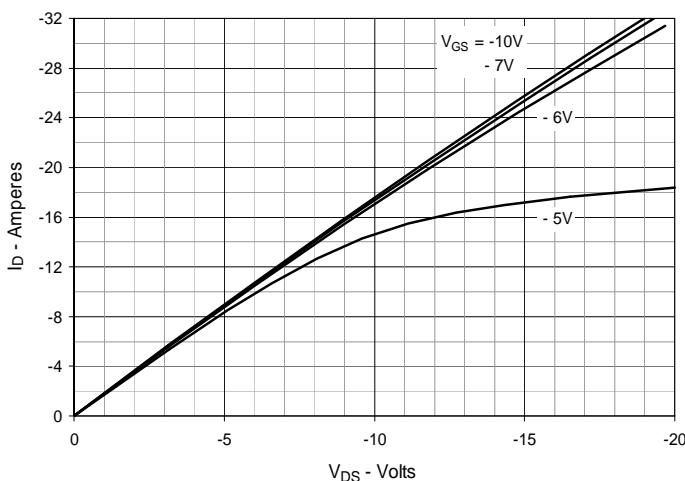
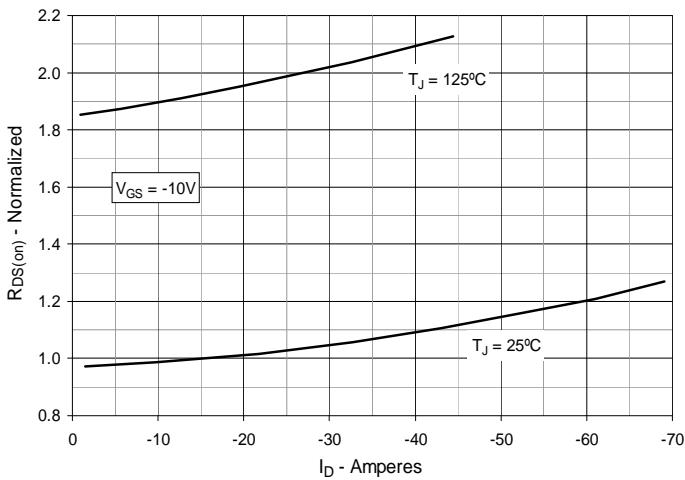
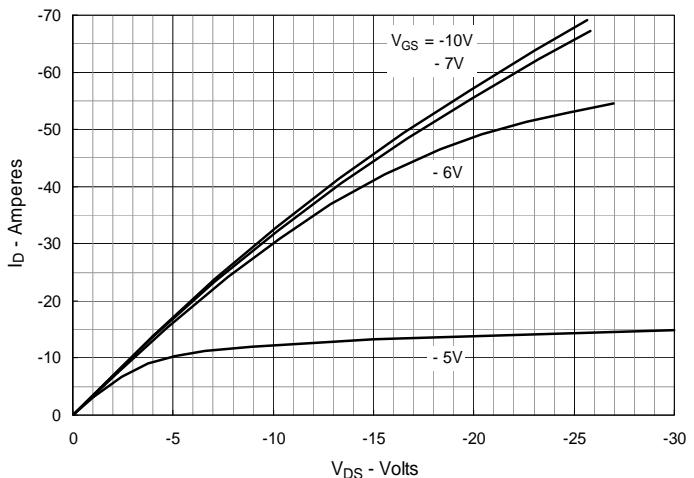
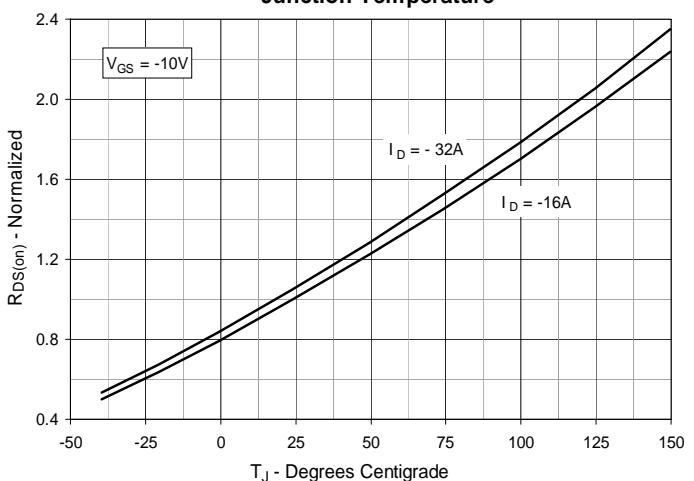
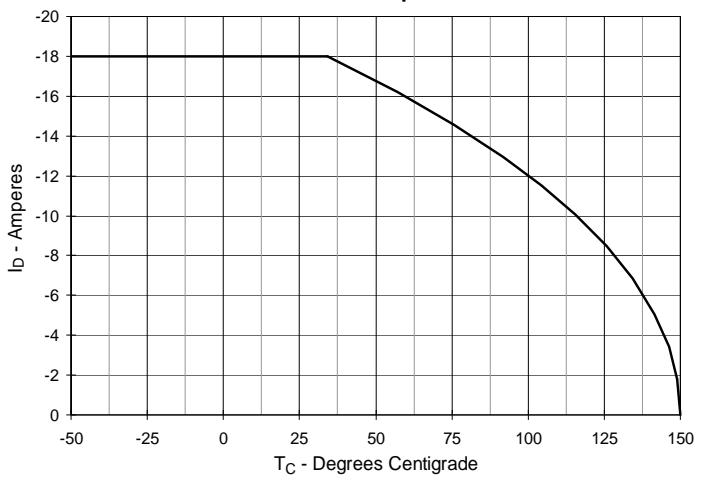
Symbol	Test Conditions (T <sub>J</sub> = 25°C, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
I <sub>s</sub>	V <sub>GS</sub> = 0V		- 32	A
I <sub>SM</sub>	Repetitive, Pulse Width Limited by T <sub>JM</sub>		- 128	A
V <sub>SD</sub>	I <sub>F</sub> = -16A, V <sub>GS</sub> = 0V, Note 1		- 2.8	V
t <sub>rr</sub>	I <sub>F</sub> = -16A, -di/dt = -150A/μs V <sub>R</sub> = -100V, V <sub>GS</sub> = 0V	480		ns
Q <sub>RM</sub>		11.4		μC
I <sub>RM</sub>		- 47.6		A

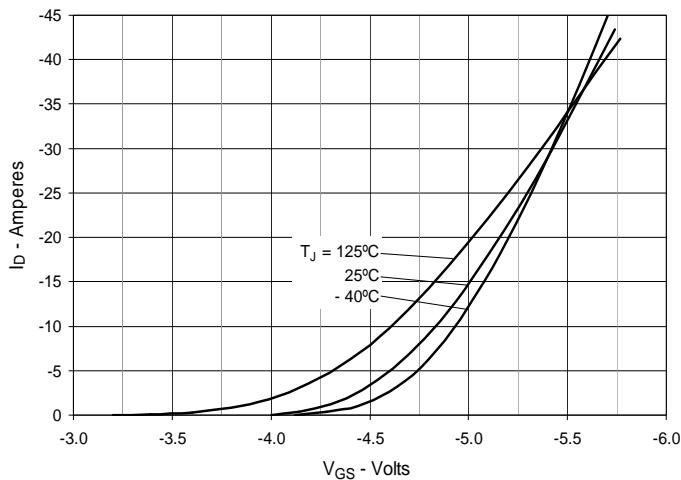
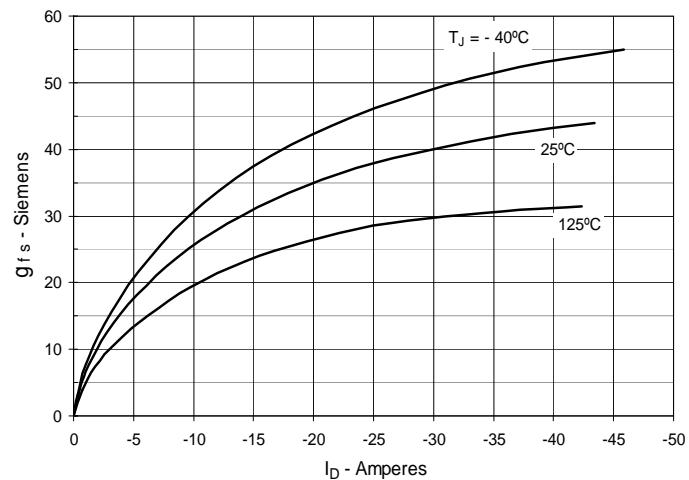
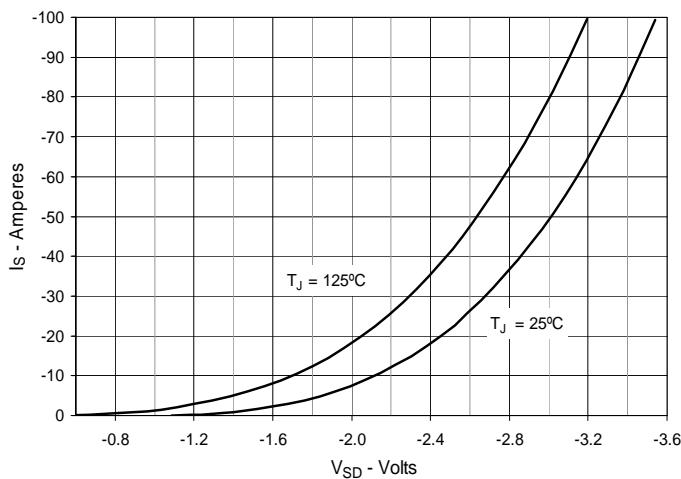
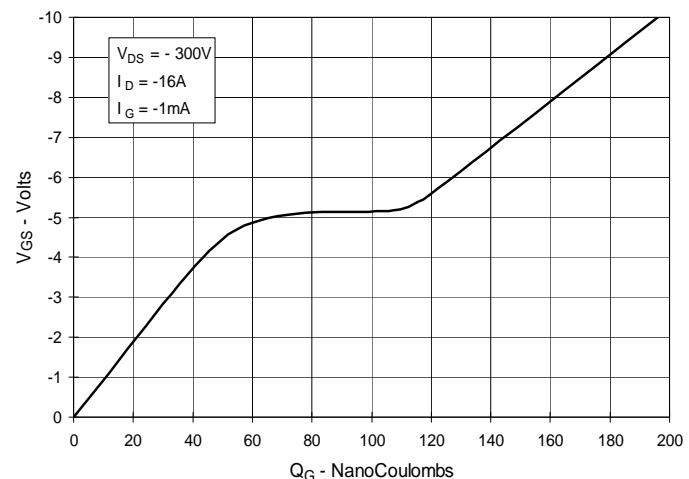
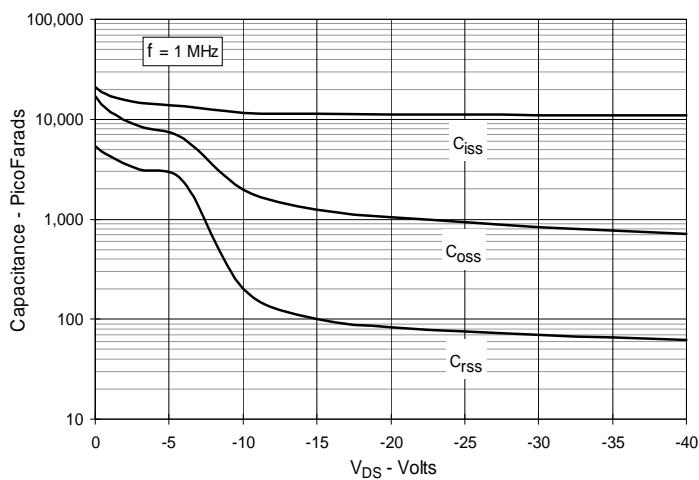
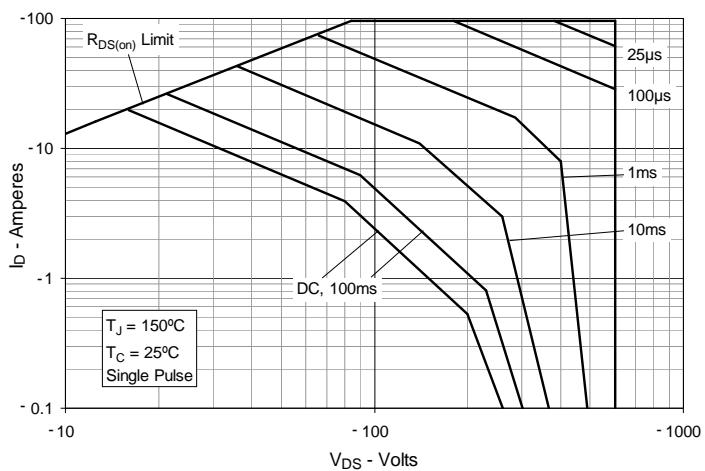
Note 1: Pulse test, t ≤ 300μs, duty cycle, d ≤ 2%.

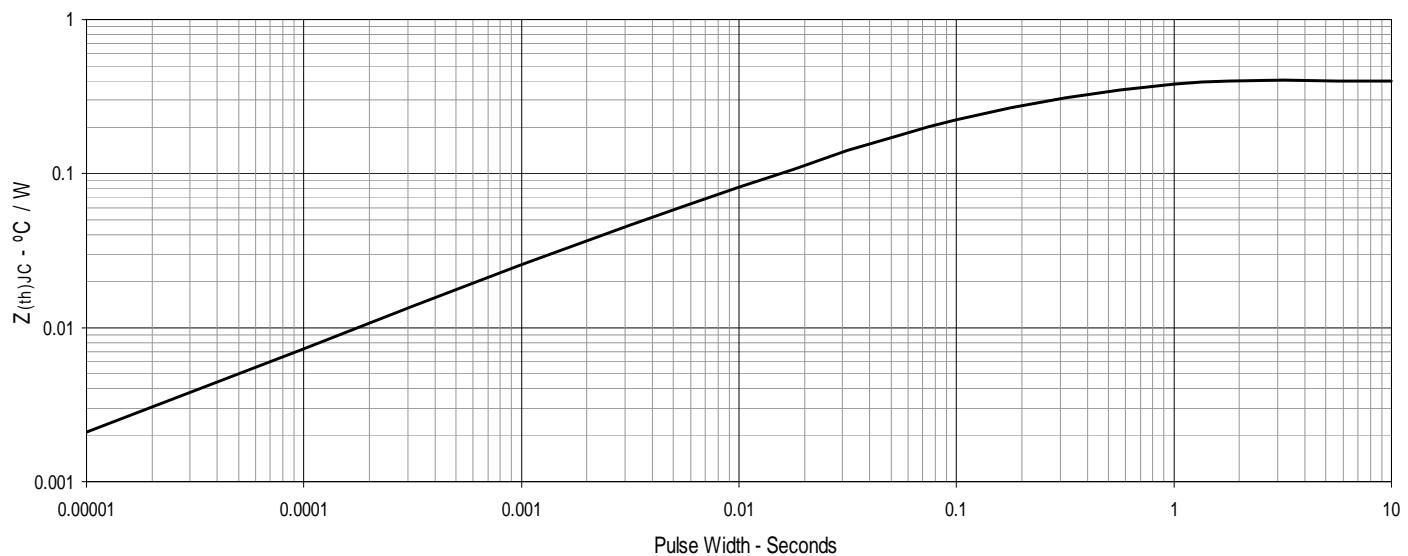
### ISOPLUS247 (IXTR) Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.190	.205	4.83	5.21
A1	.090	.100	2.29	2.54
A2	.075	.085	1.91	2.16
b	.045	.055	1.14	1.40
b1	.075	.084	1.91	2.13
b2	.115	.123	2.92	3.12
C	.024	.031	0.61	0.80
D	.819	.840	20.80	21.34
E	.620	.635	15.75	16.13
e	.215 BSC		5.45 BSC	
L	.780	.800	19.81	20.32
L1	.150	.170	3.81	4.32
Q	.220	.244	5.59	6.20
R	.170	.190	4.32	4.83
S	.520	.540	13.21	13.72
T	.620	.640	15.75	16.26
U	.065	.080	1.65	2.03

**Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$** **Fig. 3. Output Characteristics @  $T_J = 125^\circ\text{C}$** **Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = -16\text{A}$  Value vs. Drain Current****Fig. 2. Extended Output Characteristics @  $T_J = 25^\circ\text{C}$** **Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = -16\text{A}$  Value vs. Junction Temperature****Fig. 6. Maximum Drain Current vs. Case Temperature**

**Fig. 7. Input Admittance****Fig. 8. Transconductance****Fig. 9. Forward Voltage Drop of Intrinsic Diode****Fig. 10. Gate Charge****Fig. 11. Capacitance****Fig. 12. Forward-Bias Safe Operating Area**

**Fig. 13. Maximum Transient Thermal Impedance**

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[DMN1006UCA6-7](#) [DMN16M9UCA6-7](#)