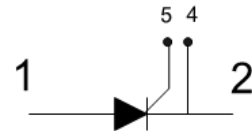


## Phase Control Thyristor Module Types MCO801-14io1 to MCO801-18io1

### Absolute Maximum Ratings

V <sub>RRM</sub> V <sub>DRM</sub> [V]	Type
1400	MCO801-14io1
1800	MCO801-18io1



	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V <sub>DRM</sub>	Repetitive peak off-state voltage <sup>1)</sup>	1400-1800	V
V <sub>DSM</sub>	Non-repetitive peak off-state voltage <sup>1)</sup>	1400-1800	V
V <sub>RRM</sub>	Repetitive peak reverse voltage <sup>1)</sup>	1400-1800	V
V <sub>RSM</sub>	Non-repetitive peak reverse voltage <sup>1)</sup>	1500-1900	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS	
I <sub>T(AV)M</sub>	Maximum average on-state current, T <sub>c</sub> =85°C <sup>2)</sup>	830	A	
I <sub>T(AV)M</sub>	Maximum average on-state current, T <sub>c</sub> =100°C <sup>2)</sup>	615	A	
I <sub>T(RMS)M</sub>	Nominal RMS on-state current, T <sub>c</sub> =25°C <sup>2)</sup>	2335	A	
I <sub>T(d.c.)</sub>	D.C. on-state current, T <sub>c</sub> =25°C <sup>2)</sup>	1965	A	
I <sub>TSM</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>rm</sub> =60%V <sub>RRM</sub> <sup>3)</sup>	30	kA	
I <sub>TSM2</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>rm</sub> ≤10V <sup>3)</sup>	33	kA	
I <sup>2</sup> t	I <sup>2</sup> t capacity for fusing t <sub>p</sub> =10ms, V <sub>rm</sub> =60%V <sub>RRM</sub> <sup>3)</sup>	4.50×10 <sup>6</sup>	A <sup>2</sup> s	
I <sup>2</sup> t	I <sup>2</sup> t capacity for fusing t <sub>p</sub> =10ms, V <sub>rm</sub> ≤10V <sup>3)</sup>	5.45×10 <sup>6</sup>	A <sup>2</sup> s	
(di/dt) <sub>cr</sub>	Critical rate of rise of on-state current <sup>4)</sup>	(continuous, 50Hz)	100	A/μs
		(repetitive, 50Hz, 60s)	200	
		(non-repetitive)	400	
V <sub>RGM</sub>	Peak reverse gate voltage	5	V	
P <sub>G(AV)</sub>	Mean forward gate power	4	W	
P <sub>GM</sub>	Peak forward gate power	30	W	
V <sub>ISOL</sub>	Isolation Voltage <sup>5)</sup>	3000	V	
T <sub>vj op</sub>	Operating temperature range	-40 to +130	°C	
T <sub>stg</sub>	Storage temperature range	-40 to +150	°C	

**Notes:**

- 1) De-rating factor of 0.13% per °C is applicable for T<sub>vj</sub> below 25°C.
- 2) Single phase; 50 Hz, 180° half-sinewave.
- 3) Half-sinewave, 130°C T<sub>vj</sub> initial.
- 4) V<sub>D</sub> = 67% V<sub>DRM</sub>, I<sub>FG</sub> = 2 A, t<sub>r</sub> ≤ 0.5μs, T<sub>c</sub> = 130°C.
- 5) AC RMS voltage, 50 Hz, 1min test

**Characteristics**

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS <sup>1)</sup>	UNITS
V <sub>TM</sub>	Maximum peak on-state voltage	-	-	1.43	I <sub>TM</sub> =2400A	V
		-	-	1.50	I <sub>TM</sub> =3000A	V
V <sub>T0</sub>	Threshold voltage	-	-	0.80		V
r <sub>T</sub>	Slope resistance	-	-	0.24		mΩ
(dv/dt) <sub>cr</sub>	Critical rate of rise of off-state voltage	1000	-	-	V <sub>D</sub> = 80% V <sub>DRM</sub> , linear ramp, Gate o/c	V/μs
I <sub>DRM</sub>	Peak off-state current	-	-	150	Rated V <sub>DRM</sub>	mA
I <sub>RRM</sub>	Peak reverse current	-	-	150	Rated V <sub>RDM</sub>	
V <sub>GT</sub>	Gate trigger voltage	-	-	2.5	T <sub>vj</sub> = 25°C, V <sub>D</sub> = 12 V, I <sub>T</sub> = 3 A	V
I <sub>GT</sub>	Gate trigger current	-	-	300		mA
I <sub>H</sub>	Holding current	-	-	300	T <sub>vj</sub> = 25°C	mA
I <sub>L</sub>	Latching current	-	-	1500	T <sub>vj</sub> = 25°C	mA
t <sub>gd</sub>	Gate controlled turn-on delay time	-	0.8	2.0	V <sub>D</sub> =40% V <sub>DRM</sub> , I <sub>T</sub> =830A, di/dt=10A/μs, I <sub>FG</sub> =2A, t <sub>r</sub> =0.5μs, T <sub>j</sub> =25°C	μs
t <sub>gt</sub>	Turn-on time	-	1.4	3.0		
Q <sub>rr</sub>	Recovered Charge	-	2800	3100	I <sub>TM</sub> =800A, t <sub>p</sub> =1000μs, di/dt=10A/μs, V <sub>r</sub> =100V	μC
Q <sub>ra</sub>	Recovered Charge, 50% chord	-	2200	-		μC
I <sub>rm</sub>	Reverse recovery current	-	160	-		A
t <sub>rr</sub>	Reverse recovery time, 50% chord	-	27	-		μs
t <sub>q</sub>	Turn-off time	-	225	-	I <sub>TM</sub> =830A, t <sub>p</sub> =1000μs, di/dt=10A/μs, V <sub>r</sub> =100V, V <sub>dr</sub> =67%V <sub>DRM</sub> , dV <sub>dr</sub> /dt=20V/μs	μs
		-	300	-	I <sub>TM</sub> =830A, t <sub>p</sub> =1000μs, di/dt=10A/μs, V <sub>r</sub> =100V, V <sub>dr</sub> =67%V <sub>DRM</sub> , dV <sub>dr</sub> /dt=200V/μs	
R <sub>thJC</sub>	Thermal resistance, junction to case	-	-	0.042		K/W
R <sub>thCH</sub>	Thermal resistance, case to heatsink	-	-	0.010		K/W
F <sub>1</sub>	Mounting torque (to heatsink)	5.1	-	6.9		Nm
F <sub>2</sub>	Mounting torque (to terminals)	16.2	-	19.8		Nm
W <sub>t</sub>	Weight	-	2.8	-		kg

**Notes:**

 1) Unless otherwise indicated T<sub>vj</sub>=130°C.

## Notes on Ratings and Characteristics

### 1.0 Voltage Grade Table

Voltage Grade	$V_{DRM}$ $V_{DSM}$ $V_{RRM}$ V	$V_{RSM}$ V	$V_D$ $V_R$ DC V
14	1400	1500	950
18	1800	1900	1150

### 2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

### 3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for  $T_{vj}$  below 25°C.

### 4.0 Repetitive dv/dt

Standard dv/dt is 1000V/μs.

### 5.0 Snubber Components

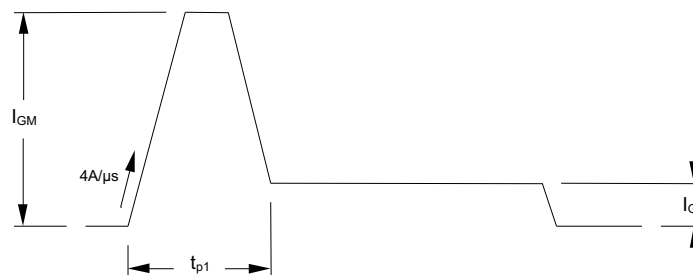
When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

### 6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 300A/μs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 150A/μs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

### 7.0 Gate Drive

The nominal requirement for a typical gate drive is illustrated below. An open circuit voltage of at least 30V is assumed. This gate drive must be applied when using the full di/dt capability of the device.



The magnitude of  $I_{GM}$  should be between five and ten times  $I_{GT}$ , which is shown on page 2. Its duration ( $t_{p1}$ ) should be 20μs or sufficient to allow the anode current to reach ten times  $I_L$ , whichever is greater. Otherwise, an increase in pulse current could be needed to supply the necessary charge to trigger. The 'back-porch' current  $I_G$  should remain flowing for the same duration as the anode current and have a magnitude in the order of 1.5 times  $I_{GT}$ .

## 8.0 Computer Modelling Parameters

### 8.1 Thyristor dissipation calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \quad \text{and:} \quad W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j\max} - T_C$$

Where  $V_{T0} = 0.80 \text{ V}$ ,  $r_T = 0.24 \text{ m}\Omega$ .

$R_{th}$  = Supplementary thermal impedance, see table below and

$ff$  = Form factor, see table below.

Supplementary Thermal Impedance (Junction to Case)							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave	0.0465	0.0460	0.0453	0.0448	0.0439	0.0428	0.0420
Sine wave	0.0460	0.0453	0.0447	0.0442	0.0428		

Form Factors							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave	3.464	2.449	2	1.732	1.414	1.149	1
Sine wave	4.025	2.778	2.22	1.879	1.57		

### 8.2 Calculating thyristor $V_T$ using ABCD coefficients – For loss calculations

The on-state characteristic,  $I_T$  vs.  $V_T$ , is represented in two ways;

- (i) the well established  $V_{T0}$  and  $r_T$  tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for  $V_T$  in terms of  $I_T$  given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics.

The resulting values for  $V_T$  agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		130°C Coefficients	
A	0.7422077	A	0.5980003
B	0.04165964	B	$5.402853 \times 10^{-3}$
C	$2.164076 \times 10^{-4}$	C	$1.545497 \times 10^{-4}$
D	$-3.218256 \times 10^{-3}$	D	$7.218154 \times 10^{-3}$

### 8.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left( 1 - e^{-\frac{t}{\tau_p}} \right)$$

Where  $p = 1$  to  $n$

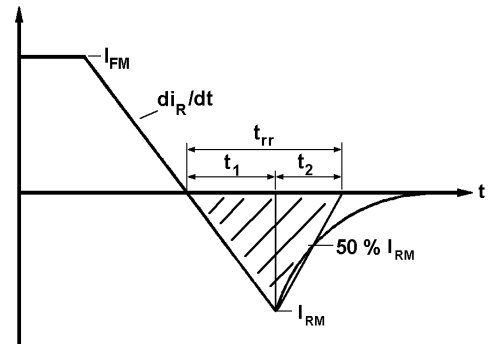
- $n$  = number of terms in the series and
- $t$  = Duration of heating pulse in seconds.
- $r_t$  = Thermal resistance at time  $t$ .
- $r_p$  = Amplitude of  $p^{\text{th}}$  term.
- $\tau_p$  = Time Constant of  $r_{\text{th}}$  term.

The coefficients for this device are shown in the tables below:

D.C. Junction to Case			
Term	1	2	3
$r_p$	0.01904887	0.01751398	$5.364393 \times 10^{-3}$
$\tau_p$	21.99824	1.079638	0.0132514

### 9.0 Reverse recovery ratings

(i)  $Q_{ra}$  is based on 50%  $I_{RM}$  chord as shown in Fig. 1



**Fig. 1**

(ii)  $Q_{rr}$  is based on a  $150 \mu s$  integration time i.e.

$$Q_{rr} = \int_0^{150 \mu s} i_{rr} \cdot dt$$

(iii)

$$K \text{ Factor} = \frac{t_1}{t_2}$$

**Curves**

Figure 1 – On-state characteristics of Limit device

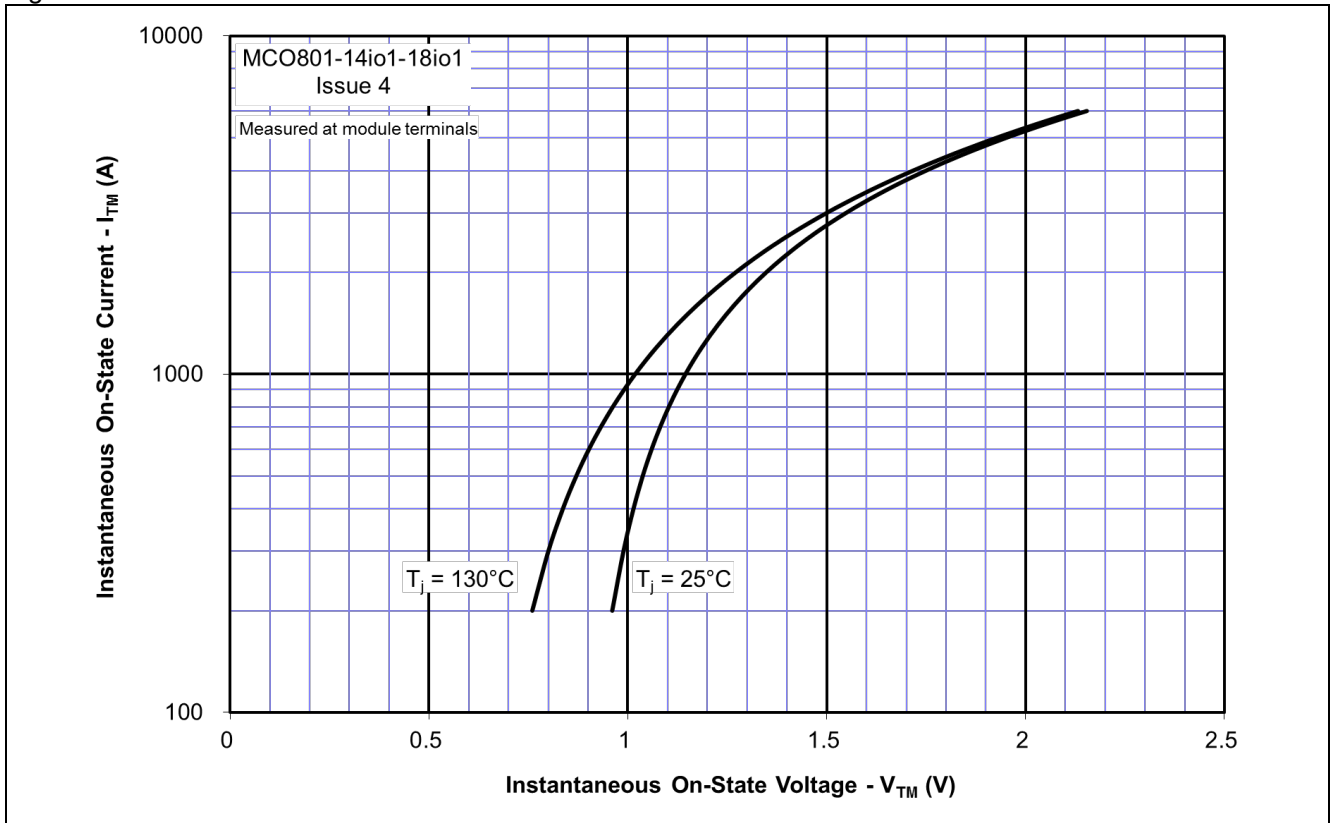


Figure 2 – Gate characteristics – Trigger limits

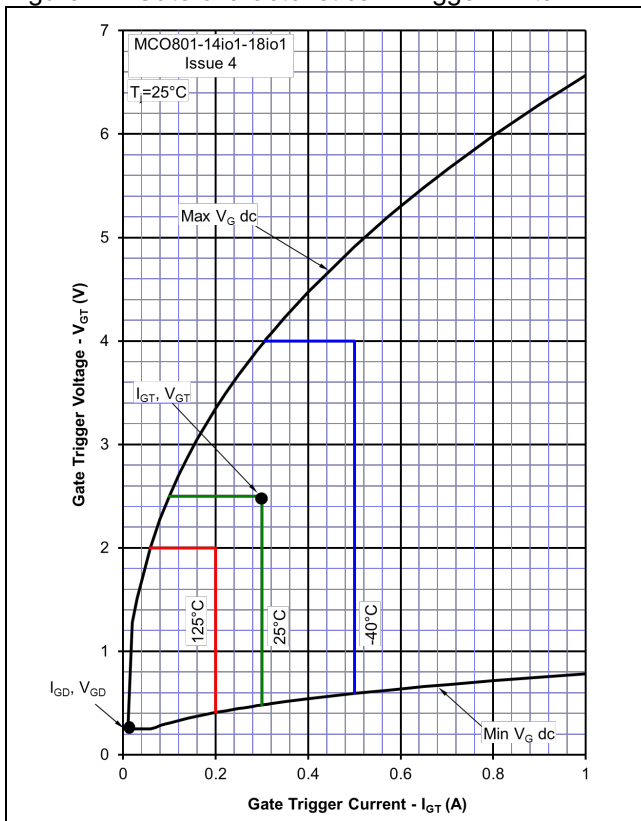


Figure 3 – Gate characteristics – Power curves

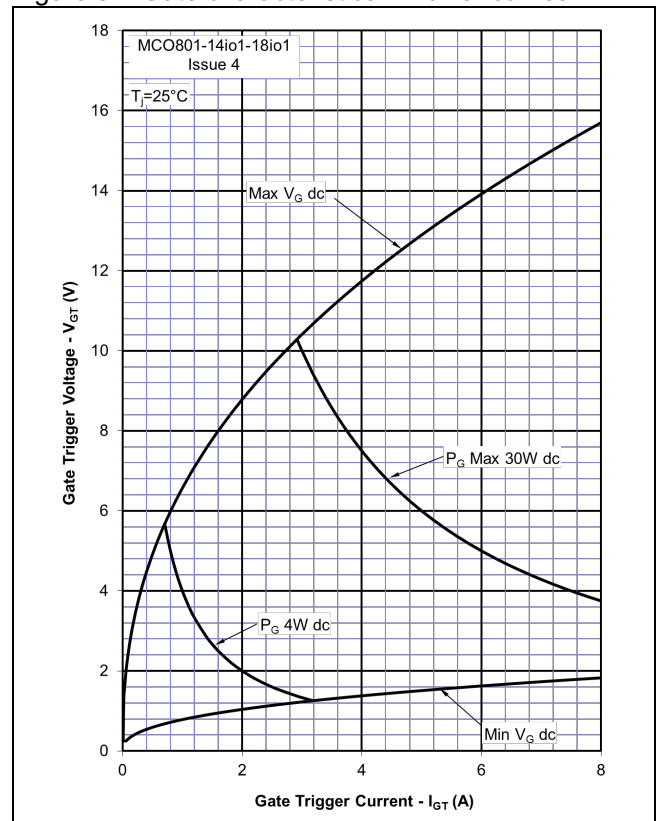


Figure 4 – On-state current vs. Power dissipation – Sine wave

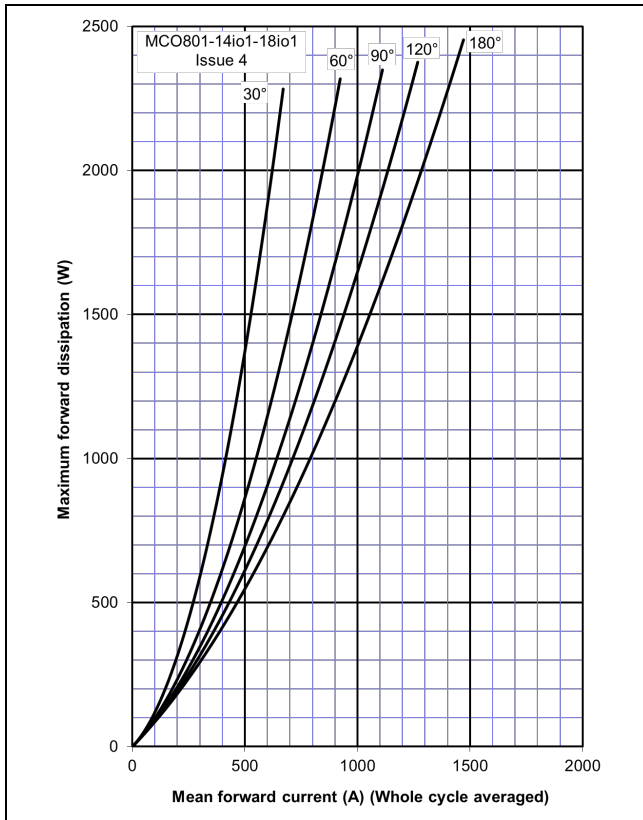


Figure 5 – On-state current vs. Heatsink temperature – Sine wave

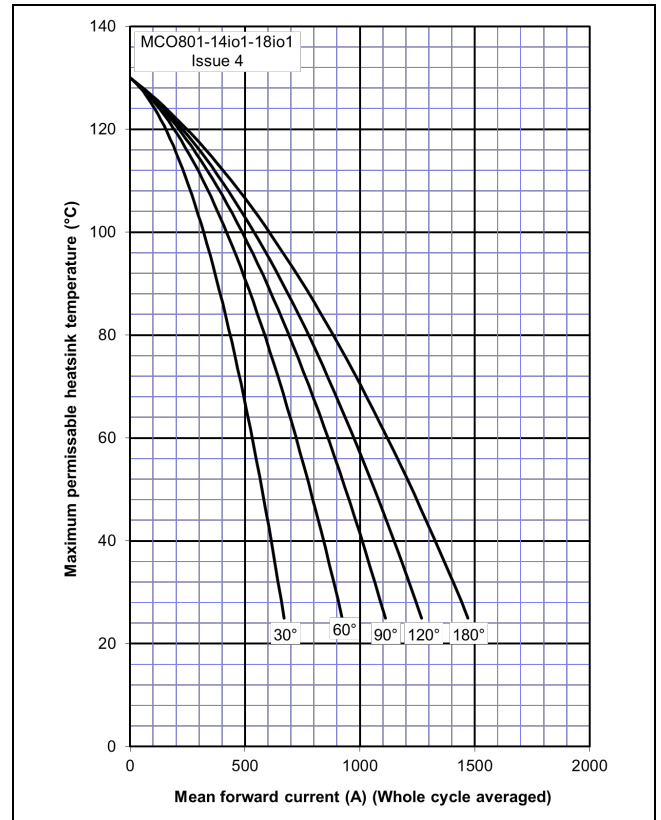


Figure 6 – On-state current vs. Power dissipation – Square wave

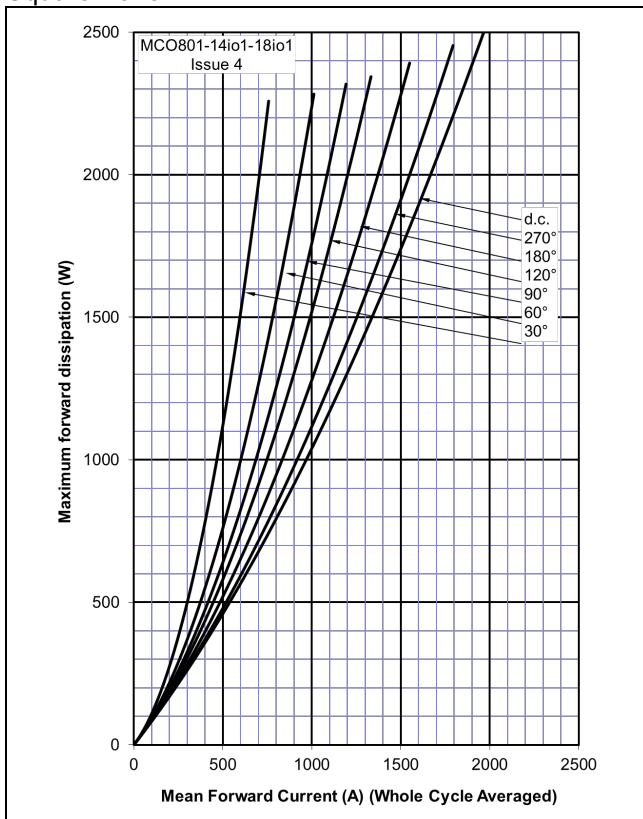


Figure 7 – On-state current vs. Heatsink temperature – Square wave

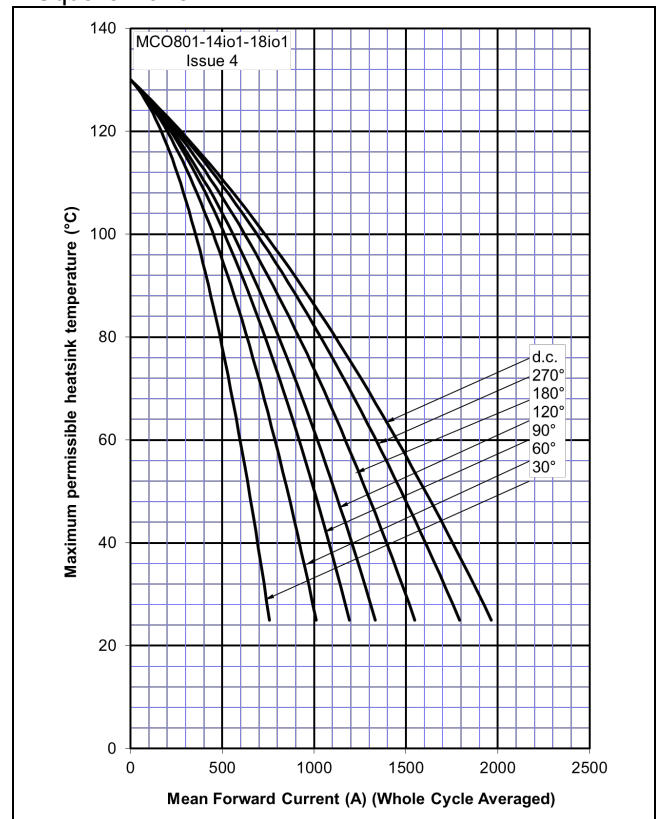


Figure 8 – Maximum surge and I<sup>2</sup>t Ratings

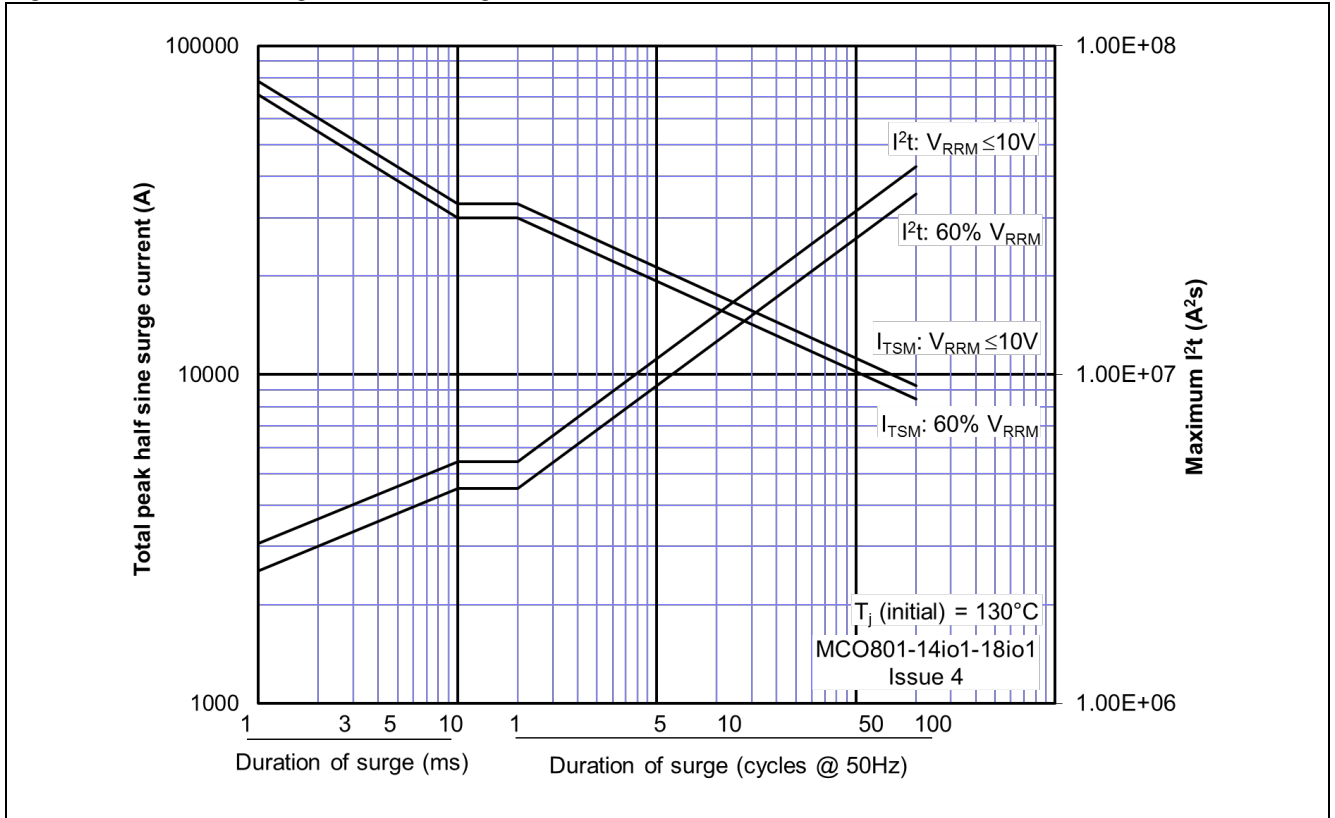
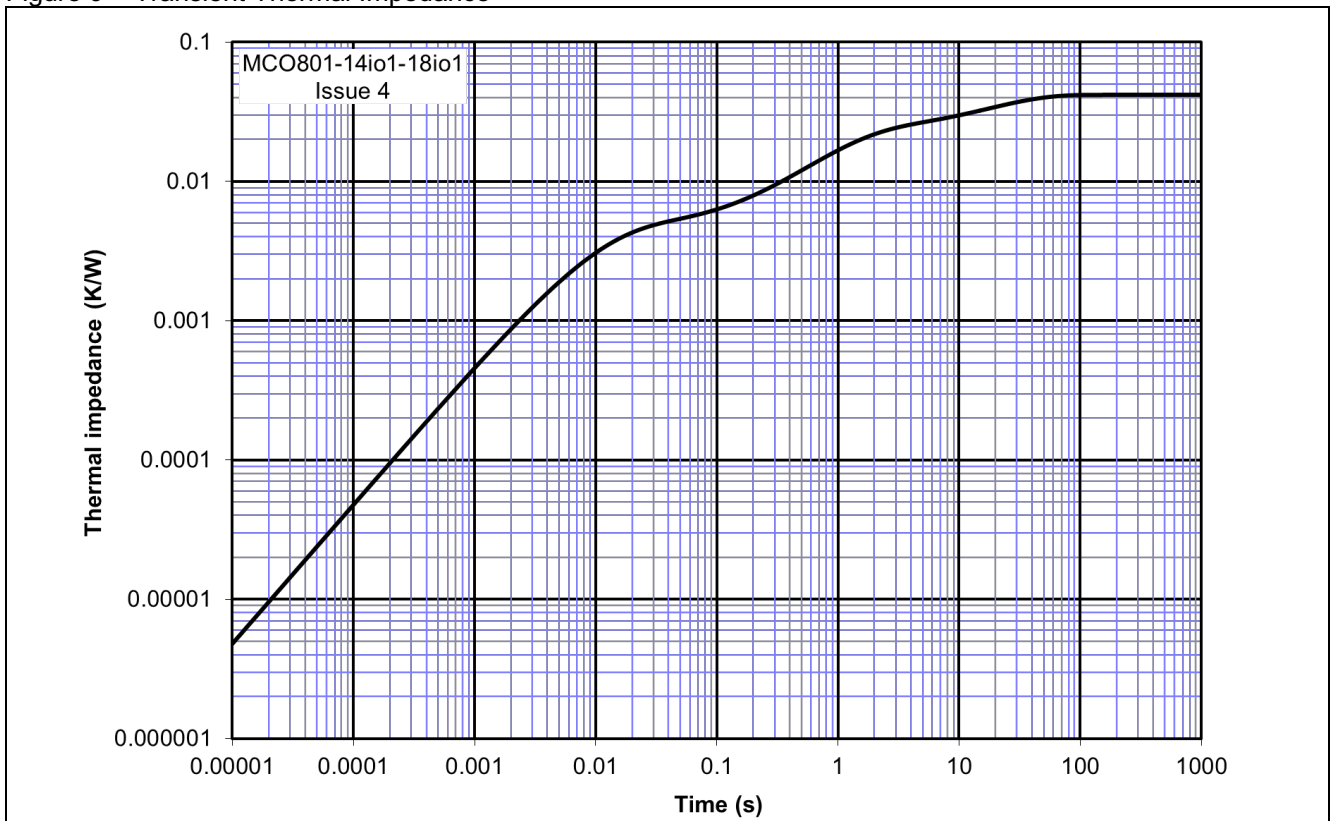
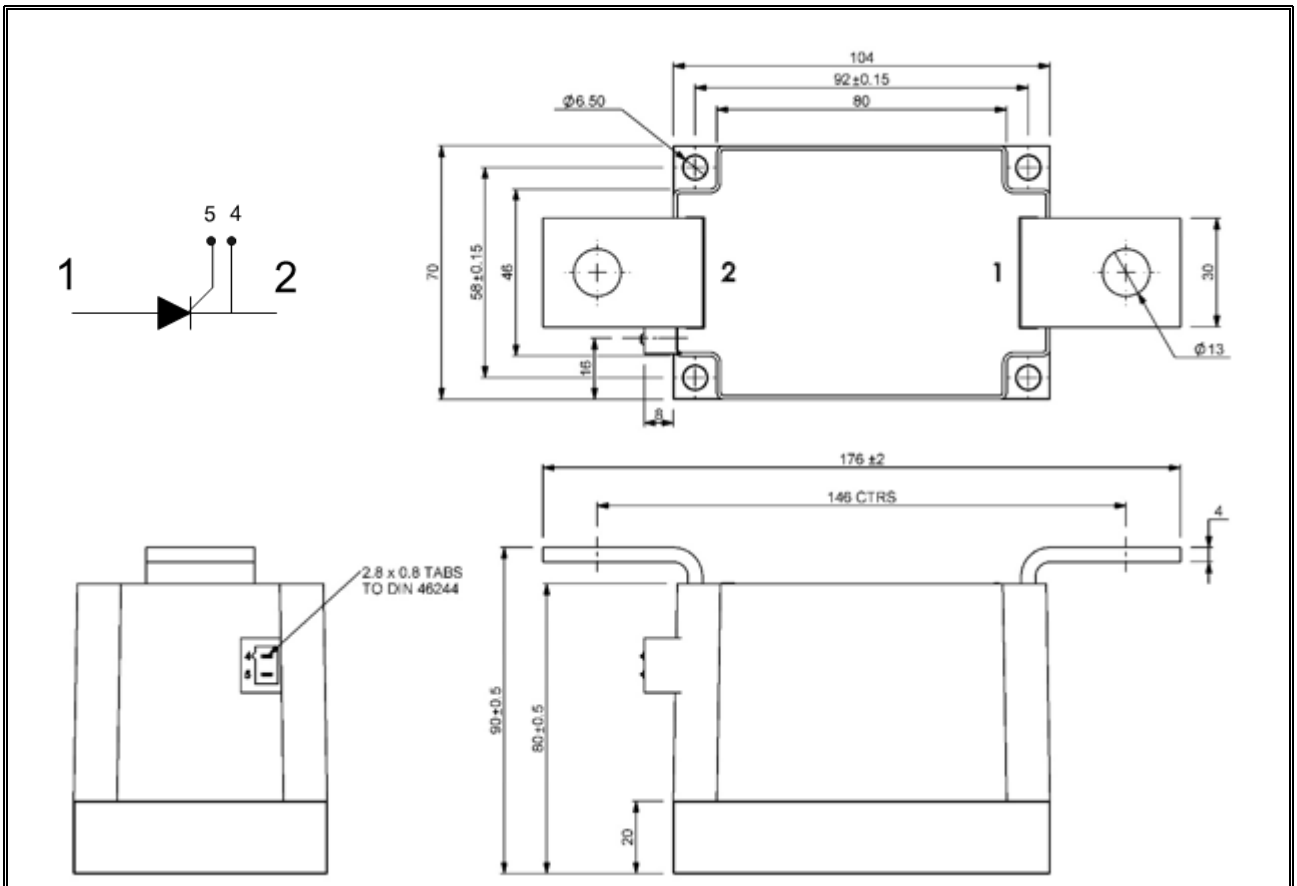


Figure 9 – Transient Thermal Impedance





**Outline Drawing & Ordering Information**



W73 – 150A122

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