

High Voltage Standard Rectifier Module

 $V_{RRM} = 2200 V$

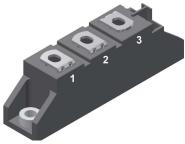
 $I_{EAV} = 2x 120 A$

 $V_{\rm F} = 1.13 \, \rm V$

Common Anode

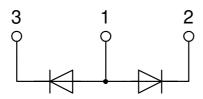
Part number

MDA95-22N1B



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

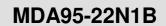
- Diode for main rectification
- For single and three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

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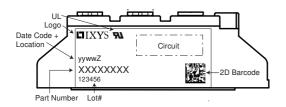


Rectifier	•			1	Ratings	S	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM}	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			2300	V
V _{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			2200	٧
I _R	reverse current	V _R = 2200 V	$T_{VJ} = 25^{\circ}C$			200	μΑ
		$V_R = 2200 \text{ V}$	$T_{VJ} = 150$ °C			15	mΑ
V _F	forward voltage drop	I _F = 150 A	$T_{VJ} = 25^{\circ}C$			1.20	٧
		$I_F = 300 A$				1.43	٧
		$I_F = 150 \text{ A}$	T _{VJ} = 125°C			1.13	٧
		$I_F = 300 A$				1.46	٧
I _{FAV}	average forward current	T _C = 100°C	T _{vJ} = 150°C			120	Α
I _{F(RMS)}	RMS forward current	180° sine				180	Α
V _{F0}	threshold voltage		T _{vJ} = 150°C			0.75	V
r _F	slope resistance \(\) for power	loss calculation only				1.95	mΩ
R _{thJC}	thermal resistance junction to ca	ase				0.26	K/W
R _{thCH}	thermal resistance case to heats	sink			0.2		K/W
P _{tot}	total power dissipation		$T_{\text{C}} = 25^{\circ}\text{C}$			481	W
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			2.80	kA
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			3.03	kA
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			2.38	kA
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			2.57	kA
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			39.2	kA2s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			38.1	kA2s
		t = 10 ms; (50 Hz), sine	T _{vJ} = 150°C			28.3	kA2s
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			27.5	kA2s
C	junction capacitance	$V_{R} = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		116		pF





Package TO-240AA			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					200	Α
T _{VJ}	virtual junction temperature				-40		150	°C
Top	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						76		g
M _D	mounting torque				2.5		4	Nm
$\mathbf{M}_{\scriptscriptstyleT}$	terminal torque				2.5		4	Nm
d _{Spp/App}	araanaga diatanaa an aurfa	as latriking distance through air	terminal to terminal	13.0	9.7			mm
d _{Spb/Apb}	creepage distance on surface striking distance through		terminal to backside	16.0	16.0			mm
V	isolation voltage	t = 1 second	50/60 Hz, RMS; lisoL ≤ 1 mA		4800			V
.002		t = 1 minute			4000			٧

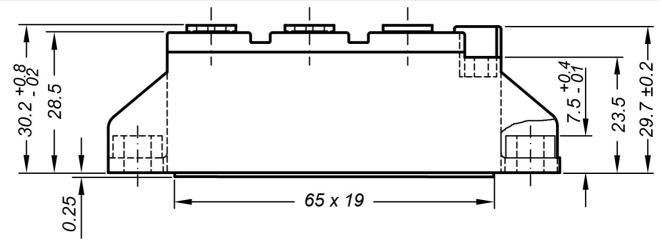


Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MDA95-22N1B	MDA95-22N1B	Box	36	510571

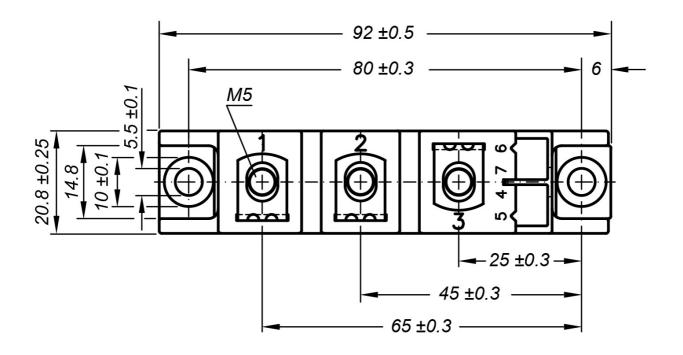
Equiva	alent Circuits for	Simulation	* on die level	$T_{VJ} = 150$ °C
$I \rightarrow V_0$)— <u>R</u> o	Rectifier		
V _{0 max}	threshold voltage	0.75		V
$R_{0 \text{ max}}$	slope resistance *	0.76		$m\Omega$

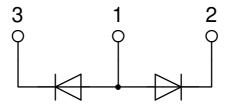


Outlines TO-240AA



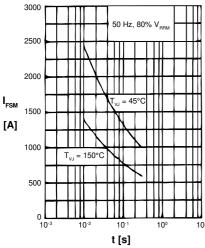
General tolerance: DIN ISO 2768 class "c"

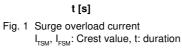






Rectifier





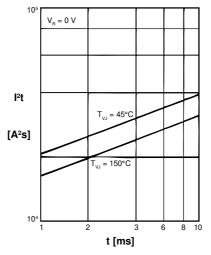


Fig. 2 I2t versus time (1-10 ms)

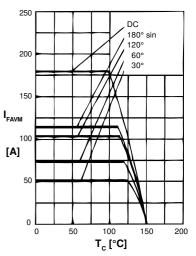


Fig. 3 Maximum forward current at case temperature

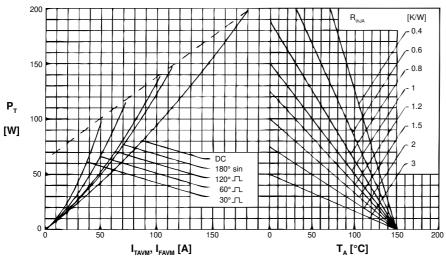


Fig. 4 Power dissipation vs. onstate current and ambient temperature (per diode)

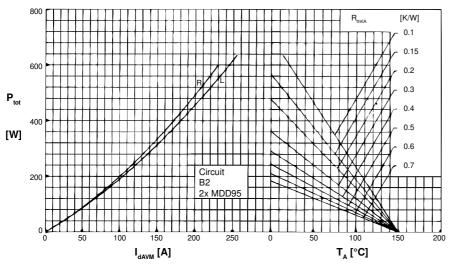


Fig. 6 Single phase rectifier bridge: Power dissipation versus direct output current and ambient temperature; R = resistive load,L = inductive load



Rectifier

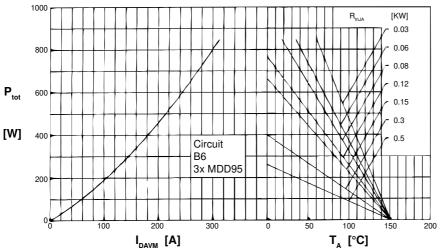


Fig. 6 Three phase rectifier bridge: Power dissipation versus direct output current and ambient temperature

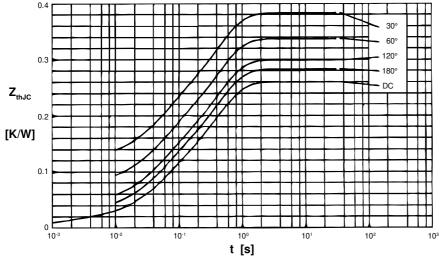


Fig. 7 Transient thermal impedance junction to case (per diode)

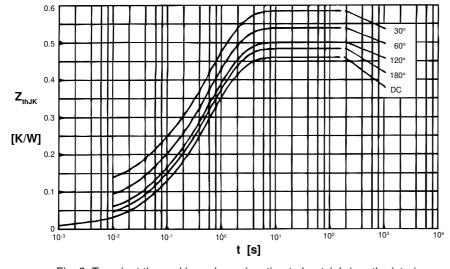


Fig. 8 Transient thermal impedance junction to heatsink (per thyristor)

R_{th IC} for various conduction angles d:

thJC	
d	R _{thJC} [K/V
DC	0.26
180°	0.28
120°	0.30
60°	0.34
30°	0.38

Constants for Z_{thJC} calculation:

i	R _{thi} [K/W]	t _i [s]
1	0.013	0.0012
2	0.072	0.0470
3	0.175	0.3940

 \boldsymbol{R}_{thJK} for various conduction angles d:

UIUIX	
d	R_{thJK} [K/V
DC	0.46
180°	0.48
120°	0.50
60°	0.54
30°	0.58

Constants for Z_{thJK} calculation:

I	R _{thi} [K/W]	t _i [s]
1	0.013	0.0012
2	0.072	0.0470
3	0.175	0.3940
4	0.200	1.3200

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