



High Voltage Standard Rectifier Module

= 2x 2200 V

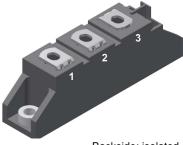
50 A

 V_{F} 1.09 V

Phase leg

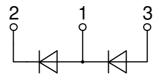
Part number

MDNA50P2200TG



Backside: isolated





Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

- Diode for main rectification
- For single and three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

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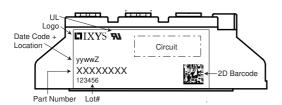


Rectifier					Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit	
V _{RSM}	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			2300	V	
V_{RRM}	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			2200	V	
I _R	reverse current	V _R = 2200 V	$T_{VJ} = 25^{\circ}C$			50	μΑ	
		$V_R = 2200 V$	$T_{VJ} = 150$ °C			1.5	mΑ	
V _F	forward voltage drop	I _F = 50 A	$T_{VJ} = 25^{\circ}C$			1.13	V	
		$I_{F} = 100 \text{ A}$				1.34	٧	
		$I_F = 50 \text{ A}$	$T_{VJ} = 125$ °C			1.09	٧	
		$I_F = 100 \text{ A}$				1.37	٧	
I _{FAV}	average forward current	T _C = 100°C	T _{vJ} = 150°C			50	Α	
		rectangular d = 0.5					1 1 1 1	
V _{F0}	threshold voltage	land and attendants	$T_{VJ} = 150$ °C			0.80	٧	
r _F	slope resistance	loss calculation only				5.7	mΩ	
R _{thJC}	thermal resistance junction to ca	ase				0.65	K/W	
R _{thCH}	thermal resistance case to heat	sink			0.2		K/W	
P _{tot}	total power dissipation		$T_{\text{C}} = 25^{\circ}\text{C}$			190	W	
I _{FSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			850	Α	
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			920	Α	
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			725	Α	
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			780	Α	
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			3.62	kA2s	
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			3.52	kA2s	
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			2.63	kA2s	
		t = 8.3 ms; (60 Hz), sine	$V_R = 0 V$			2.53	kA2s	
CJ	junction capacitance	$V_{R} = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		27		рF	
				1	1	1		



MDNA50P2200TG

Package TO-240AA			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					200	Α
T _{VJ}	virtual junction temperatur	е			-40		150	°C
T _{op}	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		125	°C
Weight						76		g
M _D	mounting torque				2.5		4	Nm
$\mathbf{M}_{_{T}}$	terminal torque				2.5		4	Nm
d _{Spp/App}	oroonaga diatanaa an aurf	terminal to terminal eepage distance on surface striking distance through air		13.0	9.7			mm
d _{Spb/Apb}	creepage distance on sum	ace Striking distance through an	triking distance trirough air terminal to backside		16.0			mm
V _{ISOL}	isolation voltage	t = 1 second			4800			٧
1002		$t = 1 \text{ minute}$ 50/60 Hz, RMS; $l_{ISOL} \le 1 \text{ mA}$			4000			٧



Part description

M = Module

D = Diode
N = High Voltage Standard Rectifier

A = (>= 2000V) 50 = Current Rating [A]

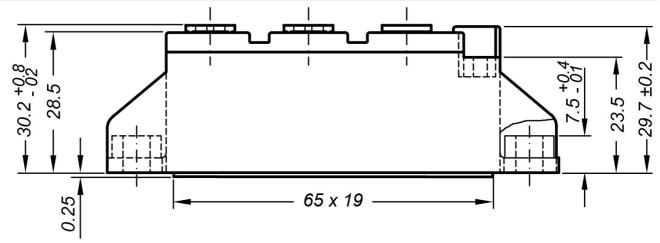
P = Phase leg 2200 = Reverse Voltage [V]

TG = TO-240AA

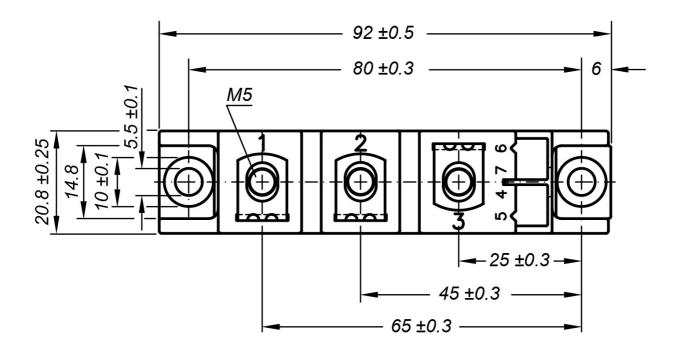
Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MDNA50P2200TG	MDNA50P2200TG	Box	36	525439

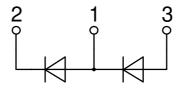
Equivalent Circuits for Simulation			* on die level	$T_{VJ} = 150^{\circ}C$
$I \rightarrow V_0$)—[R_o]-	Rectifier		
V _{0 max}	threshold voltage	8.0		V
R_{0max}	slope resistance *	4.5		mΩ

Outlines TO-240AA



General tolerance: DIN ISO 2768 class "c"









Rectifier

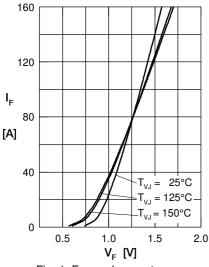


Fig. 1 Forward current versus voltage drop per diode

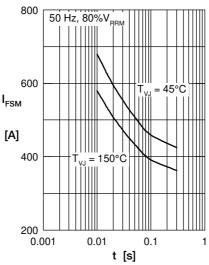


Fig. 2 Surge overload current vs. time per diode

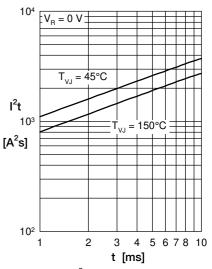


Fig. 3 I²t versus time per diode

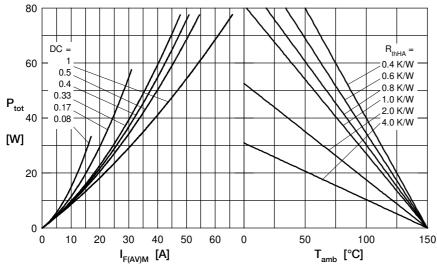


Fig. 4 Power dissipation vs. forward current and ambient temperature per diode

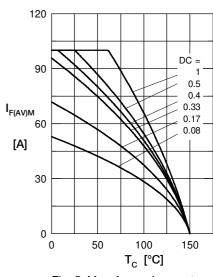


Fig. 5 Max. forward current vs. case temperature per diode

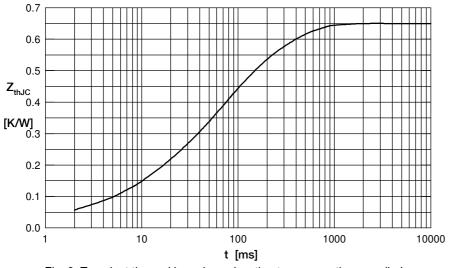


Fig. 6 Transient thermal impedance junction to case vs. time per diode

Constants for Z_{thJC} calculation:

i	R_{thi} (K/W)	t _i (s)
1	0.032	0.001
2	0.098	0.010
3	0.305	0.060
4	0.215	0.270

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T2001N34TOF T901N35TOF T1080N02TOF T360N22TOF TZ810N22KOF T420N18TOF T420N14TOF TD305N16KOF T740N26TOF
T360N24TOF T430N16TOF T300N16TOF TD520N22KOF TT305N16KOF TT270N16KOF TD600N16KOF T740N22TOF T640N12TOF
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