

Date: - 5th Sept 2018

Data Sheet Issue: - 4

# Phase Control Thyristor Types N0910LC260 and N0910LC280

# **Absolute Maximum Ratings**

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
$V_{DRM}$	Repetitive peak off-state voltage, (note 1)	2600-2800	٧
$V_{DSM}$	Non-repetitive peak off-state voltage, (note 1)	2600-2800	V
$V_{RRM}$	Repetitive peak reverse voltage, (note 1)	2600-2800	V
$V_{RSM}$	Non-repetitive peak reverse voltage, (note 1)	2700-2900	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{T(AVM)}$	Maximum average on-state current, T <sub>sink</sub> =55°C, (note 2)	910	Α
$I_{T(AVM)}$	Maximum average on-state current. T <sub>sink</sub> =85°C, (note 2)	630	Α
$I_{T(AVM)}$	Maximum average on-state current. T <sub>sink</sub> =85°C, (note 3)	386	Α
$I_{T(RMS)}$	Nominal RMS on-state current, T <sub>sink</sub> =25°C, (note 2)	1788	Α
$I_{T(d.c.)}$	D.C. on-state current, T <sub>sink</sub> =25°C, (note 4)	1569	Α
I <sub>TSM</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>rm</sub> =0.6V <sub>RRM</sub> , (note 5)	9.2	kA
I <sub>TSM2</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>rm</sub> ≤10V, (note 5)	10.1	kA
I <sup>2</sup> t	$I^{2}t$ capacity for fusing $t_{p}$ =10ms, $V_{rm}$ =0.6 $V_{RRM}$ , (note 5)	423×10 <sup>3</sup>	A <sup>2</sup> s
I <sup>2</sup> t	I²t capacity for fusing t <sub>p</sub> =10ms, V <sub>rm</sub> ≤10V, (note 5)	510×10 <sup>3</sup>	A <sup>2</sup> s
(-1:/-14)	Critical rate of rise of on-state current (repetitive), (Note 6)	300	A/µs
(di/dt) <sub>cr</sub>	Critical rate of rise of on-state current (non-repetitive), (Note 6)	600	A/µs
$V_{RGM}$	Peak reverse gate voltage	10	V
$P_{G(AV)}$	Mean forward gate power	4	W
$P_GM$	Peak forward gate power	30	W
Ts	Operating temperature range	-40 to +125	°C
$T_{\text{stg}}$	Storage temperature range	-40 to +150	°C

#### Notes:-

- 1) De-rating factor of 0.13% per  $^{\circ}$ C is applicable for  $T_{j}$  below 25 $^{\circ}$ C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, 125°C T<sub>i</sub> initial.
- 6)  $V_D=67\% V_{DRM}$ ,  $I_{FG}=2A$ ,  $t_r \le 0.5 \mu s$ ,  $T_{case}=125^{\circ}C$ .



# **Characteristics**

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V <sub>TM</sub>	Maximum peak on-state voltage	-	-	2.07	I <sub>TM</sub> =1700A	V
$V_{T0}$	Threshold voltage	-	-	1.04		V
r <sub>T</sub>	Slope resistance	-	-	0.606		mΩ
(dv/dt) <sub>cr</sub>	Critical rate of rise of off-state voltage	1000	-	-	V <sub>D</sub> =80% V <sub>DRM</sub> , linear ramp, gate o/c	V/μs
I <sub>DRM</sub>	Peak off-state current	-	-	60	Rated V <sub>DRM</sub>	mA
I <sub>RRM</sub>	Peak reverse current	-	-	60	Rated V <sub>RRM</sub>	mA
$V_{GT}$	Gate trigger voltage	-	-	3.0	T-25°C	V
$I_{GT}$	Gate trigger current	-	-	300	$T_j$ =25°C $V_D$ =10V, $I_T$ =3A	mA
$V_{\sf GD}$	Gate non-trigger voltage	-	-	0.25	Rated V <sub>DRM</sub>	V
I <sub>H</sub>	Holding current	-	-	1000	T <sub>j</sub> =25°C	mA
$t_{gd}$	Gate-controlled turn-on delay time		0.5	1.0	V <sub>D</sub> =67% V <sub>DRM</sub> , I <sub>T</sub> =1000A, di/dt=10A/µs,	μs
<b>t</b> gt	Turn-on time	-	1.0	2.0	I <sub>FG</sub> =2A, t <sub>r</sub> =0.5µs, T <sub>j</sub> =25°C	μs
Q <sub>rr</sub>	Recovered charge	-	2400	-		μC
Q <sub>ra</sub>	Recovered charge, 50% Chord	-	1250	1500	I <sub>TM</sub> =1000A, t <sub>p</sub> =1000μs, di/dt=10A/μs,	μC
Irr	Reverse recovery current	-	100	-	V <sub>r</sub> =50V	Α
t <sub>rr</sub>	Reverse recovery time	-	25.0	-		μs
+	Turn-off time	-	600	-	I <sub>TM</sub> =1000A, t <sub>p</sub> =1000μs, di/dt=10A/μs, V <sub>r</sub> =50V, V <sub>dr</sub> =80%V <sub>DRM</sub> , dV <sub>dr</sub> /dt=20V/μs	116
t <sub>q</sub>	Turr-on time	-	1000	-	$I_{TM}$ =1000A, $t_p$ =1000 $\mu$ s, $di/dt$ =10A/ $\mu$ s, $V_r$ =50V, $V_{dr}$ =80% $V_{DRM}$ , $dV_{dr}/dt$ =200V/ $\mu$ s	μs
D	Thermal registance junction to be stainly	-	-	0.032	Double side cooled	K/W
$R_{thJK}$	Thermal resistance, junction to heatsink	-	-	0.064	Single side cooled	K/W
F	Mounting force	10	-	20		kN
$W_t$	Weight	-	340	-		g

# Notes:-

1) Unless otherwise indicated  $T_j$ =125°C.



#### **Notes on Ratings and Characteristics**

#### 1.0 Voltage Grade Table

Voltage Grade	V <sub>DRM</sub> V <sub>DSM</sub> V <sub>RRM</sub> V	V <sub>RSM</sub> V	V <sub>D</sub> V <sub>R</sub> DC V
26	2600	2700	1560
28	2800	2900	1680

#### 2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

# 3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T<sub>i</sub> below 25°C.

#### 4.0 Repetitive dv/dt

Standard dv/dt is 1000V/µs.

### 5.0 Snubber Components

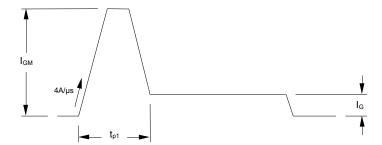
When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

#### 6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 600A/µs at any time during turnon on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 300A/µs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

#### 7.0 Gate Drive

The nominal requirement for a typical gate drive is illustrated below. An open circuit voltage of at least 30V is assumed. This gate drive must be applied when using the full di/dt capability of the device.



The magnitude of  $I_{GM}$  should be between five and ten times  $I_{GT}$ , which is shown on page 2. Its duration  $(t_{p1})$  should be  $20\mu s$  or sufficient to allow the anode current to reach ten times  $I_L$ , whichever is greater. Otherwise, an increase in pulse current could be needed to supply the necessary charge to trigger. The 'back-porch' current  $I_G$  should remain flowing for the same duration as the anode current and have a magnitude in the order of 1.5 times  $I_{GT}$ .



#### 8.0 Computer Modelling Parameters

#### 8.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{{V_{T0}}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \qquad \text{and:} \qquad W_{AV} = \frac{\Delta T}{R_{th}} \\ \Delta T = T_{j \max} - T_{Hs}$$

Where  $V_{T0}$ =1.04V,  $r_T$ =0.606m $\Omega$ ,

 $R_{th}$  = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave Double Side Cooled	0.048	0.0436	0.0413	0.0388	0.036	0.0345	0.032
Square wave Single Side Cooled	0.079	0.0769	0.074	0.0716	0.0688	0.0665	0.064
Sine wave Double Side Cooled	0.0415	0.0394	0.0378	0.0355	0.032		
Sine wave Single Side Cooled	0.0735	0.0718	0.07	0.0679	0.064		

Form Factors							
Conduction Angle	30°	60°	90°	120°	180°	270°	d.c.
Square wave	3.464	2.449	2	1.732	1.414	1.149	1
Sine wave	3.98	2.778	2.22	1.879	1.57		

# 8.2 Calculating V<sub>T</sub> using ABCD Coefficients

The on-state characteristic I<sub>T</sub> vs. V<sub>T</sub>, on page 6 is represented in two ways;

- (i) the well established V<sub>T0</sub> and r<sub>T</sub> tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V<sub>T</sub> in terms of I<sub>T</sub> given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for  $V_T$  agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients		125°C Coefficients
Α	0.908566	Α	0.417877
В	0.02200912	В	0.1200233
С	3.661922x10 <sup>-4</sup>	С	6.308007x10 <sup>-4</sup>
D	5.349066×10 <sup>-3</sup>	D	-7.297986×10 <sup>-3</sup>



# 8.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}}\right)$$

Where p = 1 to n, n is the number of terms in the series and:

t = Duration of heating pulse in seconds.

r, = Thermal resistance at time t.

 $r_p$  = Amplitude of  $p_{th}$  term.

 $\tau_p$  = Time Constant of  $r_{th}$  term.

The coefficients for this device are shown in the tables below:

	D.C. Double Side Cooled							
Term	1 2 3 4							
<b>r</b> p	0.01771901	4.240625×10 <sup>-3</sup>	6.963806×10 <sup>-3</sup>	3.043661×10 <sup>-3</sup>				
$ au_{\mathcal{P}}$	0.7085781	0.1435833	0.03615196	2.130842×10 <sup>-3</sup>				

	D.C. Single Side Cooled						
Term	Term 1 2 3 4 5						
$r_p$	0.03947164	0.01022837	8.789912×10 <sup>-3</sup>	4.235162×10 <sup>-3</sup>	1.907609×10 <sup>-3</sup>		
$ au_{\mathcal{P}}$	4.090062	1.078983	0.08530917	0.01128791	1.240861×10 <sup>-3</sup>		

# 9.0 Reverse recovery ratings

(i) Qra is based on 50% Irm chord as shown in Fig. 1

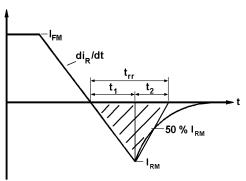


Fig. 1

$$Q_{rr} = \int_{0}^{150 \, \mu s} i_{rr}.dt$$

(iii) 
$$K Factor = \frac{t1}{t2}$$



#### **Curves**

Figure 1 - On-state characteristics of Limit device

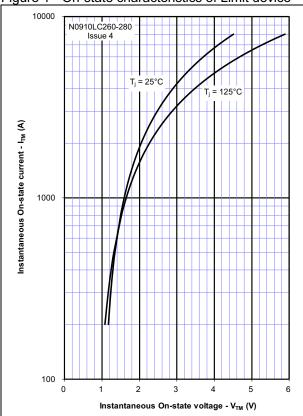


Figure 2 - Transient thermal impedance

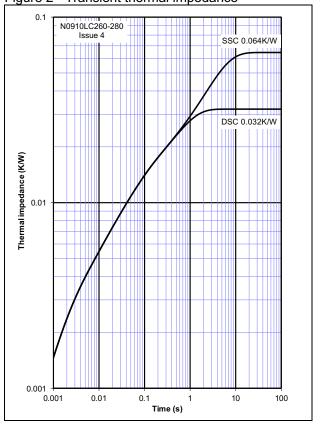


Figure 3 - Gate characteristics - Trigger limits

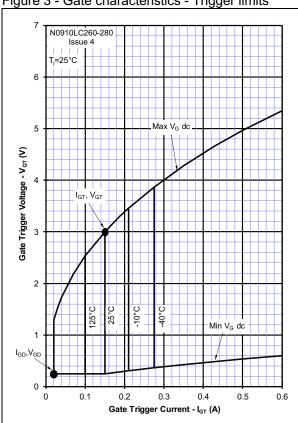


Figure 4 - Gate characteristics - Power curves

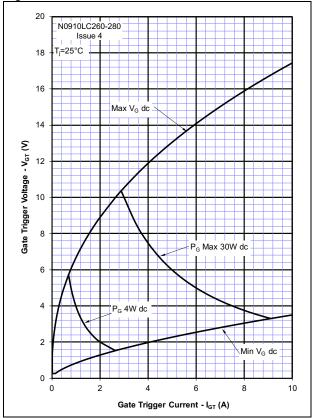




Figure 5 - Total recovered charge, Q<sub>rr</sub>

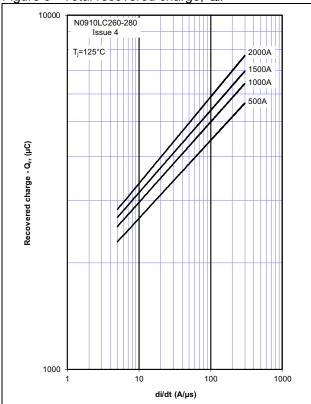


Figure 6 - Recovered charge, Qra (50% chord)

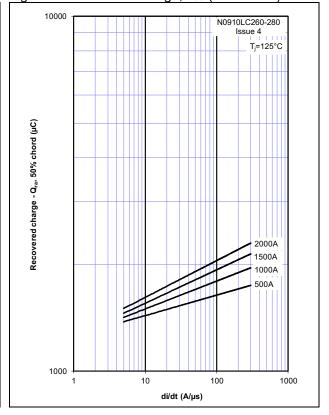


Figure 7 - Peak reverse recovery current, Irm

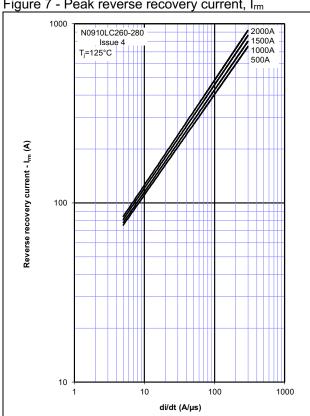


Figure 8 - Maximum recovery time, t<sub>rr</sub> (50% chord)

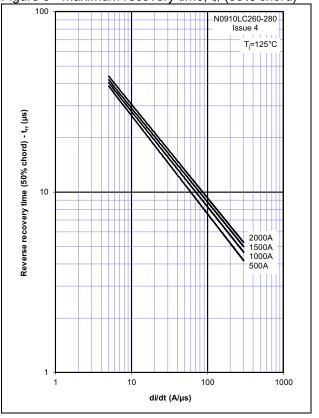




Figure 9 – On-state current vs. Power dissipation – Double Side Cooled (Sine wave)

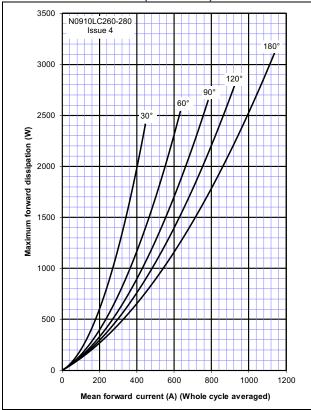


Figure 11 – On-state current vs. Power dissipation – Double Side Cooled (Square wave)

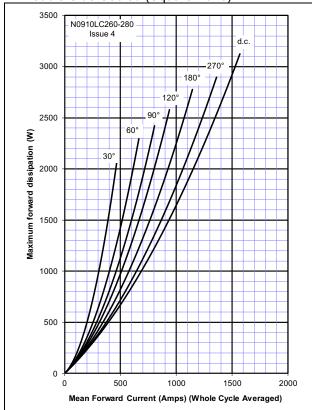


Figure 10 – On-state current vs. Heatsink temperature - Double Side Cooled (Sine wave)

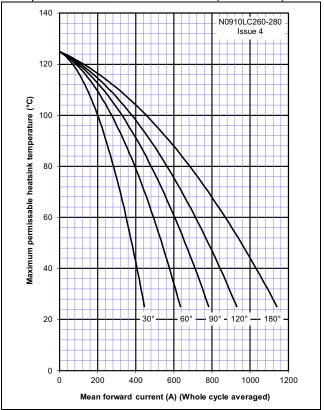


Figure 12 – On-state current vs. Heatsink temperature – Double Side Cooled (Square wave)

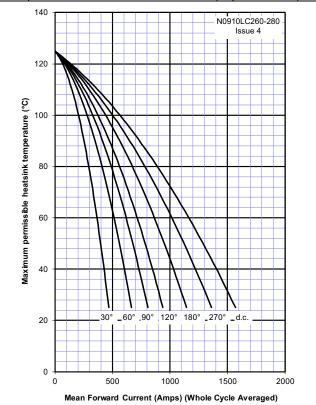




Figure 13 – On-state current vs. Power dissipation – Single Side Cooled (Sine wave)

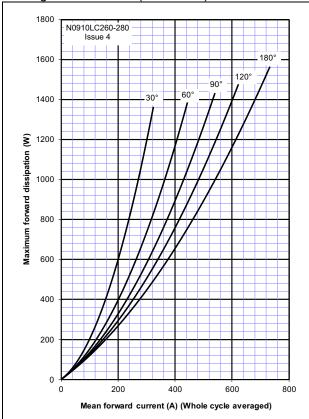


Figure 15 – On-state current vs. Power dissipation – Single Side Cooled (Square wave)

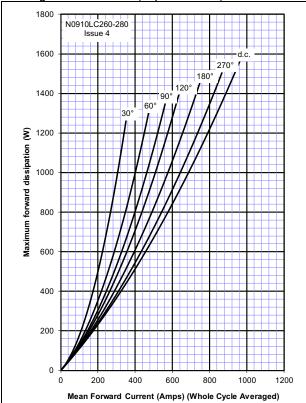


Figure 14 – On-state current vs. Heatsink temperature – Single Side Cooled (Sine wave)

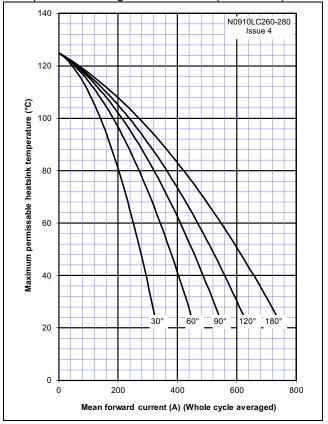
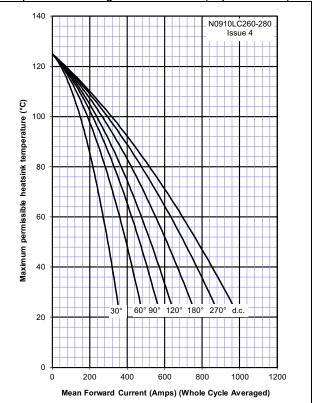
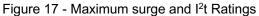
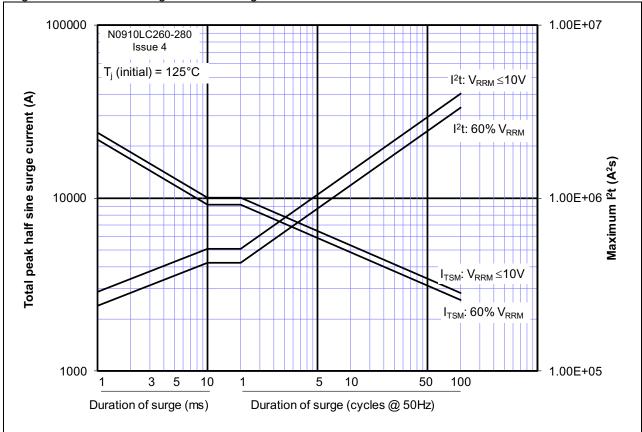


Figure 16 – On-state current vs. Heatsink temperature – Single Side Cooled (Square wave)



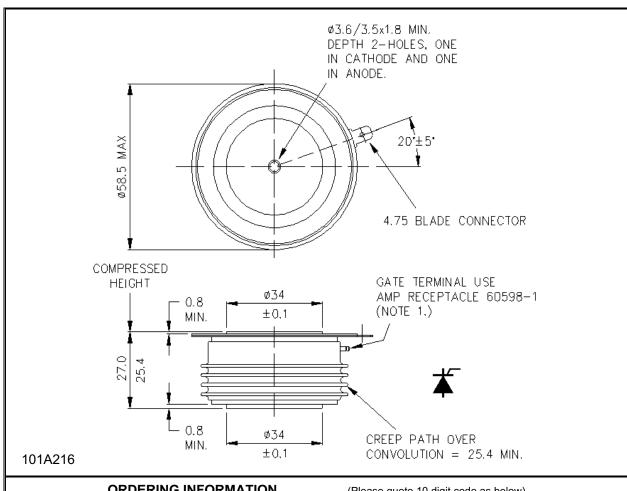








#### **Outline Drawing & Ordering Information**



ORDERI	NG INFORMATION	(Please quote 10 digit code as below)		
N0910	LC	<b>**</b>	0	
Fixed Type Code	Fixed outline code	Voltage code V <sub>DRM</sub> /100 26-28	Fixed code	

Order code: N0910LC280 – 2800V V<sub>DRM</sub>, V<sub>RRM</sub>, 27mm clamp height capsule.

#### IXYS Semiconductor GmbH

Edisonstraße 15 D-68623 Lampertheim Tel: +49 6206 503-0 Fax: +49 6206 503-627 E-mail: marcom@ixys.de



#### IXYS UK Westcode Ltd

Langley Park Way, Langley Park, Chippenham, Wiltshire, SN15 1GE. Tel: +44 (0)1249 444524 E-mail: sales@ixysuk.com

#### **IXYS** Corporation

1590 Buckeye Drive Milpitas CA 95035-7418 Tel: +1 (408) 457 9000 Fax: +1 (408) 496 0670 E-mail: sales@ixys.net www.littelfuse.com www.ixysuk.com

www.ixys.com

# IXYS Long Beach

IXYS Long Beach, Inc 2500 Mira Mar Ave, Long Beach CA 90815

Tel: +1 (562) 296 6584 Fax: +1 (562) 296 6585 E-mail: service@ixyslongbeach.com

The information contained herein is confidential and is protected by Copyright. The information may not be used or disclosed except with the written permission of and in the manner permitted by the proprietors IXYS UK Westcode Ltd.

© IXYS UK Westcode Ltd.

In the interest of product improvement, IXYS UK Westcode Ltd reserves the right to change specifications at any time without prior notice.

Devices with a suffix code (2-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.



# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for SCRs category:

Click to view products by IXYS manufacturer:

Other Similar products are found below:

NTE5428 T1500N16TOF VT TT162N16KOF-A TT162N16KOF-K TT330N16AOF VS-22RIA20 VS-2N685 057219R T1190N16TOF VT T1220N22TOF VT T201N70TOH T700N22TOF T830N18TOF TT250N12KOF-K VS-16RIA120 VS-110RKI40 NTE5427 NTE5442 T2160N28TOF VT TT251N16KOF-K VS-22RIA100 VS-16RIA40 TD250N16KOF-A VS-ST110S16P0 T930N36TOF VT T2160N24TOF VT T1190N18TOF VT T1590N28TOF VT 2N1776A T590N14TOF NTE5375 NTE5460 NTE5481 NTE5512 NTE5514 NTE5518 NTE5519 NTE5529 NTE5553 NTE5557 NTE5557 NTE5567 NTE5570 NTE5570 NTE5574 NTE5576 NTE5578 NTE5579 NTE5589 NTE5592