

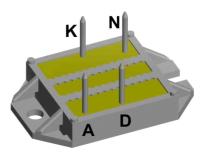
## **Standard Rectifier Module**

1~ Rectifier			
$V_{\text{RRM}}$	=	1600	
IDAV	=	55	
I <sub>FSM</sub>	=	300	

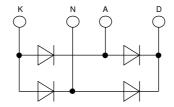
## 1~ Rectifier Bridge

#### Part number

### VBO54-16NO7







#### Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

#### **Applications:**

- Diode for main rectification
- For one phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: ECO-PAC1

- Isolation Voltage: 3000 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Height: 9 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

#### Terms \_Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747 and per semiconductor unless otherwise specified

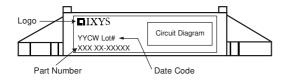
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Rectifier				Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V <sub>RSM</sub>	max. non-repetitive reverse bloc	cking voltage	$T_{VJ} = 25^{\circ}C$			1700	V
V <sub>RRM</sub>	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1600	V
I <sub>R</sub>	reverse current	V <sub>R</sub> = 1600 V	$T_{VJ} = 25^{\circ}C$			40	μΑ
		$V_R = 1600 V$	$T_{VJ} = 150$ °C			1.5	mΑ
V <sub>F</sub>	forward voltage drop	I <sub>F</sub> = 20 A	$T_{VJ} = 25^{\circ}C$			1.15	٧
		$I_F = 40 \text{ A}$				1.34	٧
		I <sub>F</sub> = 20 A	T <sub>VJ</sub> = 125°C			1.12	٧
		$I_F = 40 \text{ A}$				1.32	٧
IDAV	bridge output current	T <sub>c</sub> =105°C	T <sub>VJ</sub> = 150°C			55	Α
		rectangular d = 0.5					
V <sub>F0</sub>	threshold voltage		T <sub>VJ</sub> = 150°C			0.82	٧
r <sub>F</sub>	slope resistance	loss calculation only				12.2	mΩ
R <sub>thJC</sub>	thermal resistance junction to ca	ase				1.1	K/W
R <sub>thCH</sub>	thermal resistance case to heat	sink			0.4		K/W
P <sub>tot</sub>	total power dissipation		$T_{C} = 25^{\circ}C$			110	W
I <sub>FSM</sub>	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			300	Α
		t = 8.3  ms; (60 Hz), sine	$V_R = 0 V$			325	Α
		t = 10 ms; (50 Hz), sine	T <sub>VJ</sub> = 150°C			255	Α
		t = 8.3  ms; (60 Hz), sine	$V_R = 0 V$			275	Α
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			450	A <sup>2</sup> s
		t = 8,3  ms; (60 Hz), sine	$V_R = 0 V$			440	A²s
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150$ °C			325	A <sup>2</sup> s
		t = 8,3 ms; (60 Hz), sine	$V_R = 0 V$			315	A²s
C	junction capacitance	$V_{R} = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^{\circ}C$		10		pF
				+	l	l	<del></del>



Package ECO-PAC1					Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit	
I <sub>RMS</sub>	RMS current	per terminal				100	Α	
T <sub>VJ</sub>	virtual junction temperature			-40		150	°C	
T <sub>op</sub>	operation temperature			-40		125	°C	
T <sub>stg</sub>	storage temperature			-40		125	°C	
Weight					19		g	
M <sub>D</sub>	mounting torque			1.4		2	Nm	
d <sub>Spp/App</sub>	creepage distance on surface   striking distance through air		terminal to terminal	6.0			mm	
$d_{\text{Spb/Apb}}$	creepage distance on surfac	e   striking distance through an	terminal to backside	10.0			mm	
V <sub>ISOL</sub>	icolation voltage	t = 1 second	50/60 Hz. RMS: IIsoi ≤ 1 mA	3000			٧	
		t = 1 minute		2500			٧	

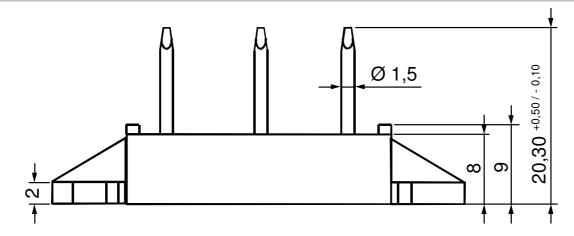


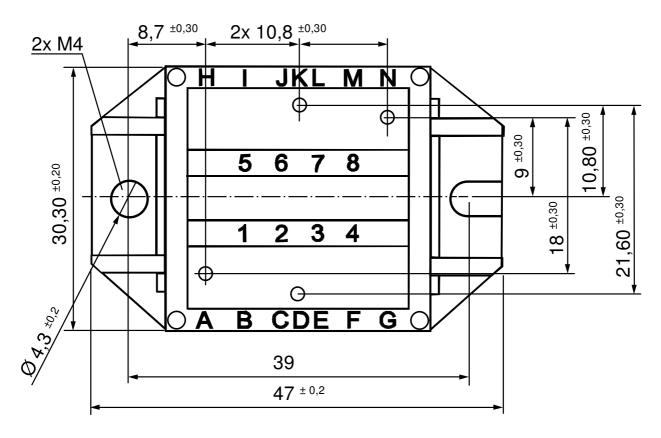
	Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Ī	Standard	VBO54-16NO7	VBO54-16NO7	Box	25	479551

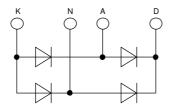
Equiva	alent Circuits for	Simulation	* on die level	$T_{VJ} = 150 ^{\circ}\text{C}$
$I \rightarrow V_0$	$R_0$	Rectifier		
V <sub>0 max</sub>	threshold voltage	0.82		V
$R_{0 \text{ max}}$	slope resistance *	11		$m\Omega$



#### **Outlines ECO-PAC1**









### Rectifier

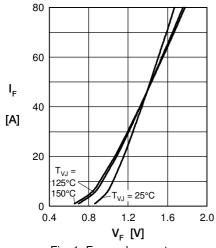


Fig. 1 Forward current versus voltage drop per diode

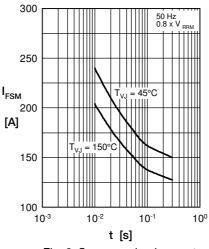


Fig. 2 Surge overload current

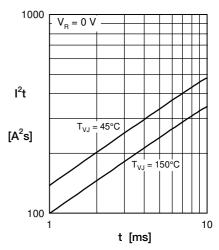


Fig. 3 I<sup>2</sup>t versus time per diode

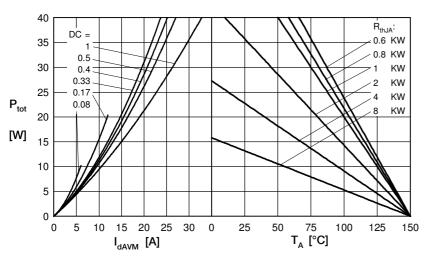


Fig. 4 Power dissipation vs. direct output current & ambient temperature

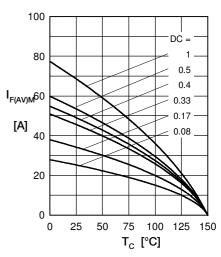


Fig. 5 Max. forward current vs. case temperature

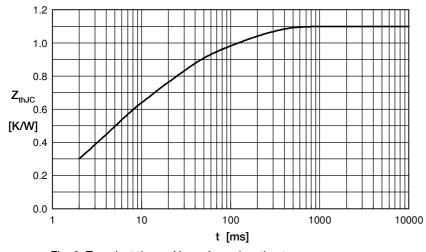


Fig. 6 Transient thermal impedance junction to case

Constants for  $\mathbf{Z}_{\text{thJC}}$  calculation:

İ	$R_{th}$ (K/W)	t <sub>i</sub> (s)
1	0.05070	0.004
2	0.163	0.0025
3	0.2805	0.0035
4	0.363	0.02
5	0.2228	0.15

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