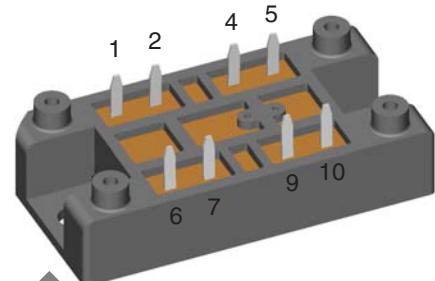
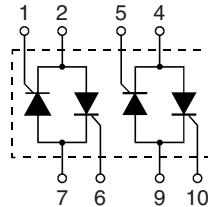


# AC Controller Modules

**I<sub>RMS</sub>** = 2x 45 A  
**V<sub>RRM</sub>** = 1200-1600 V

V <sub>RSM</sub> V <sub>DSM</sub>	V <sub>RRM</sub> V <sub>DRM</sub>	Type
V	V	
1200	1200	VW2x45-12io1
1400	1400	VW2x45-14io1
1600	1600	VW2x45-16io1



Symbol	Conditions	Maximum Ratings	
I <sub>RMS</sub>	T <sub>C</sub> = 85°C; (per phase)	45	A
I <sub>TRMS</sub>	T <sub>VJ</sub> = T <sub>VJM</sub>	32	A
I <sub>TAVM</sub>	T <sub>C</sub> = 85°C; (180° sine ; per thyristor)	20	A
I <sub>TSM</sub>	T <sub>VJ</sub> = 45°C V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	300 A 320 A
	T <sub>VJ</sub> = T <sub>VJM</sub> V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	270 A 290 A
I <sup>2</sup> t	T <sub>VJ</sub> = 45°C V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	450 A <sup>2</sup> s 430 A <sup>2</sup> s
	T <sub>VJ</sub> = T <sub>VJM</sub> V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	360 A <sup>2</sup> s 350 A <sup>2</sup> s
(di/dt) <sub>cr</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> f = 50 Hz, t <sub>p</sub> = 200 µs V <sub>D</sub> = 2/3 V <sub>DRM</sub> I <sub>G</sub> = 0.45 A di <sub>G</sub> /dt = 0.45 A/µs	repetitive, I <sub>T</sub> = 45 A	100 A/µs
		non repetitive, I <sub>T</sub> = I <sub>TAVM</sub>	500 A/µs
(dv/dt) <sub>cr</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> R <sub>GR</sub> = ∞; method 1 (linear voltage rise)	V <sub>DR</sub> = 2/3 V <sub>DRM</sub>	1000 V/µs
P <sub>GM</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> I <sub>T</sub> = I <sub>TAVM</sub>	t <sub>p</sub> = 30 µs t <sub>p</sub> = 300 µs	10 W 5 W
P <sub>GAVM</sub>			0.5 W
V <sub>RGM</sub>			10 V
T <sub>VJ</sub>			-40...+125 °C
T <sub>VJM</sub>			125 °C
T <sub>stg</sub>			-40...+125 °C
V <sub>ISOL</sub>	50/60 Hz, RMS I <sub>ISOL</sub> ≤ 1 mA	t = 1 min t = 1 s	3000 V~ 3600 V~
M <sub>d</sub>	Mounting torque (M5)	2-2.5/18-22 Nm/lb.in.	
Weight	typ.	35 g	

Data according to IEC 60747 refer to a single thyristor/diode unless otherwise stated.

- Features**
- Thyristor controller for AC (circuit W2C acc. to IEC) for mains frequency
  - Soldering connections for PCB mounting
  - Isolation voltage 3600 V~
  - Planar passivated chips
  - UL applied

## Applications

- Switching and control of three phase AC circuits
- Softstart AC motor controller
- Solid state switches
- Light and temperature control

## Advantages

- Easy to mount with two screws
- Space and weight savings
- Improved temperature and power cycling

**Recommended replacement:**  
**VW2x60-12/14/16 io1**

Symbol	Conditions	Characteristic Values		
$I_D, I_R$	$T_{VJ} = T_{VJM}; V_R = V_{RRM}; V_D = V_{DRM}$	$\leq$	5	mA
$V_T$	$I_T = 45 \text{ A}; T_{VJ} = 25^\circ\text{C}$	$\leq$	1.52	V
$V_{TO}$	For power-loss calculations only	0.85	V	
$r_T$		15	mΩ	
$V_{GT}$	$V_D = 6 \text{ V}$	$T_{VJ} = 25^\circ\text{C}$	$\leq$	1.5 V
		$T_{VJ} = -40^\circ\text{C}$	$\leq$	1.6 V
$I_{GT}$	$V_D = 6 \text{ V}$	$T_{VJ} = 25^\circ\text{C}$	$\leq$	100 mA
		$T_{VJ} = -40^\circ\text{C}$	$\leq$	200 mA
$V_{GD}$	$T_{VJ} = T_{VJM}$	$V_D = \frac{2}{3} V_{DRM}$	$\leq$	0.2 V
$I_{GD}$			$\leq$	5 mA
$I_L$	$T_{VJ} = 25^\circ\text{C}; t_p = 10 \mu\text{s}$	$\leq$	450	mA
	$I_G = 0.45 \text{ A}; di_G/dt = 0.45 \text{ A}/\mu\text{s}$			
$I_H$	$T_{VJ} = 25^\circ\text{C}; V_D = 6 \text{ V}; R_{GK} = \infty$	$\leq$	200	mA
$t_{gd}$	$T_{VJ} = 25^\circ\text{C}; V_D = \frac{1}{2} V_{DRM}$	$\leq$	2	μs
	$I_G = 0.45 \text{ A}; di_G/dt = 0.45 \text{ A}/\mu\text{s}$			
$t_q$	$T_{VJ} = T_{VJM}; I_T = 20 \text{ A}, t_p = 200 \mu\text{s}; di/dt = -10 \text{ A}/\mu\text{s}$	typ.	150	μs
	$V_R = 100 \text{ V}; dv/dt = 15 \text{ V}/\mu\text{s}; V_D = \frac{2}{3} V_{DRM}$			
$R_{thJC}$	per thyristor; DC		1.25	K/W
	per module		0.31	K/W
$R_{thJK}$	per thyristor; DC		1.55	K/W
	per module		0.39	K/W
$d_s$	Creeping distance on surface	12.7	mm	
$d_A$	Creepage distance in air	9.4	mm	
$a$	Max. allowable acceleration	50	m/s <sup>2</sup>	

Dimensions in mm (1 mm = 0.0394")

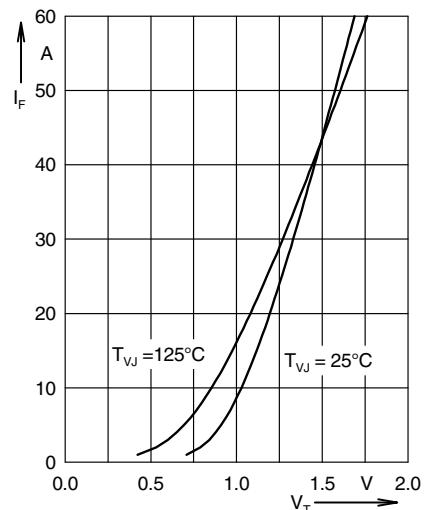
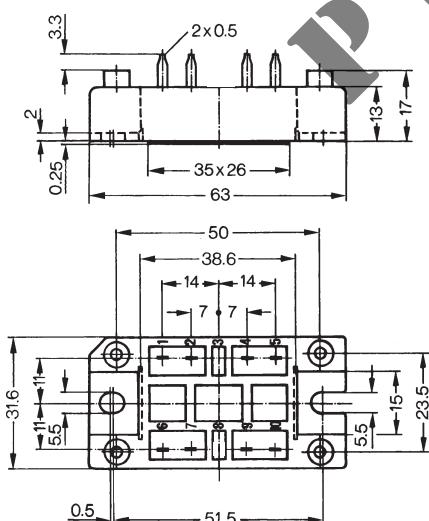


Fig. 3 Forward current vs. voltage drop per leg

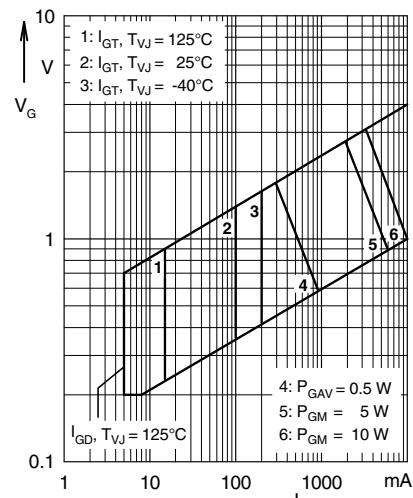


Fig. 1 Gate trigger characteristics

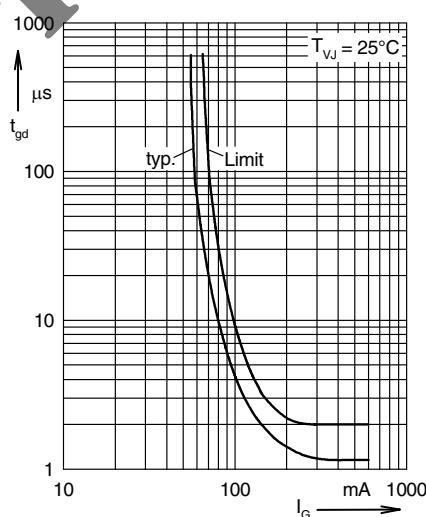


Fig. 2 Gate trigger delay time

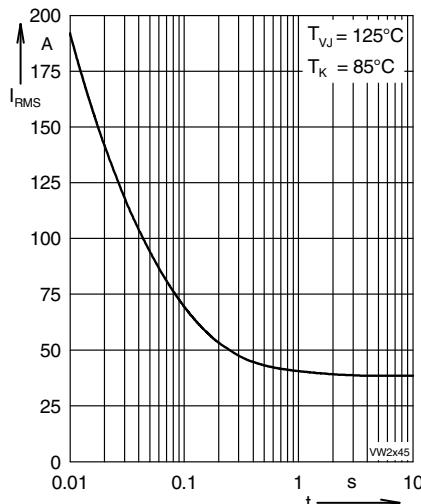


Fig. 4 Rated RMS current vs. time (360° conduction)

IXYS reserves the right to change limits, test conditions and dimensions

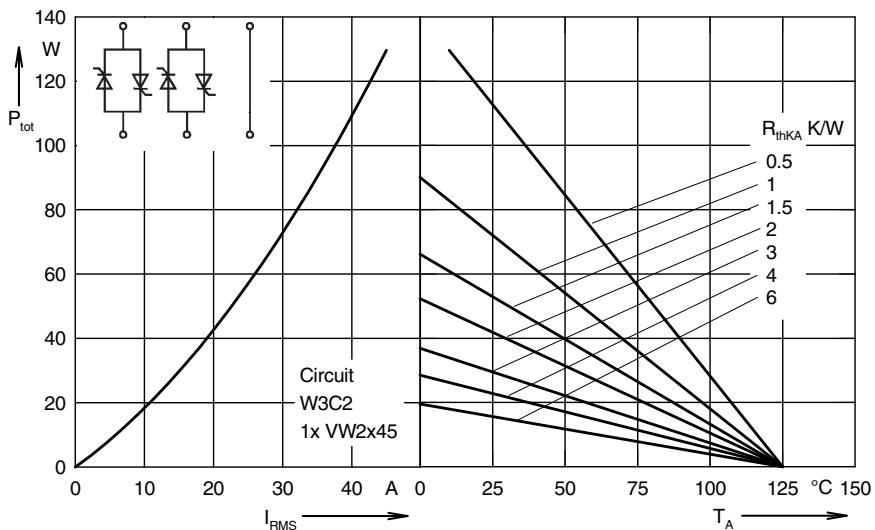


Fig. 5 Load current capability for two phase AC controller

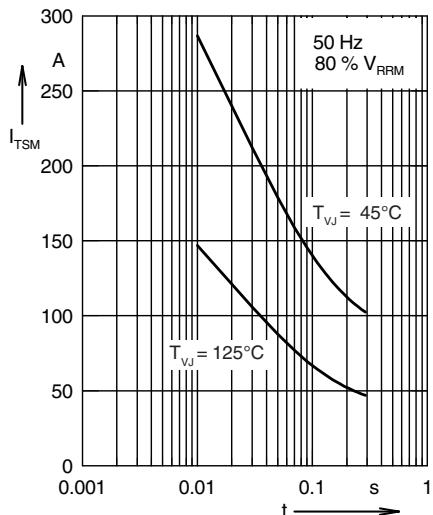


Fig. 6 Surge overload current

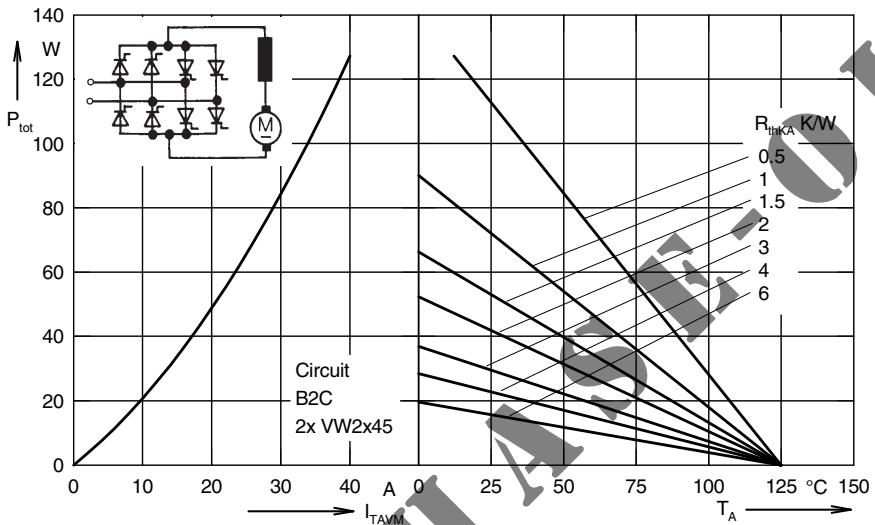


Fig. 7 Power dissipation vs. direct output current and ambient temperature cyclo converter, four quadrant operation

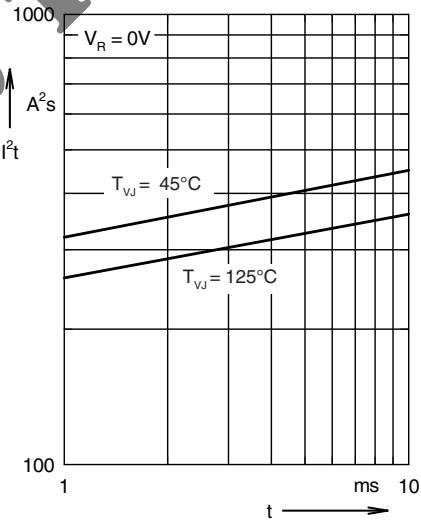


Fig. 8  $I^2t$  vs. time (per thyristor)

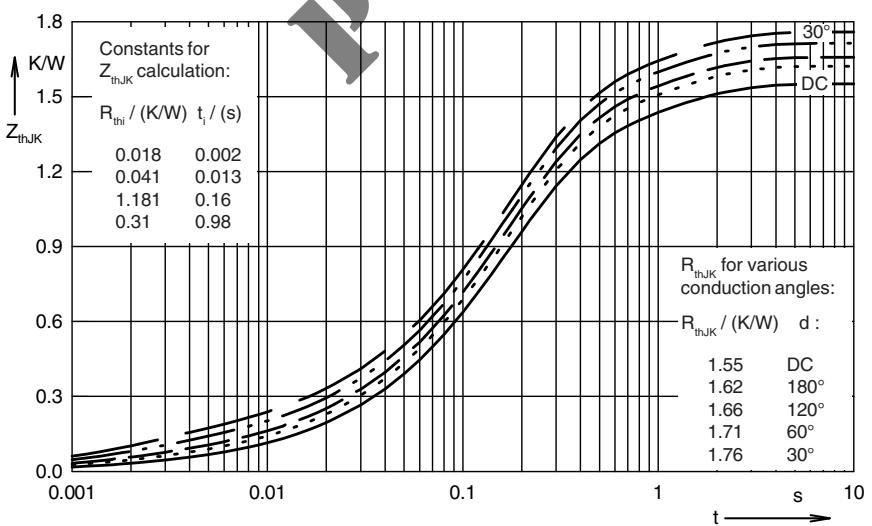


Fig. 9 Transient thermal impedance junction to heatsink (per thyristor)

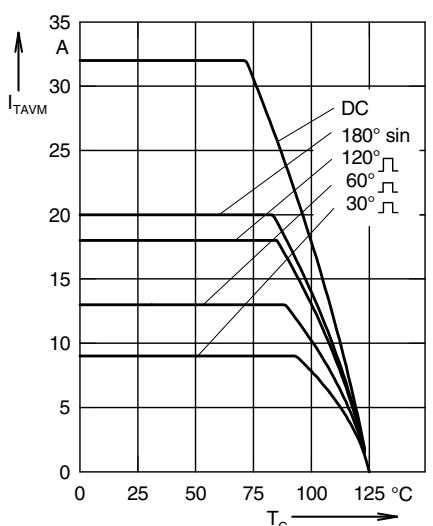


Fig. 10 Maximum forward current at case temperature  
20080828a

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[25.640.5053.0](#)