



## Description

### JMT N And P-Channel Enhancement Mode MOSFET

#### Features

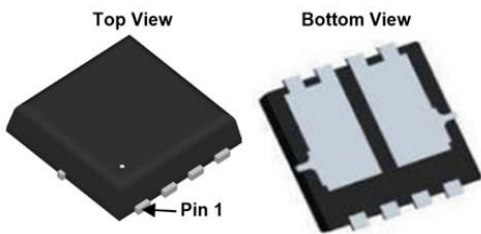
- N-Channel: 40V, 14A  
 $R_{DS(ON)} < 22m\Omega @ V_{GS} = -10V$   
 $R_{DS(ON)} < 28m\Omega @ V_{GS} = -4.5V$
- P-Channel: -40V, -14A  
 $R_{DS(ON)} < 50m\Omega @ V_{GS} = -10V$   
 $R_{DS(ON)} < 70m\Omega @ V_{GS} = -4.5V$
- Excellent Gate Charge x  $R_{DS(ON)}$  Product(FOM)
- Very Low On-resistance  $R_{DS(ON)}$
- Fast Switching Speed

#### Application

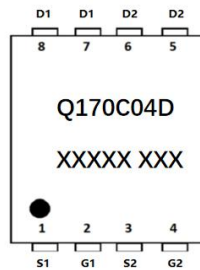
- Battery Protection
- Load Switch
- Power Management



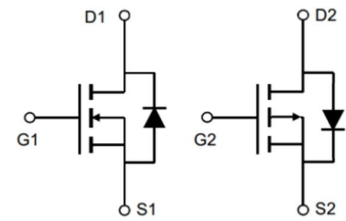
**100% UIS TESTED!**  
**100% ΔVds TESTED!**



PDFN3x3-8L-D



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
Q170C04D	JMTQ170C04D	TAPING	PDFN3x3-8L-D	13inch	5000	50000

## Absolute Maximum Ratings (T<sub>C</sub>=25°C unless otherwise specified)

Symbol	Parameter	Max. N-Channel	Max. P-Channel	Units
V <sub>DSS</sub>	Drain-Source Voltage	40	-40	V
V <sub>GSS</sub>	Gate-Source Voltage	±20	±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 25°C	-14	A
		T <sub>C</sub> = 100°C	-9	A
I <sub>DM</sub>	Pulsed Drain Current <sup>note1</sup>	56	-56	A
E <sub>AS</sub>	Single Pulsed Avalanche Energy <sup>note2</sup>	16	25	mJ
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	17	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	17	7.3	°C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150		°C



## N-Channel Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.5	2.5	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note3</small>	V <sub>GS</sub> =10V, I <sub>D</sub> =14A	-	16	22	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	20	28	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1.0MHz	-	980	-	pF
C <sub>oss</sub>	Output Capacitance		-	86.2	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	68.5	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =20V, I <sub>D</sub> =8A, V <sub>GS</sub> =10V	-	11	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	1.9	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge		-	2.2	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =20V, I <sub>D</sub> = 14A, R <sub>L</sub> =2.5Ω, R <sub>REN</sub> =3Ω	-	11	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	13	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	36	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	9	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	14	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	56	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> = 14A	-	-	1.2	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	T <sub>J</sub> =25°C,	-	19	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery	I <sub>F</sub> =14A, di/dt=100A/μs	-	11	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T<sub>J</sub>=25°C, V<sub>DD</sub>=20V, V<sub>GS</sub>=10V, L=0.5mH, R<sub>g</sub>=25Ω, I<sub>AS</sub>=8A

T<sub>J</sub>=25°C, V<sub>DD</sub>=-20V, V<sub>GS</sub>= -10V, L=0.5mH, R<sub>g</sub>=25Ω, I<sub>AS</sub>= -10A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%



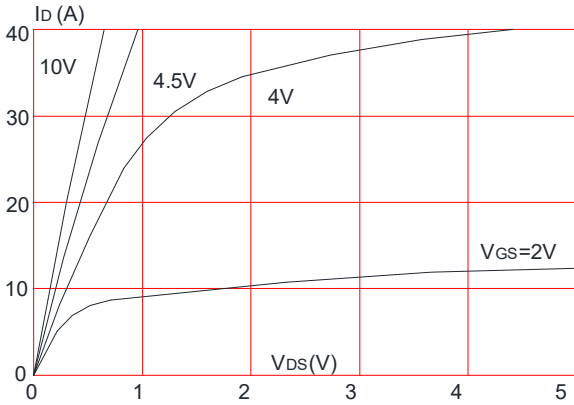
## P-Channel Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> = -250μA	-40	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = -40V, V <sub>GS</sub> =0V	-	-	-1	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = -250μA	-1.0	-1.6	-2.5	V
R <sub>DS(on)</sub>	Static Drain-Source on-Resistance <small>note3</small>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -5A	-	38	50	mΩ
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3A	-	50	70	
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = -20V, V <sub>GS</sub> =0V, f=1.0MHz	-	1034	-	pF
C <sub>oss</sub>	Output Capacitance		-	107	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	79.5	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = -20V, I <sub>D</sub> = -8A, V <sub>GS</sub> = -10V	-	20	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	3.5	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge		-	4.2	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> = -20V, I <sub>D</sub> = -14A, V <sub>GS</sub> = -10V, R <sub>GEN</sub> =2.5Ω	-	8	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	15	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	23	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	9	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	-14	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-56	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> = -14A	-	-	-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =0V, I <sub>S</sub> =-14A, di/dt=100A/μs	-	29	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	20	-	nC

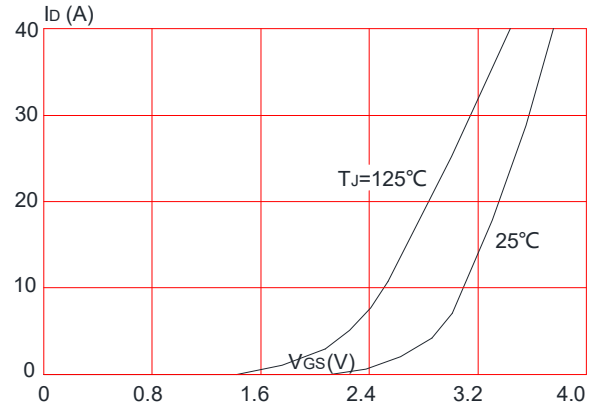


## Typical Performance Characteristics-N

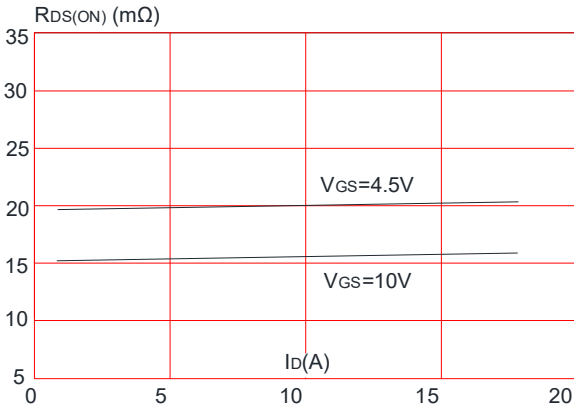
**Figure 1: Output Characteristics**



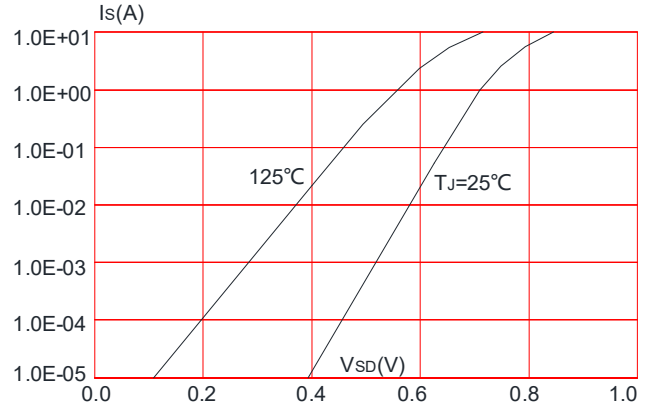
**Figure 2: Typical Transfer Characteristics**



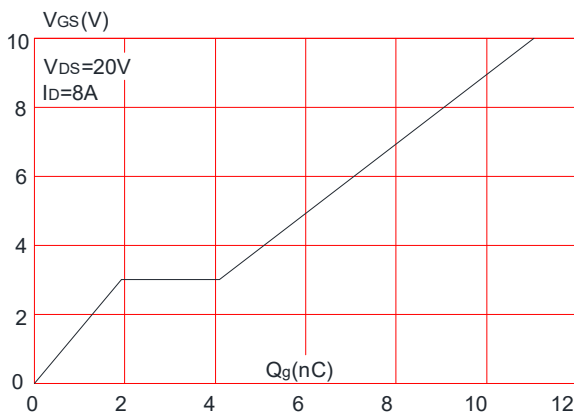
**Figure 3: On-resistance vs. Drain Current**



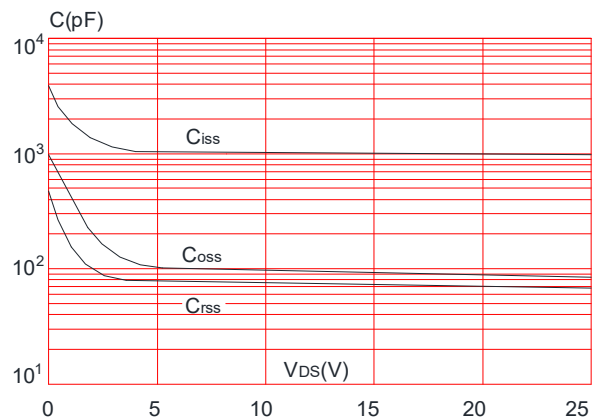
**Figure 4: Body Diode Characteristics**



**Figure 5: Gate Charge Characteristics**

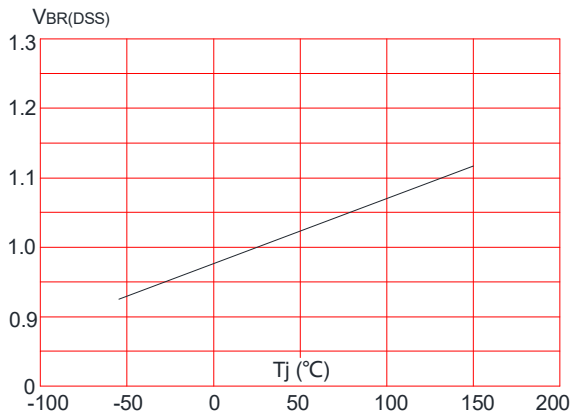


**Figure 6: Capacitance Characteristics**

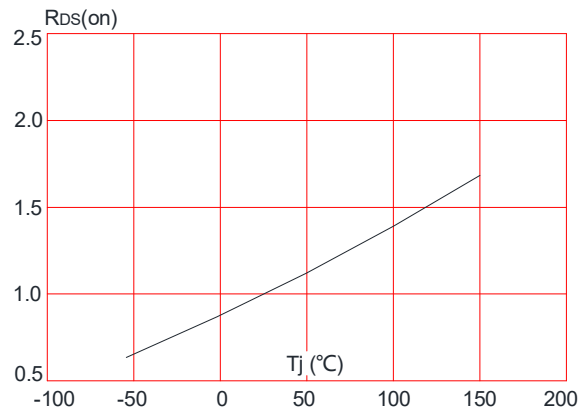




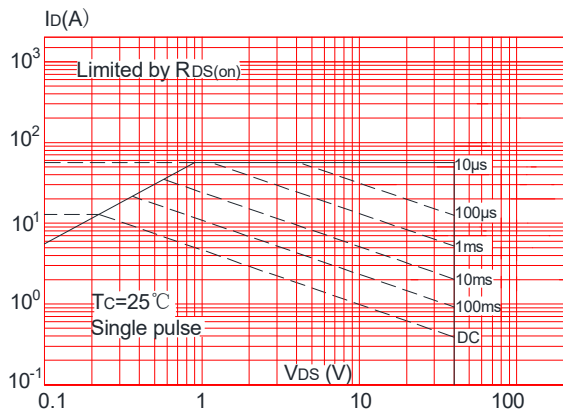
**Figure 7: Normalized Breakdown Voltage vs. Junction Temperature**



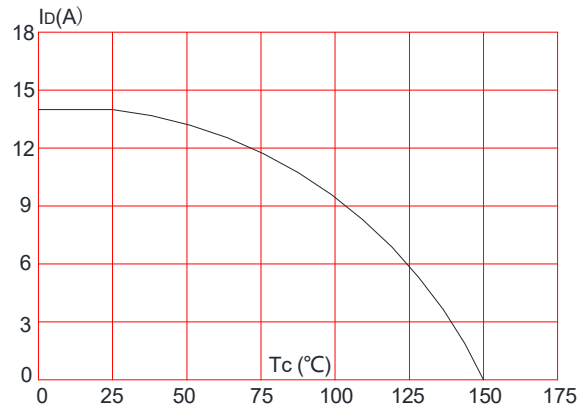
**Figure 8: Normalized on Resistance vs. Junction Temperature**



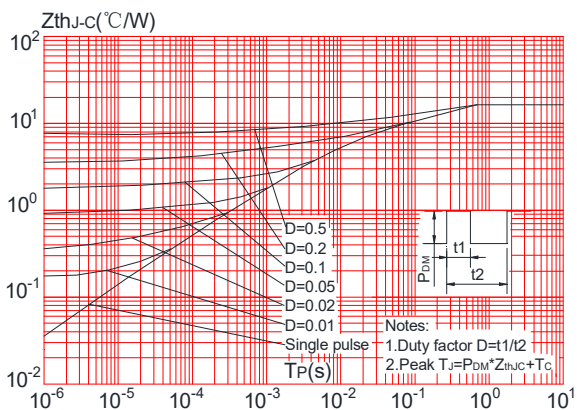
**Figure 9: Maximum Safe Operating Area**



**Figure 10: Maximum Continuous Drain Current vs. Case Temperature**



**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case**



## Test Circuit-N

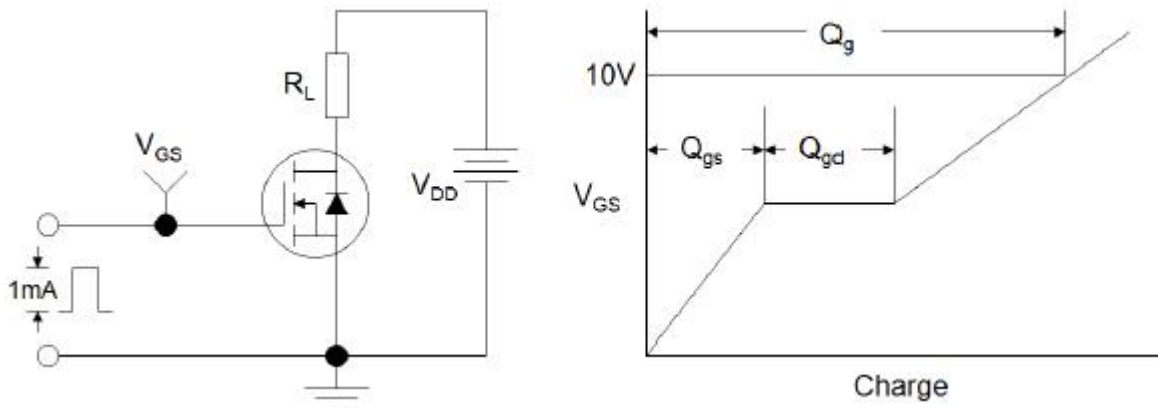


Figure1:Gate Charge Test Circuit & Waveform

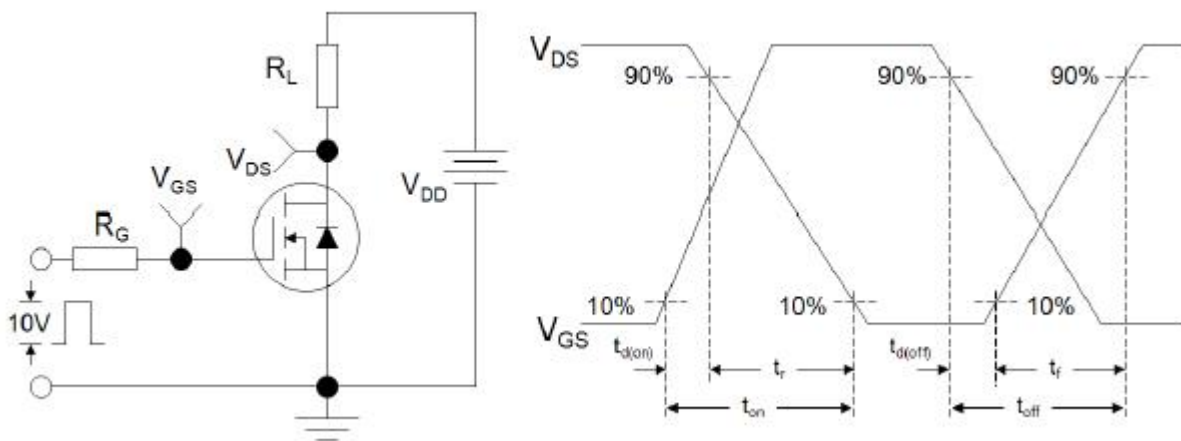


Figure 2: Resistive Switching Test Circuit & Waveforms

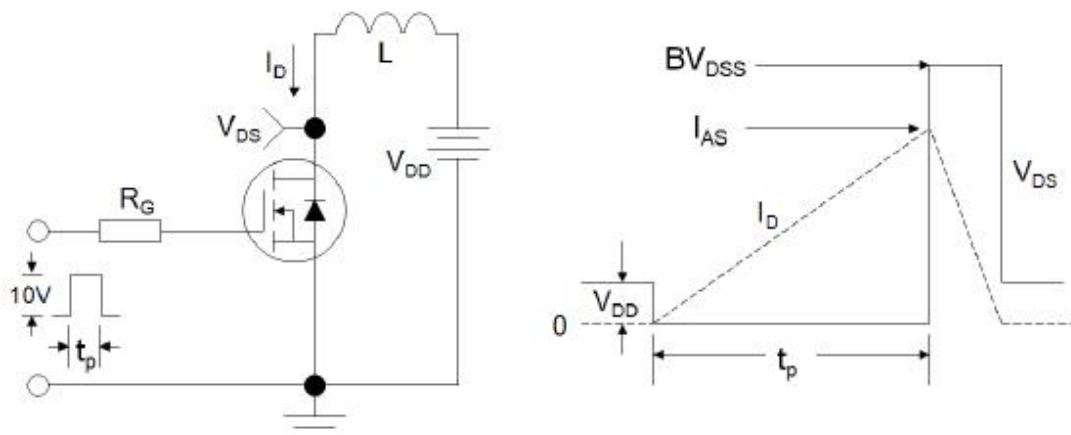
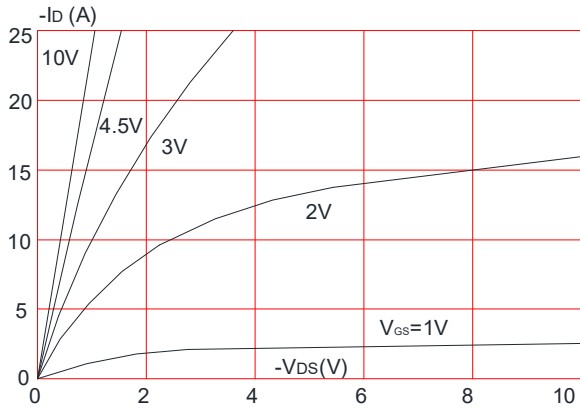


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

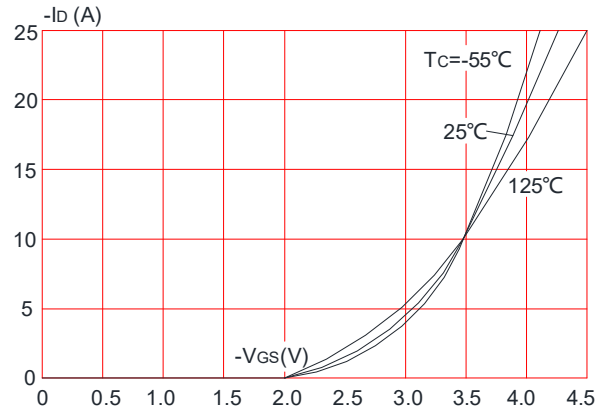


## Typical Performance Characteristics-P

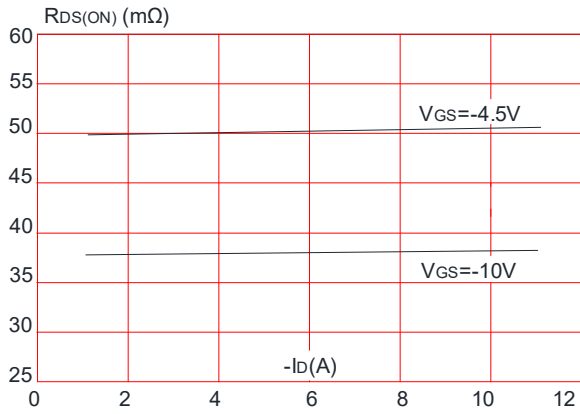
**Figure 1:** Output Characteristics



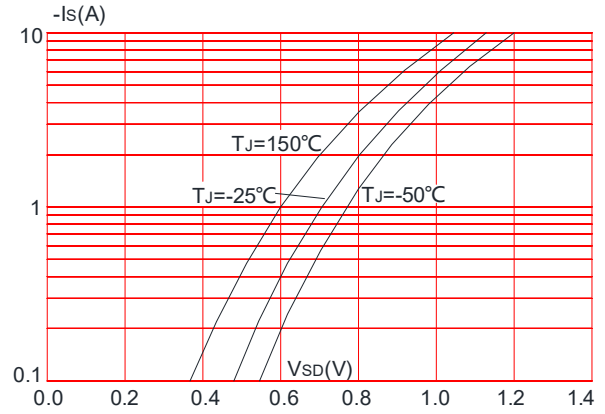
**Figure 2:** Typical Transfer Characteristics



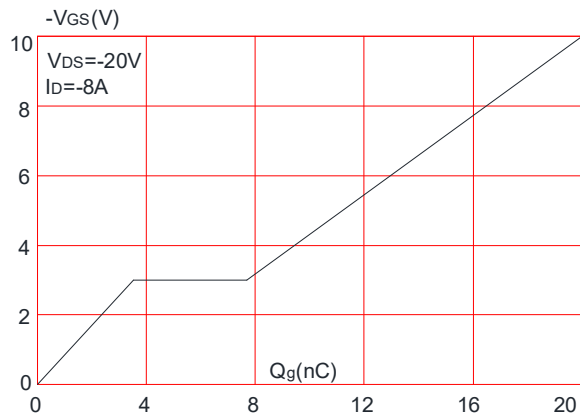
**Figure 3:** On-resistance vs. Drain Current



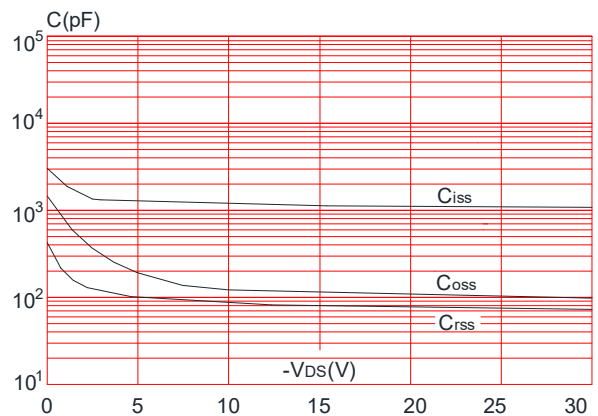
**Figure 4:** Body Diode Characteristics



**Figure 5:** Gate Charge Characteristics

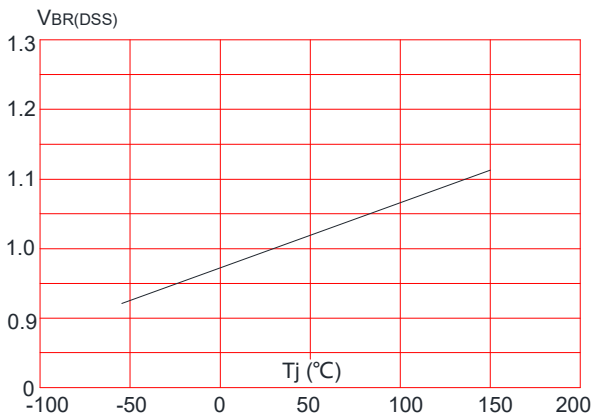


**Figure 6:** Capacitance Characteristics

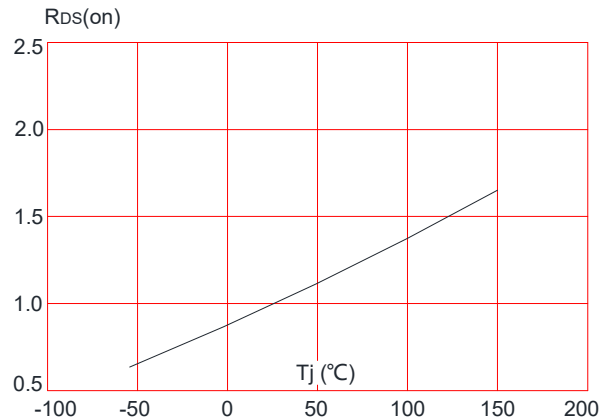




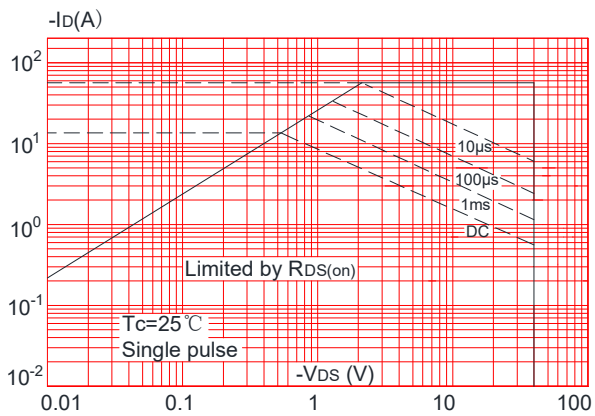
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



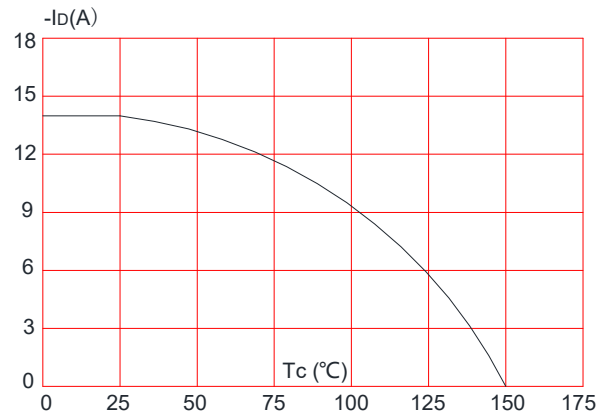
**Figure 8:** Normalized on Resistance vs. Junction Temperature



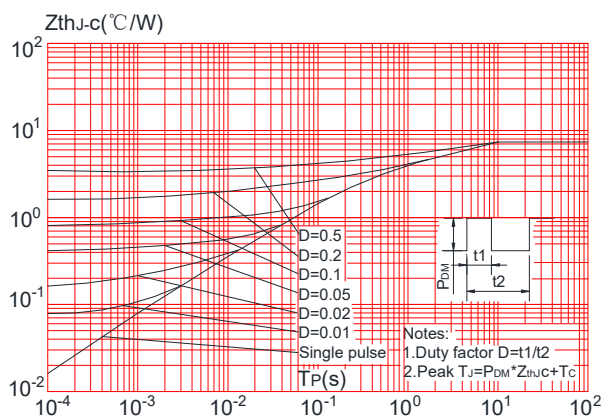
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



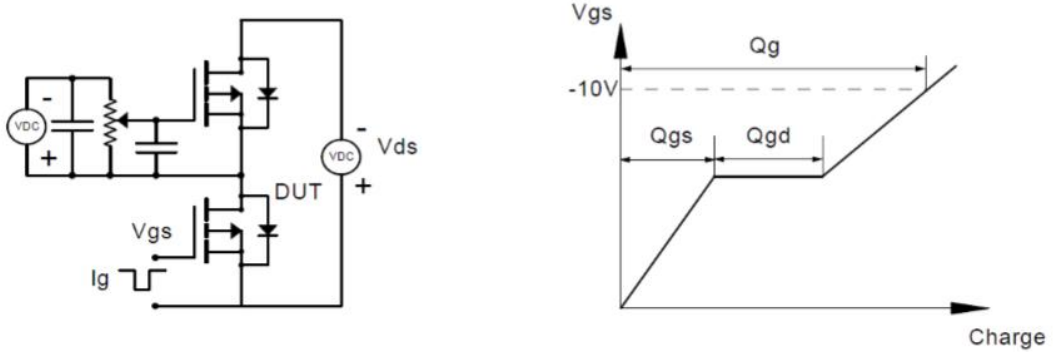
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



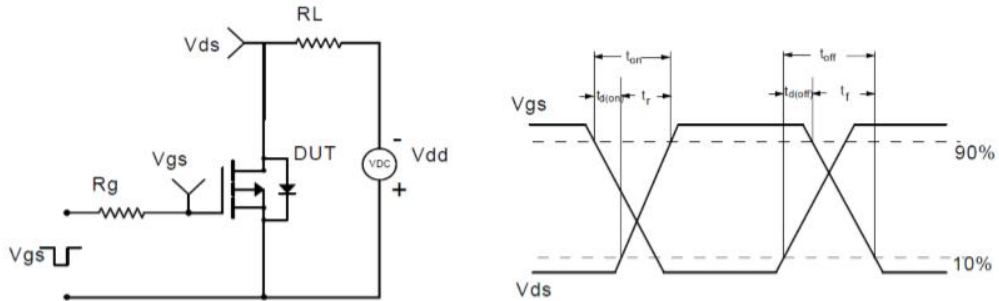


## Test Circuit-P

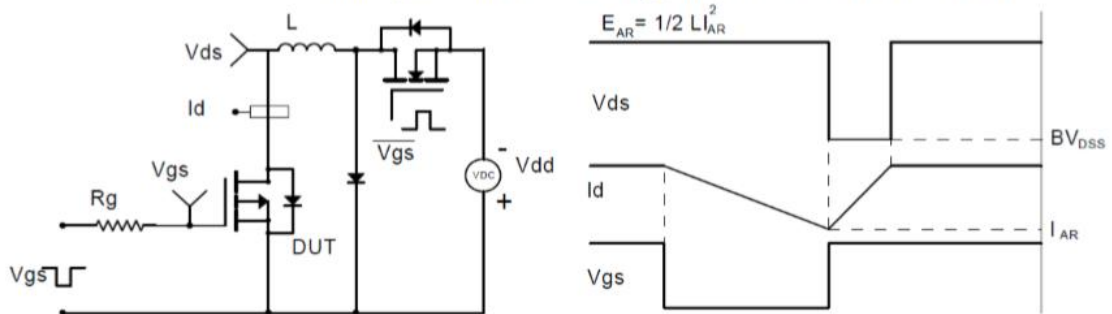
### Gate Charge Test Circuit & Waveform



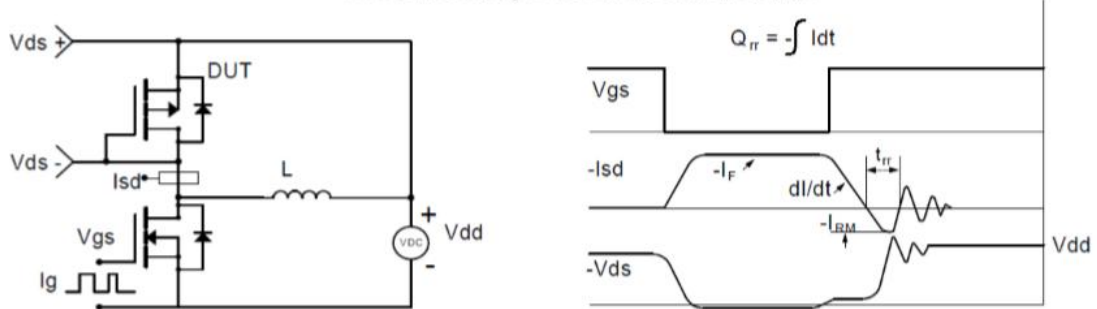
### Resistive Switching Test Circuit & Waveforms



### Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

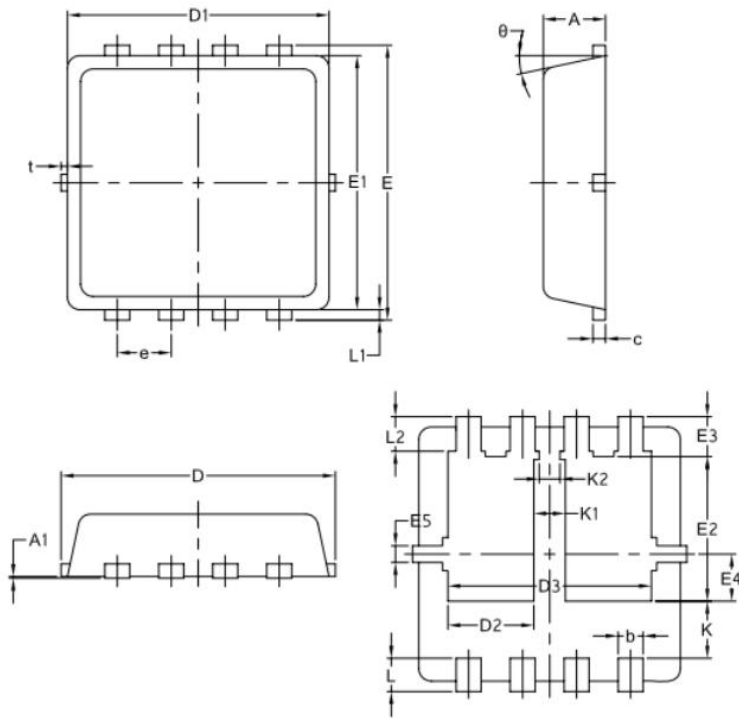


### Diode Recovery Test Circuit & Waveforms



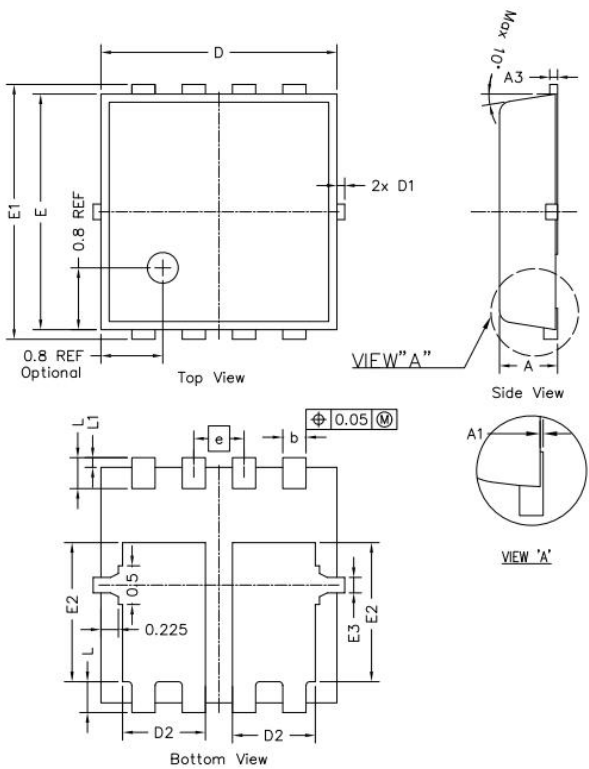


## Package Mechanical Data-PDFN3x3-8L-D-Type A



SYMBOL	COMMON		
	MM		
	MIN	NOM	MAX
A	0.70	0.75	0.85
A1	/	/	0.05
b	0.25	0.30	0.39
c	0.14	0.152	0.20
D	3.20	3.30	3.45
D1	3.05	3.15	3.25
D2	0.84	1.04	1.24
D3	2.30	2.45	2.60
E	3.20	3.30	3.40
E1	2.95	3.05	3.15
E2	1.60	1.74	1.90
E3	0.28	0.48	0.65
E4	0.37	0.57	0.77
E5	0.10	0.20	0.30
e	0.60	0.65	0.70
K	0.50	0.69	0.80
K1	0.30	0.38	0.53
K2	0.15	0.25	0.35
L	0.30	0.40	0.50
L1	0.06	0.125	0.20
L2	0.27	0.42	0.57
t	0	0.075	0.13
θ	10°	12°	14°

## Package Mechanical Data-PDFN3x3-8L-D-Type B




SYMBOLS	DIMENSION IN MM			DIMENSION IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.700	0.750	0.800	0.028	0.030	0.031
A1	---	---	0.050	----	----	0.002
A3	0.144	0.152	0.202	0.006	0.006	0.008
b	0.250	0.300	0.350	0.010	0.012	0.014
e	0.65 BSC			0.026 BSC		
D	2.950	3.050	3.150	0.116	0.120	0.124
E	2.950	3.050	3.150	0.116	0.120	0.124
D1	---	---	0.125	----	----	0.005
E1	3.200	3.300	3.400	0.126	0.130	0.134
D2	0.970	1.070	1.170	0.038	0.042	0.046
E2	1.700	1.800	1.900	0.067	0.071	0.075
E3	0.150	0.200	0.250	0.006	0.008	0.010
L	0.300	0.400	0.500	0.012	0.016	0.020
L1	0.075	0.125	0.175	0.003	0.005	0.007



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[DMN2990UFB-7B](#) [SSM3K35CT,L3F](#) [IPLK60R1K0PFD7ATMA1](#) [2N7002W-G](#) [MCAC30N06Y-TP](#) [IPWS65R035CFD7AXKSA1](#)  
[MCQ7328-TP](#) [SSM3J143TU,LXHF](#) [DMN12M3UCA6-7](#) [PJMF280N65E1\\_T0\\_00201](#) [PJMF380N65E1\\_T0\\_00201](#)  
[PJMF280N60E1\\_T0\\_00201](#) [PJMF600N65E1\\_T0\\_00201](#) [PJMF900N65E1\\_T0\\_00201](#)