



NM3400

5.8A, 30V N-CHANNEL MOSFET

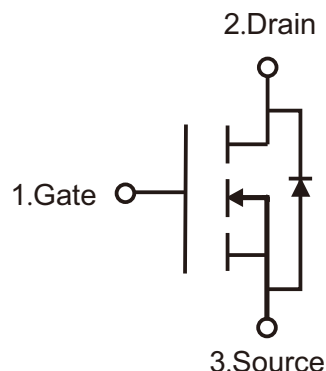
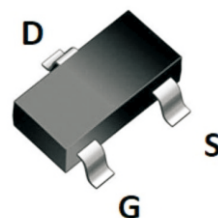
Features

- $R_{DS(ON)} \leq 30m\Omega @ V_{GS}=10V, I_D=4A$
- $R_{DS(ON)} \leq 35m\Omega @ V_{GS}=4.5, I_D=4A$
- $R_{DS(ON)} \leq 55m\Omega @ V_{GS}=2.5, I_D=3A$
- High Speed Switching
- High Density Cell Design For Low $R_{DS(ON)}$
- Trench Power LV MOSFET Technology

Features

- Load Switch
- PWM Application
- Power Management

SOT-23



Absolute Maximum Ratings (TA=25°C, unless otherwise specified)

Parameter	Symbols	Ratings	Units
Drain-Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current	I_D	5.8	A
Pulsed Drain Current(A)	I_{DM}	23	A
Power Dissipation	P_D	1.2	W
Thermal Resistance,Junction to Case(B)	$R_{\theta JA}$	100	$^{\circ}CW$
Operation Junction Temperature and Storage Temperature	T_j, T_{stg}	-55 ~ +150	$^{\circ}C$



Electrical Characteristics (TA=25°C, unless otherwise specified)

Parameter	Symbols	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	B_{VDSS}	$V_{GS} = 0V, I_D = 250\mu A$	30	32		V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 30V, V_{GS} = 0V$			1	μA
Gate- Source Leakage Current	Forward	I_{GSS}			100	nA
	Reverse				-100	
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.6	0.9	1.5	V
Static Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 4A$		21	33	m Ω
		$V_{GS} = 4.5V, I_D = 4A$		23	35	m Ω
		$V_{GS} = 2.5V, I_D = 3A$		38	55	m Ω
Drain-Source Diode Forward Voltage	V_{SD}	$I_S = 5.8A, V_{GS} = 0V$			1.2	V
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	$V_{DS} = 15V,$ $V_{GS} = 0V,$ $f = 1.0MHz$		630		pF
Output Capacitance	C_{oss}			55		pF
Reverse Transfer Capacitance	C_{rss}			71		pF
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = 15V, V_{GS} = 10V,$ $I_D = 5.8A$		17.5		nC
Gate-Source Charge	Q_{gs}			2.1		nC
Gate-Drain Charge	Q_{gd}			2		nC
Turn-On Delay Time	$t_{D(on)}$	$V_{DS} = 15V, V_{GS} = 10V,$ $I_D = 5.8A, R_{GEN} = 3\Omega$		4.4		ns
Turn-On Rise Time	t_r			28.2		ns
Turn-Off Delay Time	$t_{D(off)}$			16.2		ns
Turn-Off Fall Time	t_f			26		ns

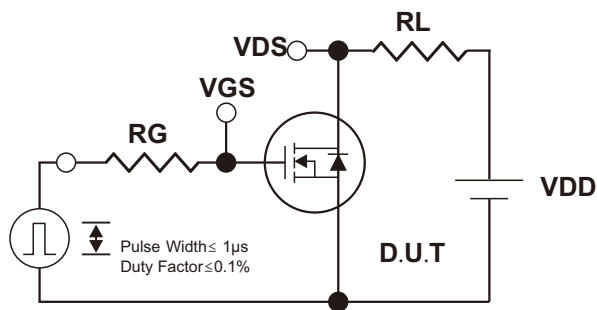
Notes:

A. Pulse Test: Pulse Width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

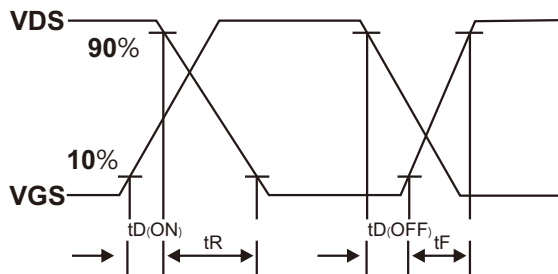
B. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the soldermounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design, while $R_{\theta JA}$ is determined by the board design. The maximum rating presented here is based on mounting on a 1 in 2 pad of 2oz copper.



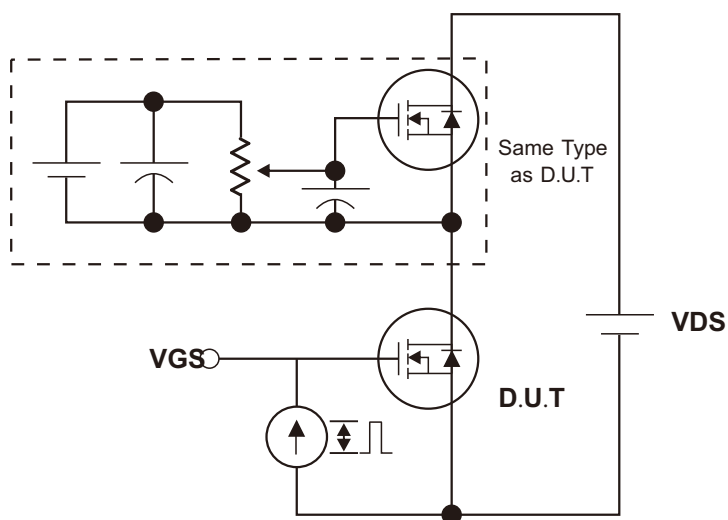
Test Circuits and waveforms



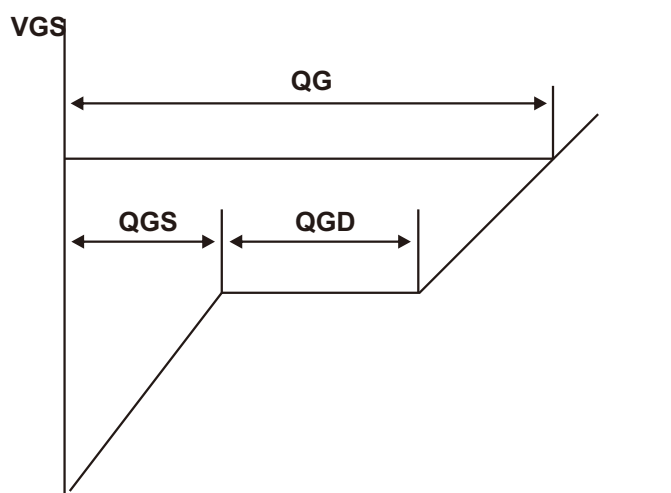
Switching Test Circuit



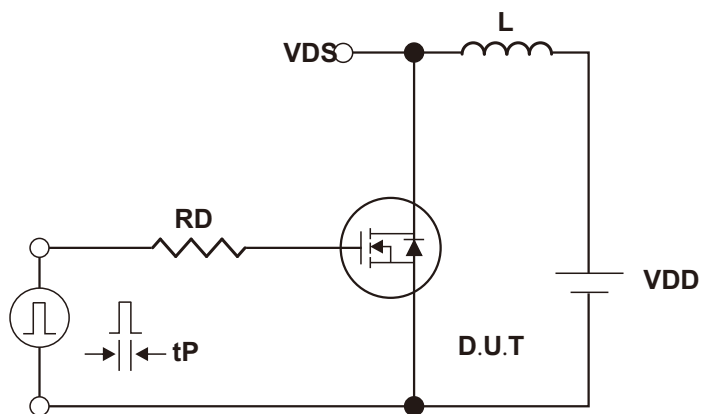
Switching Waveforms



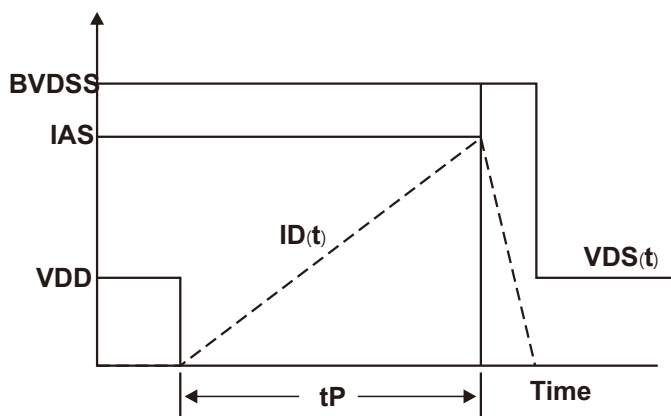
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms



Typical Characteristics

Fig.1 Output Characteristics

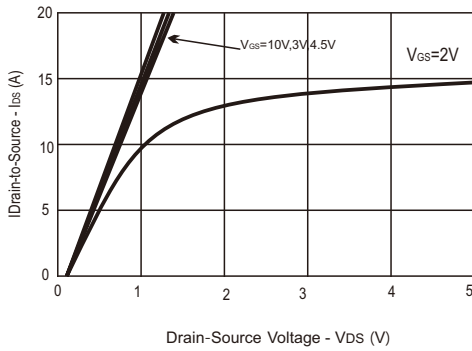


Fig.2 Typical Transfer Characteristics

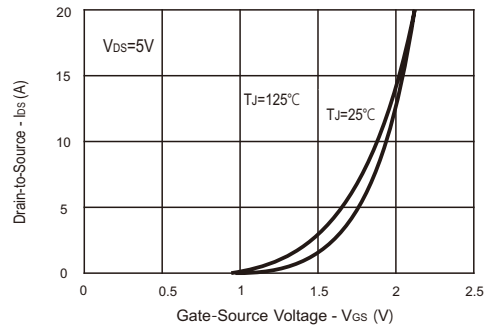


Fig.3 Drain-Source On-Resistance vs. Drain Current

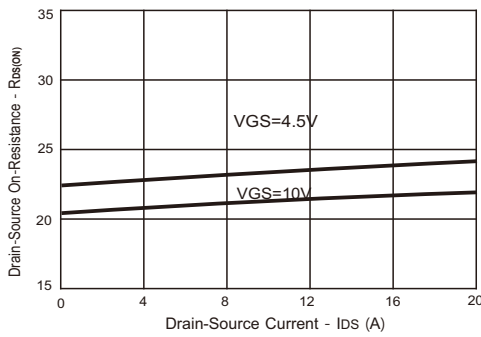


Fig.4 On-Resistance vs. Junction temperature

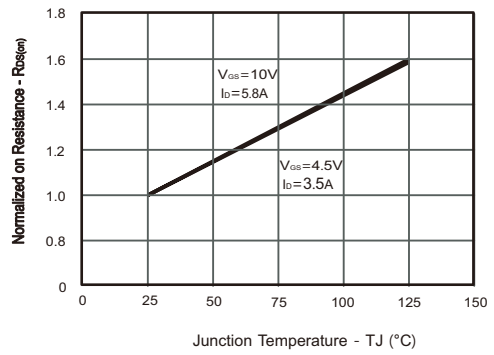


Fig.5 Gate-Charge Characteristics

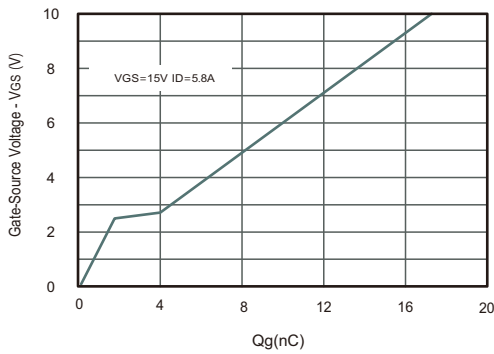
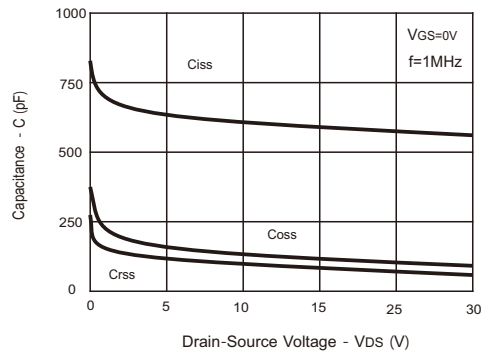
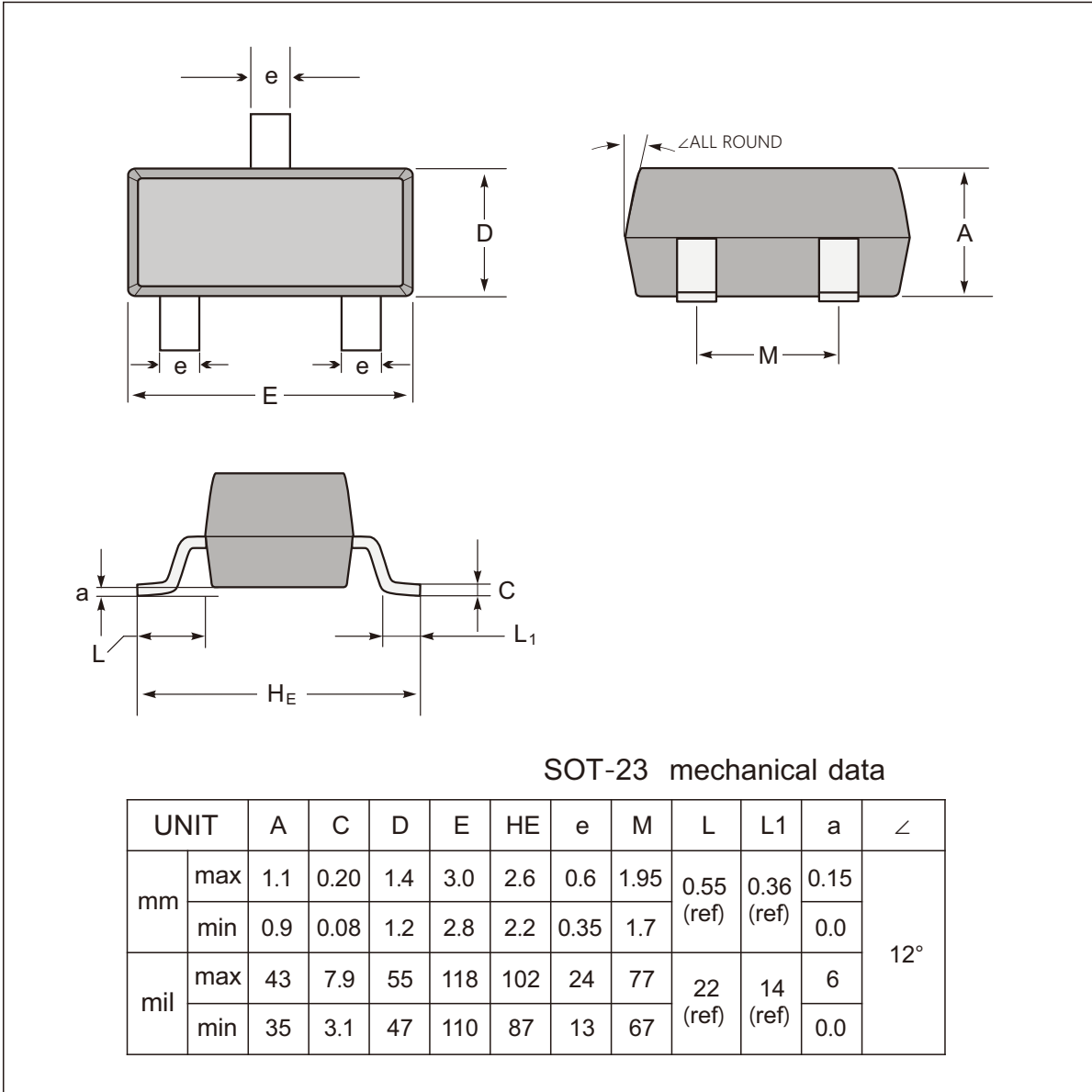


Fig.6 Capacitance vs. Drain-Source Voltage

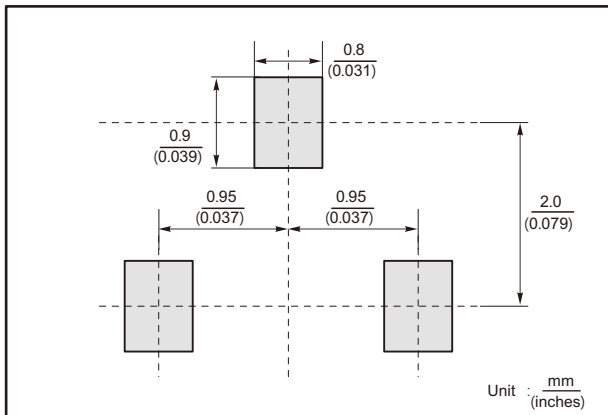




SOT-23 Package Outline Dimensions



The recommended mounting pad size



Marking

Type number	Marking code
NM3400	3400



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