### Surface Mount General Purpose Silicon Rectifiers

Reverse Voltage - 50 to 1000 V

Forward Current - 2 A

### FEATURES

- For surface mounted applications
- Low profile package
- Glass Passivated Chip Junction
- Easy to pick and place
- Lead free in comply with EU RoHS 2011/65/EU directives

### **MECHANICAL DATA**

- Case: SMB
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.09g / 0.003oz

G
DESCRIPTION
Cathode
Anode



#### **Maximum Ratings and Electrical characteristics**

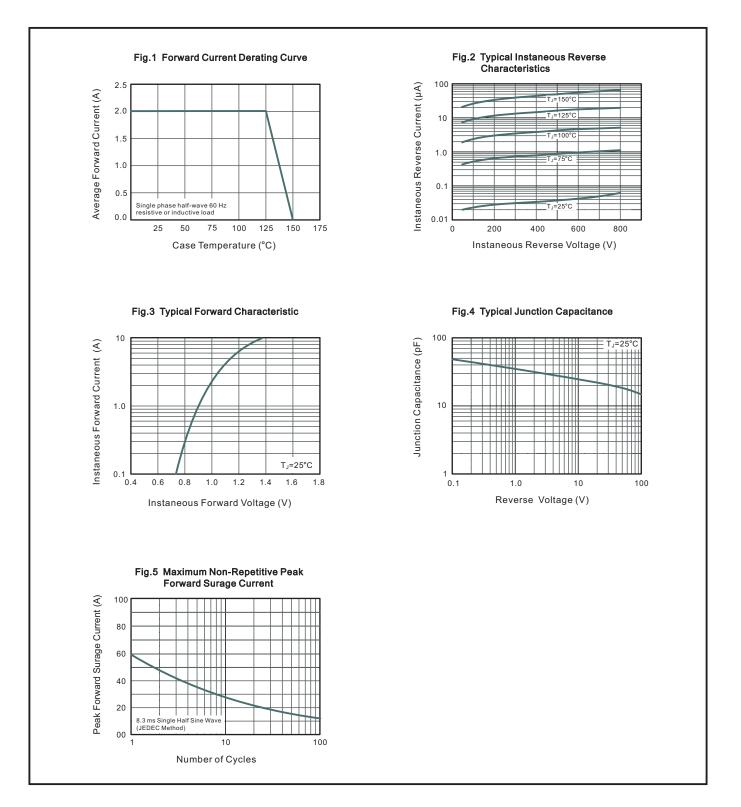
Ratings at 25 °C ambient temperature unless otherwise specified. Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

Parameter	Symbols	S2AB	S2BB	S2DB	S2GB	S2JB	S2KB	S2MB	Units
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
Maximum RMS voltage	$V_{\text{RMS}}$	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	$V_{\text{DC}}$	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current	I <sub>F(AV)</sub>	2						А	
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I <sub>fsm</sub>	60						A	
Maximum Instantaneous Forward Voltage at 2 A	V <sub>F</sub>	1.1						V	
Maximum DC Reverse Current $T_a = 25 \ ^{\circ}C$ at Rated DC Blocking Voltage $T_a = 125 \ ^{\circ}C$	I <sub>R</sub>	5 100						μA	
Typical Junction Capacitance (1)	Cj	25						pF	
Typical Thermal Resistance (2)	$R_{_{ extsf{ heta}JA}}$ $R_{_{ hetaJC}}$	60 20						°C/W	
Operating and Storage Temperature Range	Tj, T <sub>stg</sub>	-55 ~ +150					°C		

( 1 ) Measured at 1 MHz and applied reverse voltage of 4 V D.C

( 2 ) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.

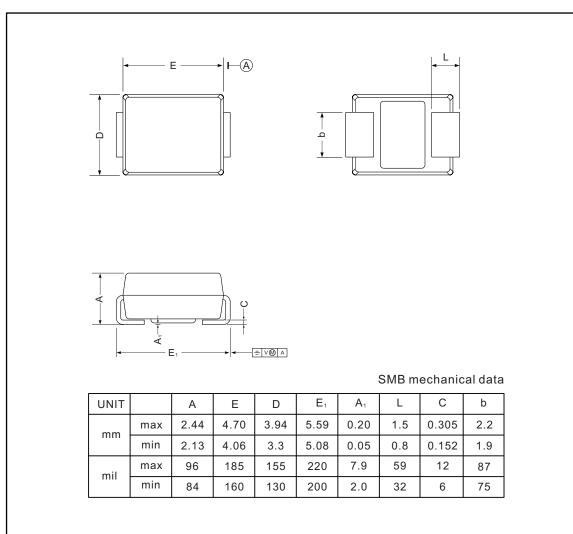
*山东晶导微电子股份有限公司 Jingdao Microelectronics co.LTD* 



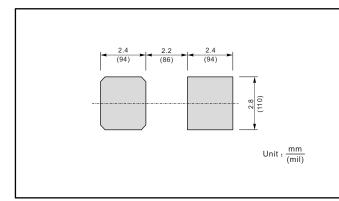
**SMB** 

## PACKAGE OUTLINE





## The recommended mounting pad size



## Marking

Type number	Marking code			
S2AB	S2A			
S2BB	S2B			
S2DB	S2D			
S2GB	S2G			
S2JB	S2J			
S2KB	S2K			
S2MB	S2M			

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