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QUAD OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM2060 integrated circuit is a high-gain, wide-bandwidth, quad operational amplifier capable of driving 20V peak-to-peak into 400 Ω loads. The NJM2060 combines many of the features of the NJM2058 as well as providing the capability of wider bandwidth, and higher slew rate make the NJM2060 ideal for active filters, data and telecommunications, and many instrumentation applications. The availability of the NJM2060 in the surface mounted micro-package allows the NJM2060 to be used in critical applications requiring very high packing densities. Each amplifier of the NJM2060 has the same electrical characteristic of the NJM4560.

■ FEATURES

Operating Voltage

Low Noise Voltage Slew Rate

 $(\pm 4V \sim \pm 18V)$

 $(4V/\mu s typ.)$

(10MHz typ.)

(25mA)

(RIAA 1.2 µVrms typ.)

DIP14, DMP14, SSOP14

High Output Current

Unity Gain Bandwidth

Package Outline

Bipolar Technology

■ PACKAGE OUTLINE





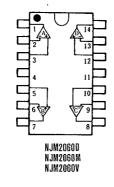
H.IM20800

NJM2060M



NJM2060V

■ PIN CONFIGURATION



PIN FUNCITON

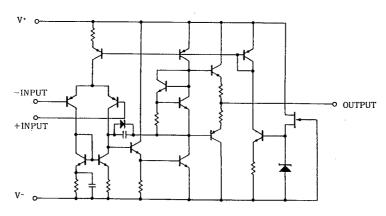
1. A OUTPUT 8. C OUTPUT

2 . A-INPUT 9 . C-INPUT 3 . A+INPUT 10 . C+INPUT 4 . V+ 11 . V-

4. V⁺ 11. V⁻ 5. B+INPUT 12. D+INPUT

6. B-INPUT 13. D-INPUT 7. BLOUTPUT 14. D OUTPUT

■ EQUIVALENT CIRCUIT (1/4 Shown)



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS		UNIT
Supply Voltage	V*/V-	±18		٧
Differential Input Voltage	V _{iD}	±30		٧
Input Voltage	V _{IC}	±15	(note 1)	V
Power Dissipation	PD	(DIP14) 700		mW
		(DIM14) 700	(note 2)	mW
		(SSOP14) 300		mW
Operating Temperature Range	Topr	-20~+75		°C
Storage Temperature Range	Tstg	-40~+125		°C

(note 1) For supply voltage less than ± 15 V. the absolute maximum input voltage is equal to the supply voltage. (note 2) At on PC board

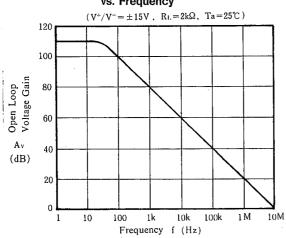
ELECTRICAL CHARACTERISTICS

 $(Ta = 25^{\circ}C, V' = 15V, V' = -15V)$

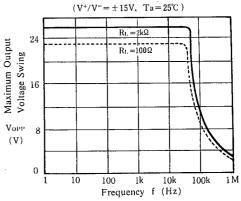
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V _{IO}	R _S ≦10kΩ		0.5	6	mV
Input Offset Current	I _{IO}	-	_	5	200	пA
Input Bias Current	l _B		_	40	500	nΑ
Input Resistance	R _{IN}		100	500	_	kΩ
Large Signal Voltage Gain	A _V	$R_L \ge 2k\Omega$, $V_O = \pm 10V$	86	100	_	dB
Maximum Output Voltage Swing 1	Vomi	$R_{L} \ge 2k\Omega$	±12	±14	<u> </u>	v
Maximum Output Voltage Swing 2	V _{OM2} :	I _O =25mA	±10	±11.5	_	v
Input Common Mode Voltage Range	V _{ICM}		±12	±14	_	v
Common Mode Rejection Ratio	CMR	R _S ≦10kΩ	70	90	_	dB
Supply Voltage Rejection Ratio	SVR	R _s ≤10kΩ	76	90	_	dB
Operating Current	Icc		_	. 9	14	mA
Slew Rate	SR		-	4	_	V/μs
Gain Bandwidth Product	GB		_	10	_	MHz
Equivalent Input Noise Voltage	V _{NI}	RIAA Rs = 2.2 k Ω , 30 kHz L.P.F.	-	1.2	·	μVrms

■ TYPICAL CHARACTERISTICS

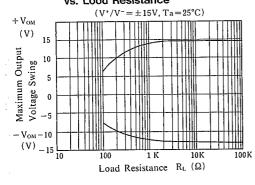
Open Loop Voltage Gain vs. Frequency



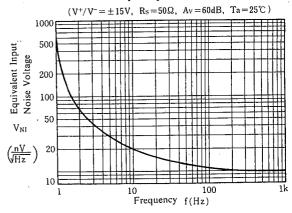
Maximum Output Voltage Swing vs. Frequency



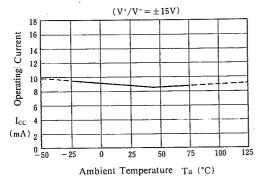
Maximum Output Voltage Swing vs. Lood Resistance



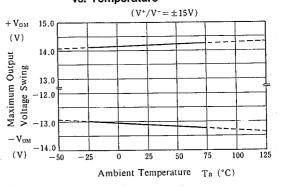
Equivalent Input Noise Voltage vs. Frequency



Operating Current vs. Temperature



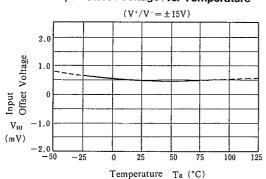
Maximum Output Voltage Swing vs. Temperature



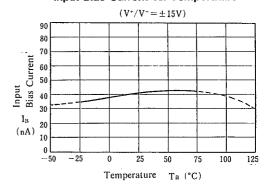
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■ TYPICAL CHARACTERISTICS

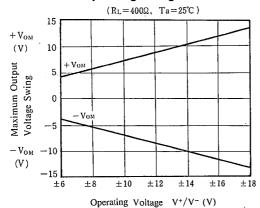
Input Offset Voltage vs. Temperature



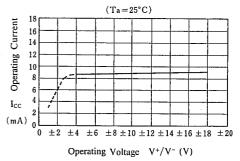
Input Bias Current vs. Temperature



Maximum Output Voltage Swing vs. Operating Voltage



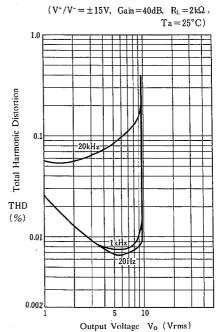
Operating Current vs. Operating Voltage



TYPICAL CHARACTERISTICS

Total Harmonic Distortion

Total Harmonic Distortion



NJM2060

MEMO

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