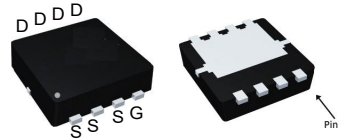


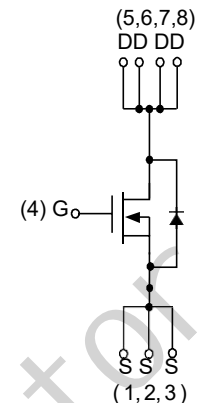
Features

- 30V/44A,
 $R_{DS(ON)} = 5.5m\Omega$ (Max.) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 8.7m\Omega$ (Max.) @ $V_{GS} = 4.5V$
- Reliable and Rugged
- Lower Q_g and Q_{gd} for high-speed switching
- Lower $R_{DS(ON)}$ to Minimize Conduction Losses
- 100% UIS + R_g Tested
- Lead Free and Green Devices Available (RoHS Compliant)

Pin Description



PDFN3x3A-8_EP



N-Channel MOSFET

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit	
Common Ratings				
V _{DSS}	Drain-Source Voltage	30	V	
V _{GSS}	Gate-Source Voltage	±20		
T _J	Maximum Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-55 to 150		
I _S	Diode Continuous Forward Current	T _C =25°C	16	A
I _D	Continuous Drain Current	T _C =25°C	44	
		T _C =100°C	34	
I _{DM} ^a	Pulsed Drain Current	T _C =25°C	100	
P _D	Maximum Power Dissipation	T _C =25°C	26.6	W
		T _C =100°C	10.6	
R _{θJC}	Thermal Resistance-Junction to Case	Steady State	4.7	°C/W
I _D ^b	Continuous Drain Current	T _A =25°C	13.8	A
		T _A =70°C	11	
I _{DM} ^b	Pulsed Drain Current	T _A =25°C	34	
P _D ^b	Maximum Power Dissipation	T _A =25°C	1.73	W
		T _A =70°C	1.11	
R _{θJA} ^b	Thermal Resistance-Junction to Ambient	t ≤ 10s	40	°C/W
		Steady State	72	
I _{AS} ^c	Avalanche Current, Single pulse	L=0.1mH	20	A
E _{AS} ^c	Avalanche Energy, Single pulse	L=0.1mH	20	mJ

Note a: Pulse width is limited by max. junction temperature.

Note b: R_{θJA} steady state t=999s.

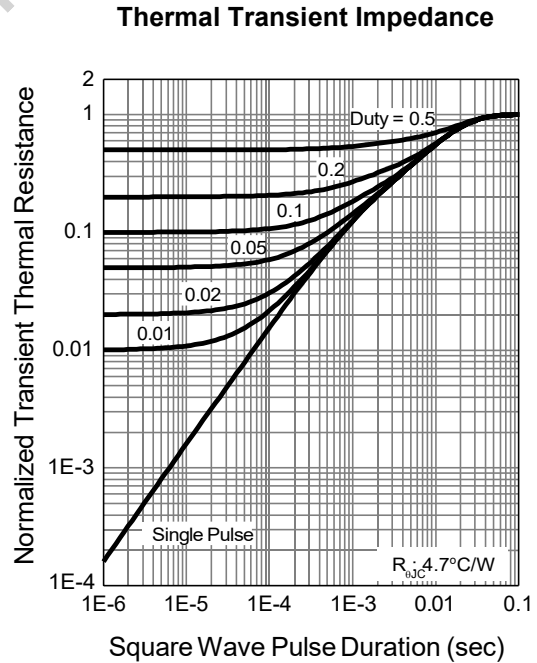
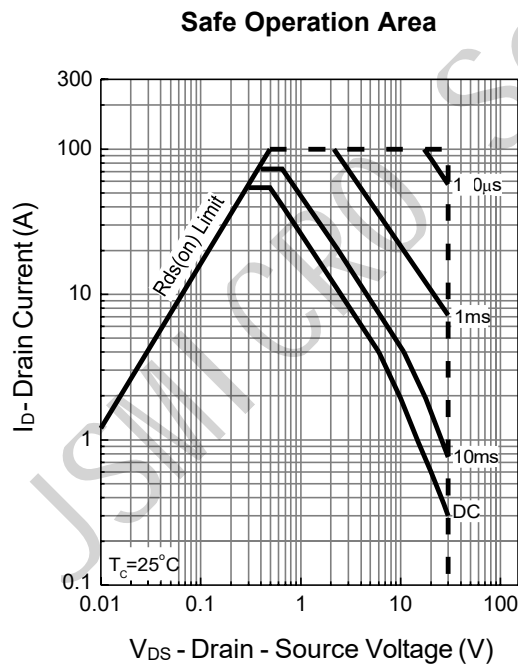
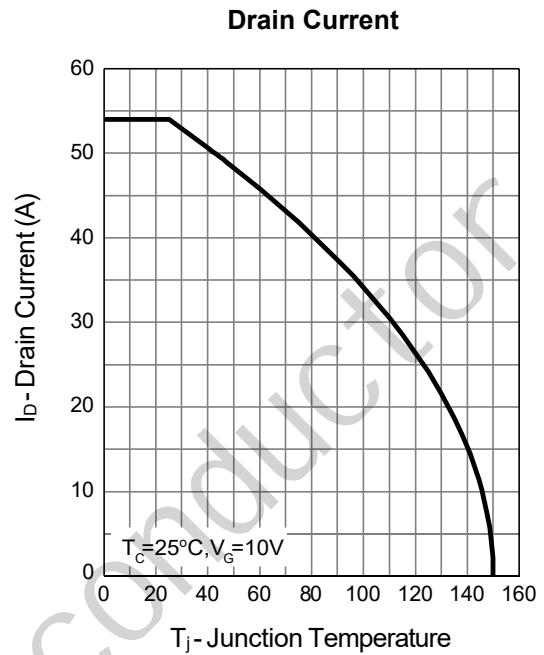
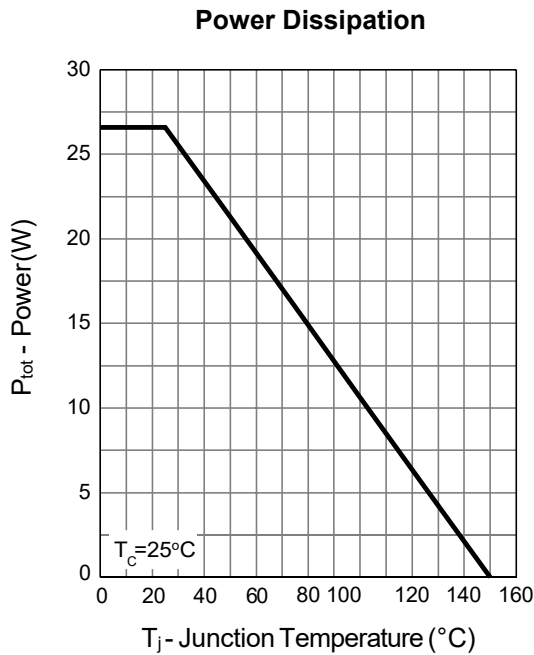
Note c: UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature T_J=25°C).

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
$BV_{DSS(t)}$	Drain-Source Breakdown Voltage (transient)	$V_{GS}=0V, I_{D(av)}=20A$ $T_{case}=25^\circ\text{C}, t_{transient}=100ns$	34	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	-	-	1	μA
		$T_J=85^\circ\text{C}$	-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	1.4	1.7	2.5	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(on)}^d$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=12A$	-	4.5	5.5	m Ω
		$T_J=125^\circ\text{C}$	-	7.6	-	
		$V_{GS}=4.5V, I_{DS}=9A$	-	6.9	8.7	
G_{fs}	Forward Transconductance	$V_{DS}=5V, I_{DS}=10A$	-	16	-	S
Diode Characteristics						
V_{SD}^d	Diode Forward Voltage	$I_{SD}=10A, V_{GS}=0V$	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	$I_{SD}=5A, di_{SD}/dt=100A/\mu s$	-	11	-	ns
t_a	Charge Time		-	14	-	
t_b	Discharge Time		-	25	-	
Q_{rr}	Reverse Recovery Charge		-	13	-	
Dynamic Characteristics						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	-	1.8	3	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=15V,$ Frequency=1.0MHz	-	750	-	pF
C_{oss}	Output Capacitance		-	530	-	
C_{rss}	Reverse Transfer Capacitance		-	37	-	
$t_{d(ON)}$	Turn-on Delay Time		-	7.8	-	
t_r	Turn-on Rise Time	$V_{DD}=15V, R_L=15\Omega,$ $I_{DS}=1A, V_{GEN}=10V,$ $R_G=1\Omega$	-	8.4	-	
$t_{d(OFF)}$	Turn-off Delay Time	-	18	-		
t_f	Turn-off Fall Time	-	17	-		
Gate Charge Characteristics						
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V,$ $I_{DS}=12A$	-	12	18	nC
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=4.5V,$ $I_{DS}=12A$	-	5.5	-	
Q_{gth}	Threshold Gate Charge		-	1.1	-	
Q_{gs}	Gate-Source Charge		-	1.9	-	
Q_{gd}	Gate-Drain Charge		-	2.2	-	

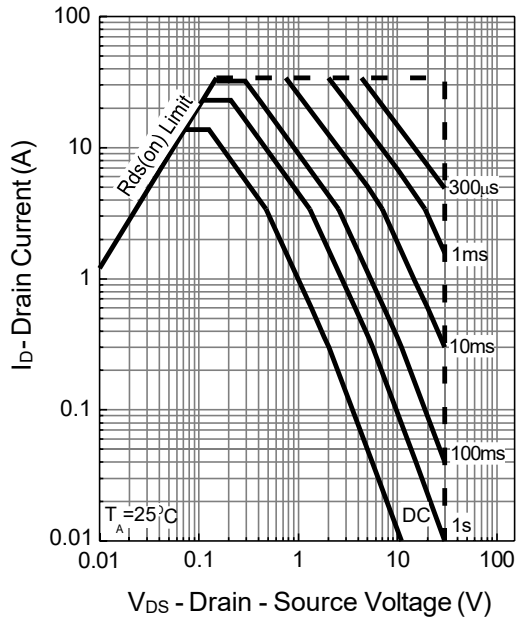
Note d: Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

Typical Operating Characteristics

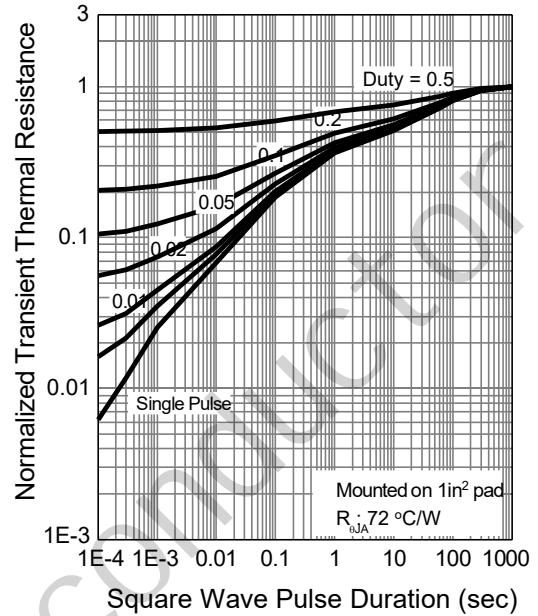


Typical Operating Characteristics (Cont.)

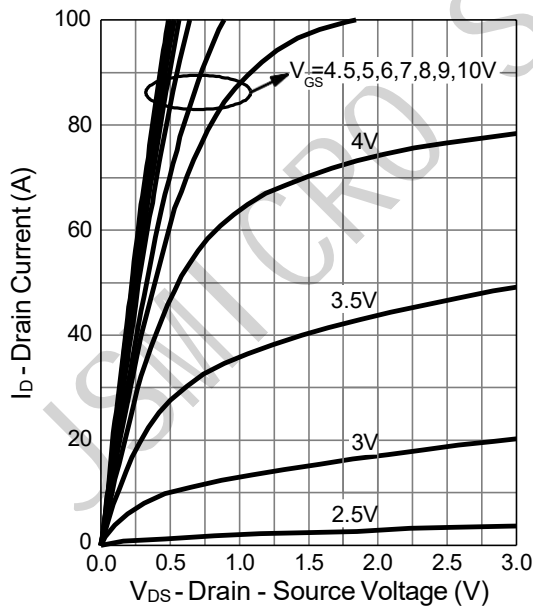
Safe Operation Area



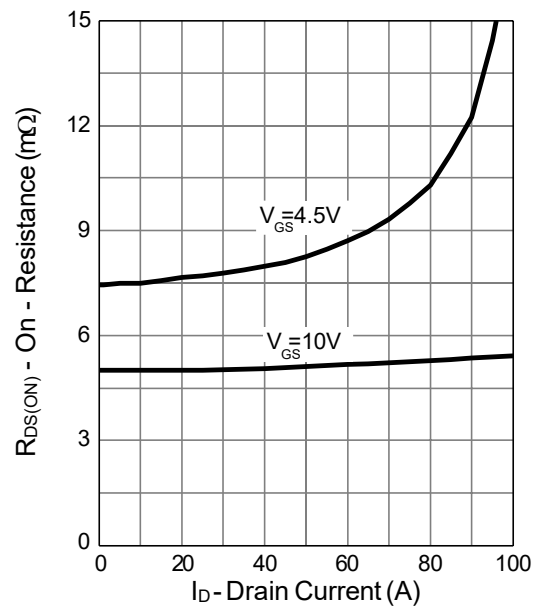
Thermal Transient Impedance



Output Characteristics

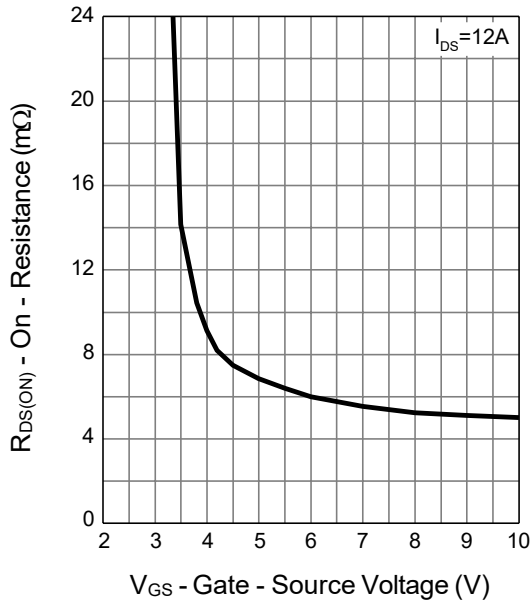


Drain-Source On Resistance

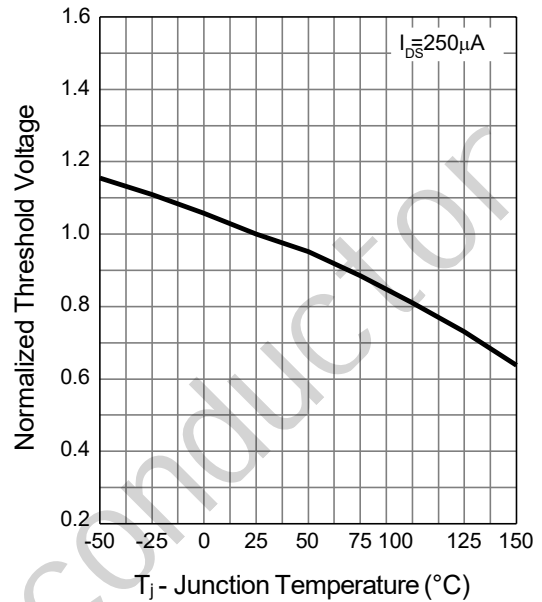


Typical Operating Characteristics (Cont.)

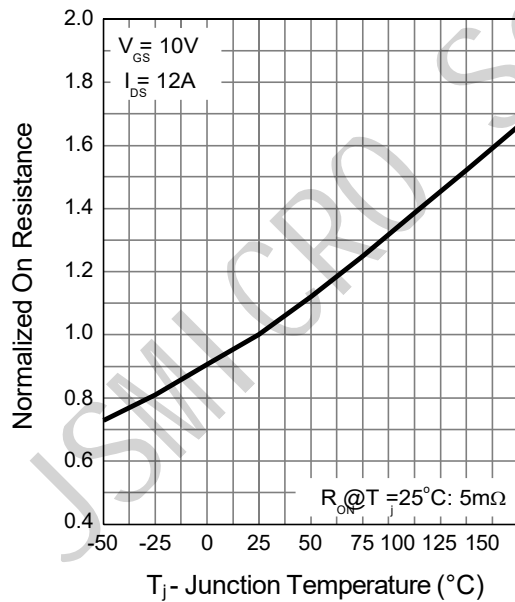
Gate-Source On Resistance



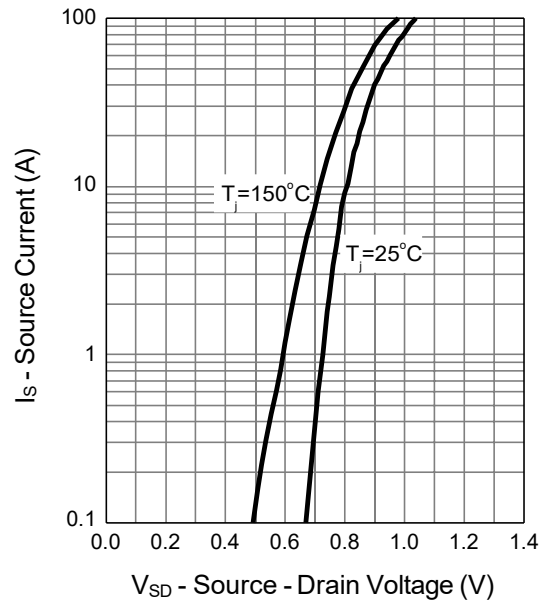
Gate Threshold Voltage



Drain-Source On Resistance

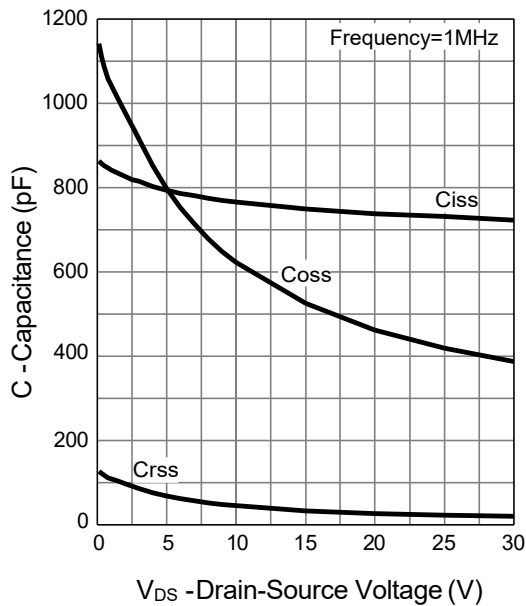


Source-Drain Diode Forward

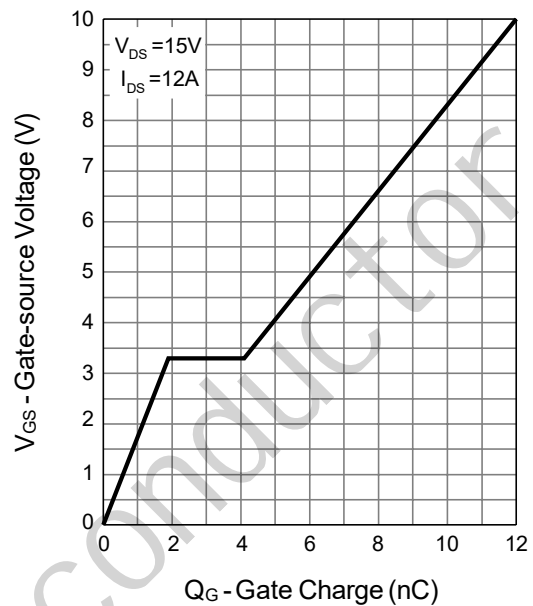


Typical Operating Characteristics (Cont.)

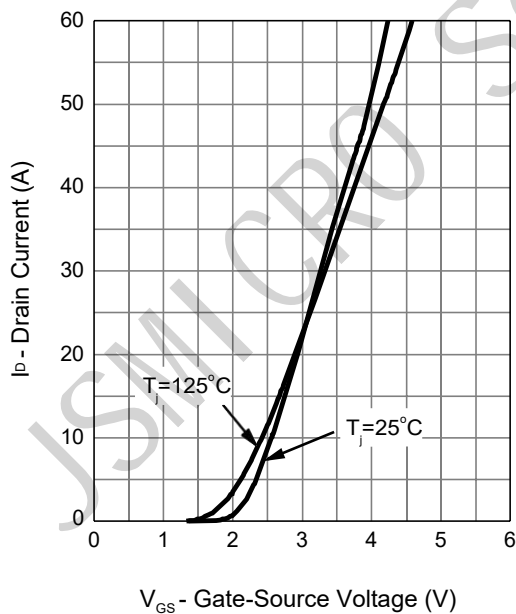
Capacitance



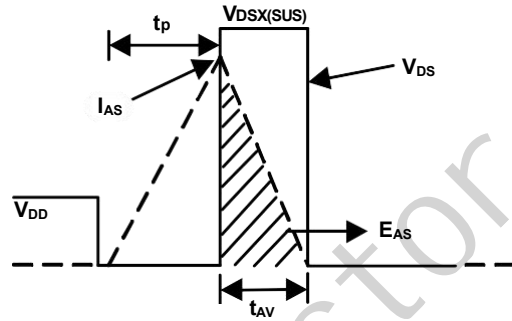
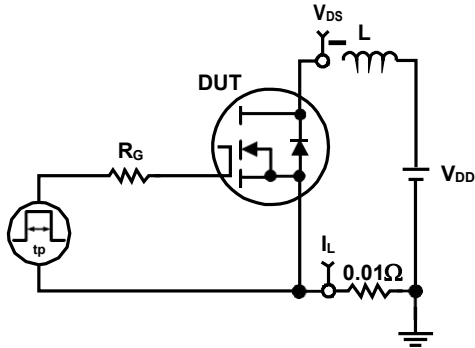
Gate Charge



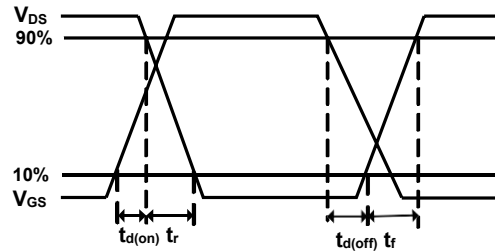
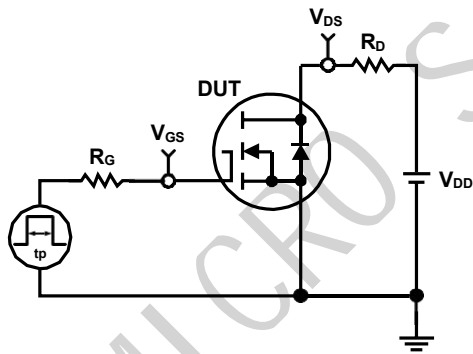
Transfer Characteristics



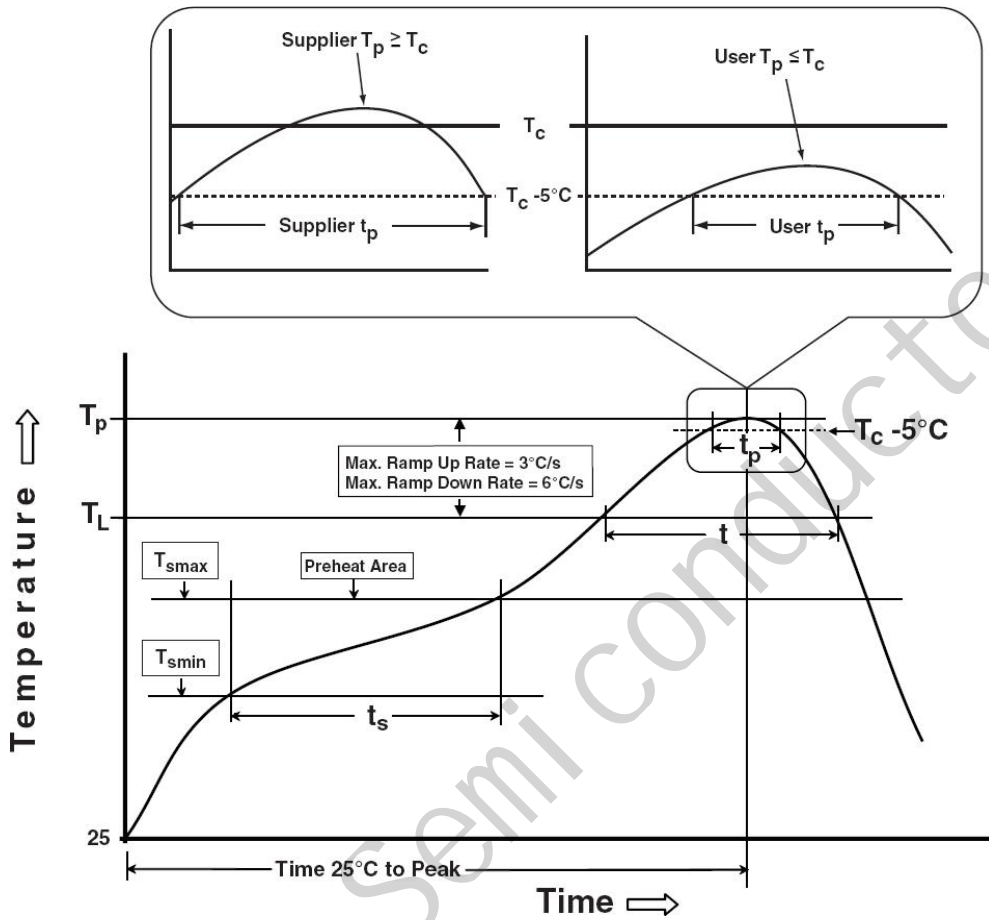
Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms

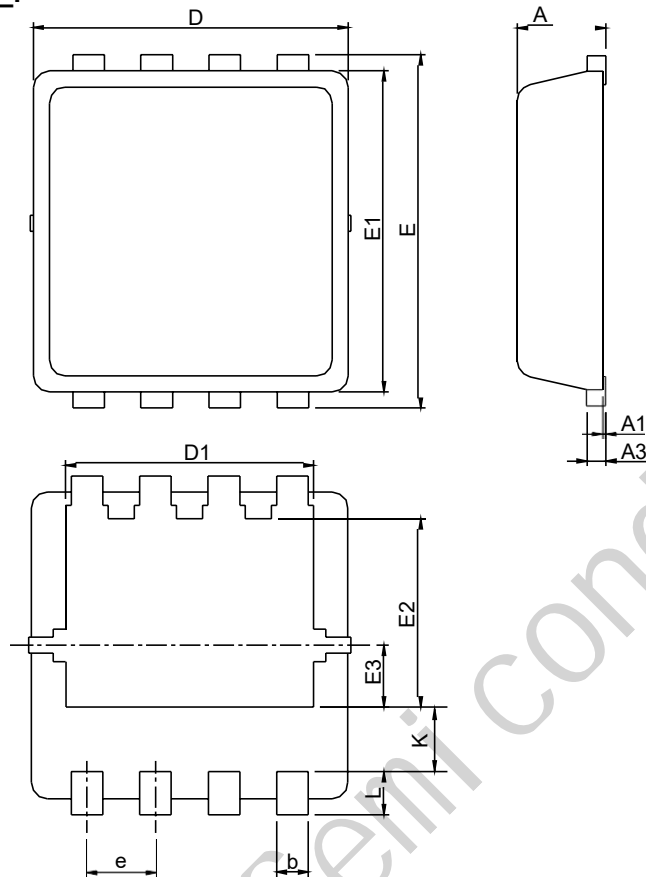


Classification Profile



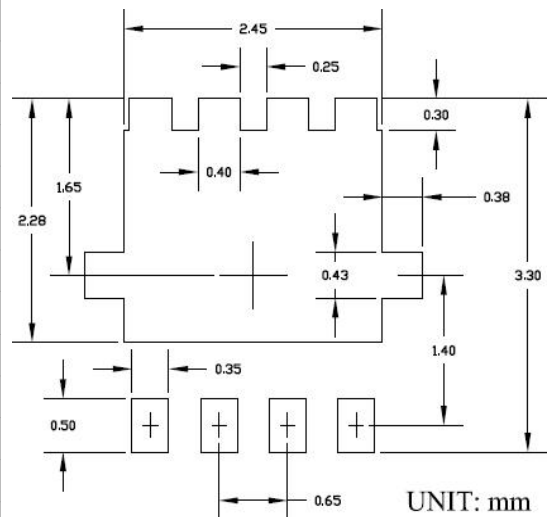
Package Information

PDFN3x3_8L_EP1_P



SYMBOL	PDFN3x3-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.80	1.00	0.031	0.039
A1	0.00	0.05	0.000	0.002
A3	0.10	0.25	0.004	0.010
b	0.24	0.35	0.009	0.014
D	2.90	3.10	0.114	0.122
D1	2.25	2.45	0.089	0.096
E	3.10	3.30	0.122	0.130
E1	2.90	3.10	0.114	0.122
E2	1.65	1.85	0.065	0.073
E3	0.56	0.58	0.022	0.023
e	0.65 BSC		0.026 BSC	
K	0.475	0.775	0.019	0.031
L	0.30	0.50	0.012	0.020

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