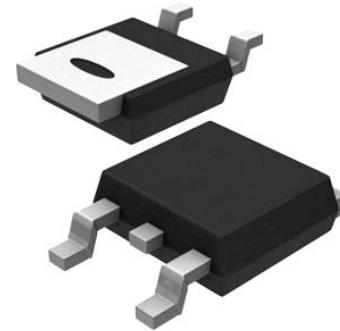


asic Thyristors

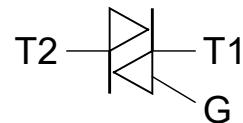
DESCRIPTION

- With TO-252(DPAK) packaging
- Operating in 3 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



APPLICATIONS

- Switching applications
- Phase control
- Motor control circuits
- Static switching on inductive or resistive load

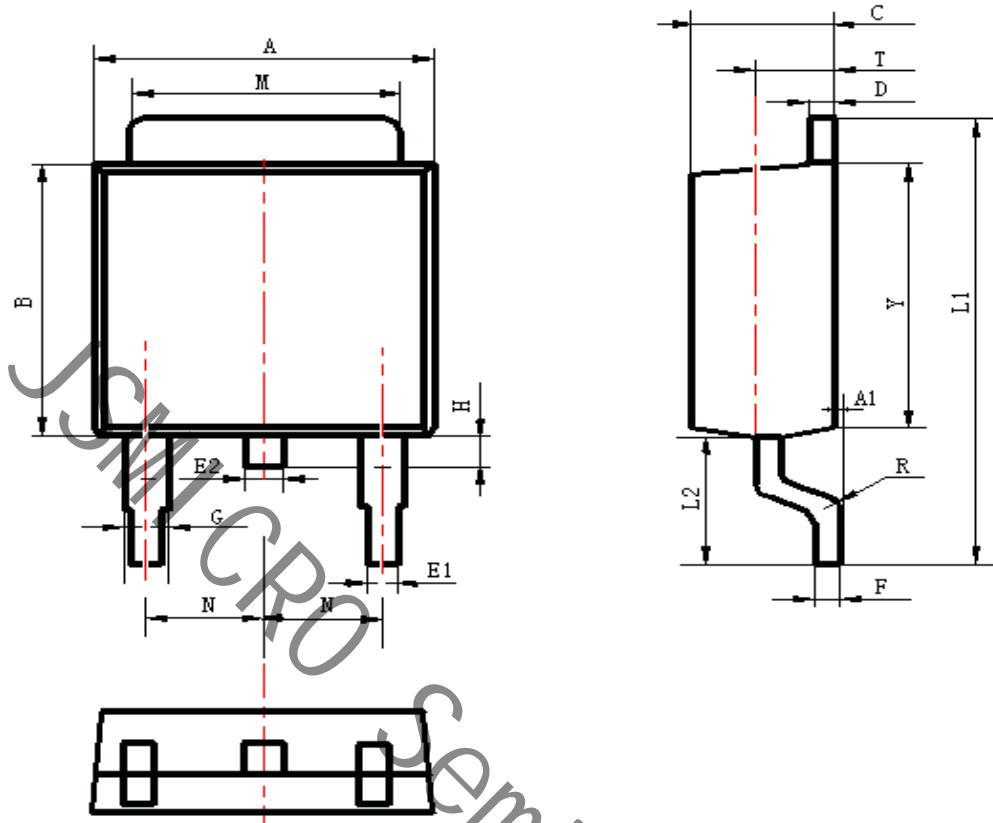


ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	MAX	UNIT
V_{DRM}	Repetitive peak off-state voltage	800	V
V_{RRM}	Repetitive peak reverse voltage	800	V
$I_{T(RSM)}$	Average on-state current	8	A
I_{TSM}	Surge non-repetitive on-state current	50HZ 60HZ 65 72	A
$P_{G(AV)}$	Average gate power dissipation (over any 20 ms period)	0.5	W
T_j	Operating junction temperature	-40~125	°C
T_{stg}	Storage temperature	-40~150	°C

ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_R=V_{RRM}$ Rated;			mA
I_{DRM}	Repetitive peak off-state current	$V_D=V_{DRM}$ Rated;		0.5	mA
V_{TM}	On-state voltage	$I_T=10\text{A}$		1.65	V
I_{GT}	Gate-trigger current	$V_D = 12\text{V}; I_T = 0.1\text{A};$	I	50	mA
			II	50	
			III	50	
V_{GT}	Gate-trigger voltage	$V_D = 12\text{V}; I_T = 0.1\text{A};$		1.5	V
$R_{th(j-mb)}$	Junction to mounting base	Half cycle		2.4	°C/W

Package Outline: TO-252


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	6.30	6.90	0.248	0.272
A1	0.00	0.16	0.000	0.006
B	5.70	6.30	0.224	0.248
C	2.10	2.50	0.083	0.098
D	0.30	0.70	0.012	0.028
E1	0.60	0.90	0.024	0.035
E2	0.70	1.00	0.028	0.039
F	0.30	0.60	0.012	0.024
G	0.70	1.20	0.028	0.047
L1	9.60	10.50	0.378	0.413
L2	2.70	3.10	0.106	0.122
H	0.40	1.00	0.016	0.039
M	5.10	5.50	0.201	0.217
N	2.09	2.49	0.082	0.098
R	0.30		0.012	
T	1.40	1.60	0.055	0.063
Y	5.10	6.30	0.201	0.248

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