## NRVB130T1G

## Surface Mount Schottky Power Rectifier <br> Plastic SOD-123 Package

This device uses the Schottky Barrier principle with a large area metal-to-silicon power diode. Ideally suited for low voltage, high frequency rectification or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system. This package also provides an easy to work with alternative to leadless 34 package style.

## Features

- Guardring for Stress Protection
- Low Forward Voltage
- $125^{\circ} \mathrm{C}$ Operating Junction Temperature
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Package Designed for Optimal Automated Board Assembly
- ESD Rating:
- Human Body Model = 3
- Machine Model = C
- NRVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb -Free Packages*


## Mechanical Characteristics

- Device Marking: S3
- Polarity Designator: Cathode Band
- Weight: 11.7 mg (approximately)
- Case: Epoxy, Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
${ }^{\bullet}$ Lead and Mounting Surface Temperature for Soldering Purposes:
${ }^{*}$ For $260^{\circ} \mathrm{C}$ Max. for 10 Seconds


SOD-123
CASE 425
STYLE 1

MARKING DIAGRAM


S3 = Specific Device Code
M = Date Code
$=\mathrm{Pb}-$ Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

| Device | Package | Shipping $^{\dagger}$ |
| :--- | :---: | :---: |
| MBR130T1G, | $\begin{array}{c}\text { SOD-123 } \\ \text { NRVB130T1G }\end{array}$ | $\begin{array}{c}3,000 / \\ \text { (Pb-Free) }\end{array}$ |
| Tape \& Reel ** |  |  |\(\left.| \begin{array}{l}MBR130T3G, <br>

NRVB130T3G\end{array} \begin{array}{c}SOD-123 <br>

(Pb-Free)\end{array}\right]\)| $10,000 /$ |
| :---: |
| Tape \& Reel *** |

** 8 mm Tape, $7^{\prime \prime}$ Reel
*** 8 mm Tape, 13" Reel
$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Peak Repetitive Reverse Voltage <br> Working Peak Reverse Voltage <br> DC Blocking Voltage | $\mathrm{V}_{\text {RRM }}$ <br> $\mathrm{V}_{\text {RWM }}$ <br> $\mathrm{V}_{\mathrm{R}}$ | 30 | V |
| Average Rectified Forward Current <br> $\left(\right.$ Rated $\left.\mathrm{V}_{\mathrm{R}}\right) \mathrm{T}_{\mathrm{L}}=65^{\circ} \mathrm{C}$ | $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | 1.0 | A |
| Non-Repetitive Peak Surge Current <br> (Surge Applied at Rated Load Conditions, Halfwave, Single Phase, 60 Hz$)$ | $\mathrm{I}_{\mathrm{FSM}}$ | 5.5 | A |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -65 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Operating Junction Temperature | $\mathrm{T}_{\mathrm{J}}$ | -65 to +125 | ${ }^{\circ} \mathrm{C}$ |
| Voltage Rate of Change (Rated $\mathrm{V}_{\mathrm{R}}$ ) | $\mathrm{dv} / \mathrm{dt}$ | 1000 | $\mathrm{~V} / \mathrm{us}$ |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Thermal Resistance, Junction to Ambient (Note 1) | $\mathrm{R}_{\text {өJA }}$ | 230 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance, Junction to Lead (Note 1) | $\mathrm{R}_{\theta \mathrm{JLL}}$ | 108 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

1. FR-4 or FR-5 $=3.5 \times 1.5$ inches using a 1 inch Cu pad.

## ELECTRICAL CHARACTERISTICS

| Characteristic | Symbol | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { Instantaneous Forward Voltage (Note 2) } \\ & \left(I_{F}=0.1 \mathrm{~A}, T_{J}=25^{\circ} \mathrm{C}\right) \\ & \left(I_{F}=0.7 \mathrm{~A}, \mathrm{~T}_{J}=25^{\circ} \mathrm{C}\right) \\ & \left(I_{F}=1.0 \mathrm{~A}, \mathrm{~T}_{J}=25^{\circ} \mathrm{C}\right) \\ & \hline \end{aligned}$ | $\mathrm{V}_{\mathrm{F}}$ | $\begin{gathered} - \\ -\overline{47} \end{gathered}$ | $\begin{aligned} & 0.35 \\ & 0.45 \end{aligned}$ | V |
| Maximum Instantaneous Reverse Current (Note 2) (Rated DC Voltage, $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ ) $\left(\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{C}}=25^{\circ} \mathrm{C}\right)$ | $\mathrm{I}_{\mathrm{R}}$ | $\begin{aligned} & 60 \\ & 10 \end{aligned}$ |  | $\mu \mathrm{A}$ |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
2. Pulse Test: Pulse Width $=300 \mu \mathrm{~s}$, Duty Cycle $\leq 2 \%$.


Figure 1. Maximum Forward Voltage


Figure 3. Typical Reverse Current


Figure 5. Current Derating, Lead, $R_{\theta J L}=108^{\circ} \mathrm{C} / \mathrm{W}$


Figure 2. Typical Forward Voltage


Figure 4. Typical Capacitance


Figure 6. Forward Power Dissipation

## PACKAGE DIMENSIONS

SOD-123
CASE 425-04
ISSUE G


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

|  | MILLIMETERS |  |  | INCHES |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| DIM | MIN | NOM | MAX | MIN | NOM | MAX |
| A | 0.94 | 1.17 | 1.35 | 0.037 | 0.046 | 0.053 |
| A1 | 0.00 | 0.05 | 0.10 | 0.000 | 0.002 | 0.004 |
| b | 0.51 | 0.61 | 0.71 | 0.020 | 0.024 | 0.028 |
| c | --- | --- | 0.15 | --- | --- | 0.006 |
| D | 1.40 | 1.60 | 1.80 | 0.055 | 0.063 | 0.071 |
| E | 2.54 | 2.69 | 2.84 | 0.100 | 0.106 | 0.112 |
| $\mathbf{H}_{\text {E }}$ | 3.56 | 3.68 | 3.86 | 0.140 | 0.145 | 0.152 |
| L | 0.25 | --- | --- | 0.010 | --- | --- |
| $\boldsymbol{\theta}$ | $0^{\circ}$ | --- | $10^{\circ}$ | $0^{\circ}$ | --- | $10^{\circ}$ |

STYLE 1:
PIN 1. CATHODE 2. ANODE

## SOLDERING FOOTPRINT*



SCALE 10:1 $\left(\frac{\mathrm{mm}}{\text { inches }}\right)$
*For additional information on our $\mathrm{Pb}-F r e e$ strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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