

General Description

The SGM2019-1.2Y N5G/TR series are a group of low-dropout (LDO)voltage regulators offering the benefits of wide input voltage range from 1.2V to 5. 5V, low dropout voltage,low power consumption, and miniaturized packaging.

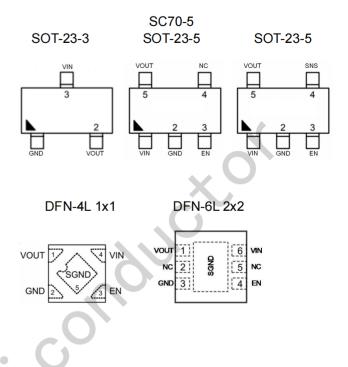
Quiescent current of only 2µA makes these devices ideal for powering the battery-powered, always-on systems that require very little idle-state power dissipation to a longer service life. There is an option of shutdown mode by selecting the parts with the EN pin and pulling it low. The shutdown current in this mode goes down to only 10nA (typical).

The SGM2019-1.2Y N5G/TR series of linear regulators are stable with the ceramic output capacitor over its wide input range from 1.2V to 5.5V and the entire range of output load current (0mA to 300mA).

Features

- 2µA Ground Current at no Load
- ±2% Output Accuracy
- 300mA Output Current
- 10nA Disable Current (by option)
- Wide Operating Input Voltage Range: 1.2V to 5.5V
- Dropout Voltage: 0.16V at 300mA/ VOUT 3.3V
- Support Fixed Output Voltage 0.8V, 0.9V, 1.2V, 1.5V, 1.6V, 1.8V, 2.5V, 2.8V, 3.0V, 3.3V
- Adjustable Output Voltage Available by Specific Application
- Stable with Ceramic or Tantalum Capacitor
- Current Limit Protection
- Over-Temperature Protection
- SOT-23-3, SOT-23-5, DFN-4L 1x1 and DFN-6L 2x2 Packages Available

Pin Configurations



Applications

- Portable, Battery Powered Equipment
- Low Power Microcontrollers
- Laptop, Palmtops and PDAs
- Wireless Communication Equipment
- Audio/Video Equipment
- Car Navigation Systems



Description of Functional Pins

SGM2019-1.2Y N5G/TR

Pin No				Pin Name	Pin Function	
SOT-23-3	SOT-23-5	DFN-1X1	DFN-2X2	Pin Name	FILFUICUON	
1	2	2	3	GND	Ground	
2	5	1	1	VOUT	Output of the Regulator	
3	1	4	6	VIN	Input of Supply Voltage.	
	3	3	4	EN	Enable Control Input.	
	4		2,5	NC	No internal connection	
		Exposed	Exposed	SGND	Substrate of Chip. Leave floating or tie to GND.	
		Pad	Pad		Substrate of Chip. Leave loating of the to GND.	

SGM2019-1.2YN5G/TRN (For SOT-23-5 with SNS pin)

Pin No	Pin Name	Pin Function				
SOT-23-5	FIIINallie	FillFullcuoli				
2	GND	Ground				
5	VOUT	Output of the Regulator				
1	VIN	Input of Supply Voltage.				
3	EN	Enable Control Input.				
4	SNS	Sense of Output Voltage.				
4 SNS Sense of Output Voltage.						



Typical Application Circuit

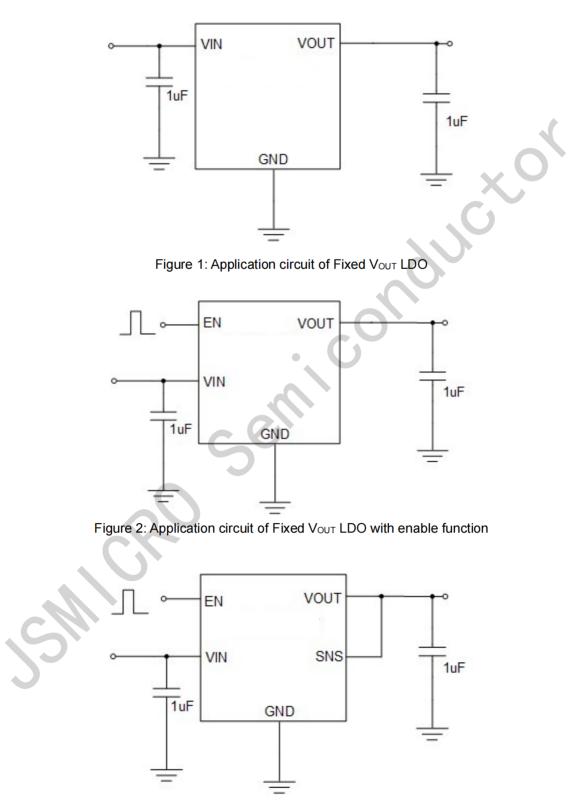


Figure 3: Application circuit of Fixed V_{OUT} LDO with enable and sense functions



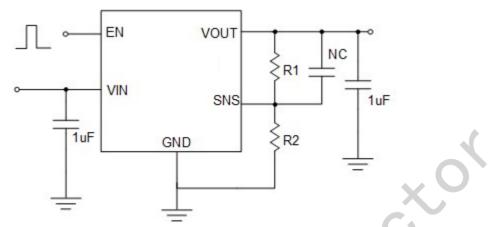
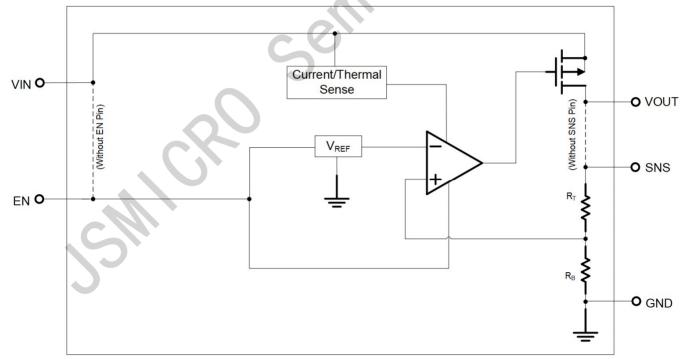


Figure 4: Adjustable output voltage LDO application circuit by SGM2019-1.2YN5G/TR

Function Block Diagram





Absolute Maximum Ratings (Note 1)

SHICRO

VIN to GND	0.3V to 6.5V
VOUT, EN, SNS to GND	0.3V to 6V
VOUT to VIN	6V to 0.3V
Package Thermal Resistance (Note 2)	
SOT-23-5, SOT-23-3, θ _{JA}	200 °C /W
DFN-4L 1x1, θ _{JA}	130 °C /W
DFN-6L 2x2, θ _{JA}	95 °C /W
Lead Temperature (Soldering, 10 sec.)	260 °C
Junction Temperature	150 °C
Storage Temperature Range	-60 °C to 150 °C
ESD Susceptibility	
НВМ	21.00
MM	
CDM	2KV
Recommended Operating Conditions	
Input Voltage VIN	1.2V to 5.5V
Junction Temperature Range	-40 °C to 125 °C
Ambient Temperature Range	40 °C to 85 °C



Electrical Characteristics

(V_{IN} =5V, V_{EN} = 5V T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit	
Supply Voltage	Vin			1.2		5.5	V	
DC Output Voltage Accuracy		ILOAD =0	.1mA	-2		2	%	
	VDROP_3V	V _{OUT} ≥ 3	V		0.16			
	VDROP_2.8V	V _{OUT} = 2.8V			0.18	D		
	VDROP_2.5V	Vout = 2	V _{OUT} = 2.5V		0.2			
Dropout Voltage (ILOAD = 300mA)	VDROP_1.8V	Vout = 1	.8V		0.25		V	
(Note 3)	VDROP_1.5V	Vout = 1	.5V		0.31			
	VDROP_1.2V	V _{OUT} = 1	.2V		0.41			
	VDROP_0.9V	Vout = 0	.9V		0.61			
Ground Current	lα	$I_{LOAD} = 0$)mA		2		μA	
Shutdown Ground Current	I _{SD}	V _{EN} = 0\	<i>I</i> ,		0.01	0.5		
VOUT Shutdown Leakage Current	ILEAK	V _{OUT} = 0V			0.01	0.5	μA	
SNS Input Current	Isns	SNS = \	/оит		0.7		μA	
	VIH	EN Rising				2		
Enable Threshold Voltage	VIL	EN Falling		0.6			V	
EN Input Current	I _{EN}	V _{EN} = 5V			10	100	nA	
Line Regulation	ΔLINE	$I_{LOAD} = 30 \text{mA},$ 1.5V $\leq V_{IN} \leq 5.5 \text{V or}$ (V _{OUT} + 0.2V) $\leq V_{IN} \leq 5.5 \text{V}$			0.2		%	
Load Regulation	ΔLOAD	$10mA \le I_{LOAD} \le 0.3A$			0.2		%	
Output Current Limit	ILIM	Vout =0		301	600		mA	
		Vout	f = 100Hz		80			
Power Supply Rejection Ratio (I _{LOAD} =5mA)	PSRR	=1.2V, V _{IN} = 2V	f = 1kHz		75		dB	
		V _{IN} =	V _{OUT} =0.9V		40			
Output Voltage Noise (BW = 10Hz to 100kHz, C _{OUT} =1µF,)		3.5V I _{LOAD} =0.1A	V _{OUT} =2.8V		50		µVrмs	
Thermal Shutdown Temperature T _{SD}					155		°C	
Thermal Shutdown Hysteresis	ΔT _{SD}	- I _{LOAD} =10mA			15		°C	



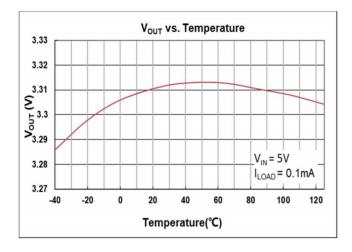
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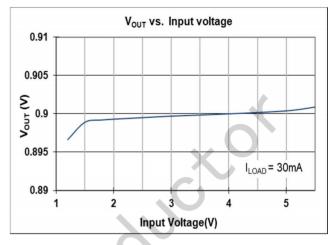
- **Note 1.** Stresses beyond those listed "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions may affect device reliability.
- **Note 2.** θ_{JA} is measured at $T_A = 25^{\circ}C$ on a DSTECH EVB board.
- **Note 3.** $V_{DROP} = V_{IN} V_{OUT}$ when the V_{OUT} is 98% of its target value.

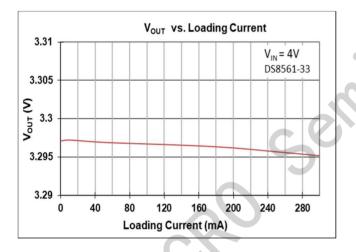
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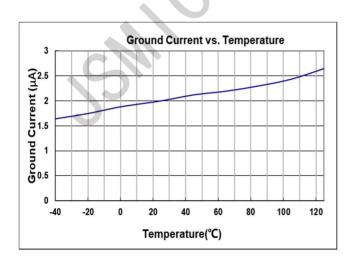


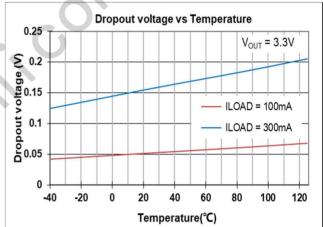
Typical Characteristics

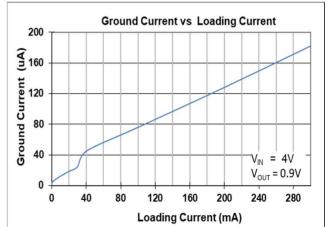






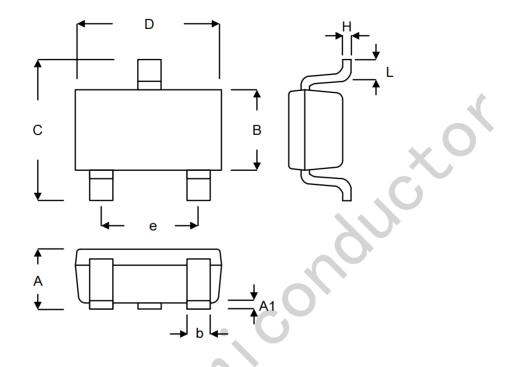






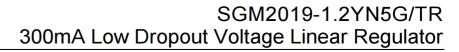


Package Information:

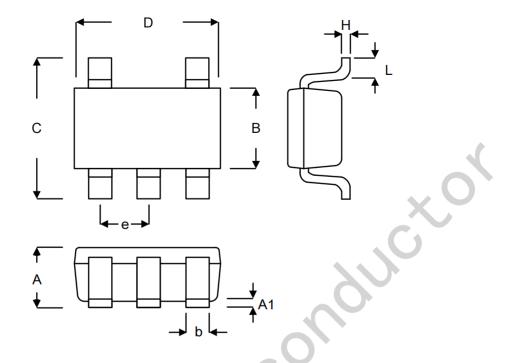


Symbol	Millim	eters	Inches					
Symbol	Min.	Max.	Min.	Max.				
А	0.889	1.295	0.035	0.051				
A1	0.000	0.152	0.000	0.006				
В	1.397	1.803	0.055	0.071				
b	0.250	0.560	0.010	0.022				
С	2.591	2.997	0.102	0.118				
D	2.692	3.099	0.106	0.122				
е	1.803	2.007	0.071	0.079				
Н	0.080	0.254	0.003	0.010				
L	0.300	0.610	0.012	0.024				

SOT-23-3L



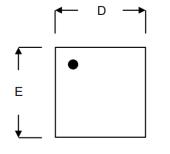


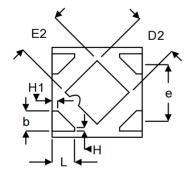


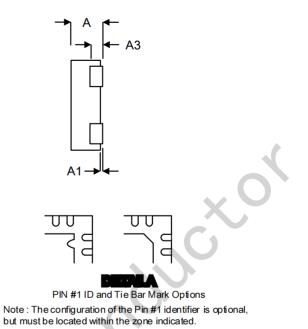
Symbol	Millim	eters	Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	0.889	1.295	0.035	0.051	
A1	0.000	0.152	0.000	0.006	
В	1.397	1.803	0.055	0.071	
b	0.250	0.560	0.010	0.022	
С	2.591	2.997	0.102	0.118	
D	2.692	3.099	0.106	0.122	
е	0.838	1.041	0.033	0.041	
Н	0.080	0.254	0.003	0.010	
Ĺ	0.300	0.610	0.012	0.024	

SOT-23-5L





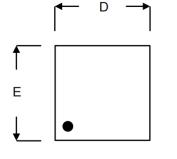


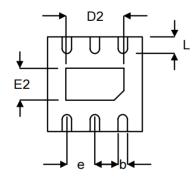


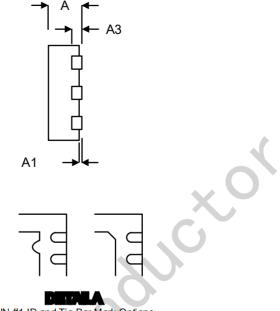
Symbol	Millim	eters	Inches					
Symbol	Min.	Max.	Min.	Max.				
Α	0.300	0.400	0.012	0.016				
A1	0.000	0.050	0.000	0.002				
A3	0.117	0.162	0.005	0.006				
b	0.175	0.280	0.007	0.011				
D	0.900	1.100	0.035	0.043				
D2	0.430	0.550	0.017	0.022				
Е	0.900	1.100	0.035	0.043				
E2	0.430	0.550	0.017	0.022				
е	0.6	50	0.026					
L	0.200	0.300	0.008	0.012				
Н	0.0	39	0.002					
H1	0.0)64	0.003					

DFN-1X1-4L









PIN #1 ID and Tie Bar Mark Options Note : The configuration of the Pin #1 identifier is optional, but must be located within the zone indicated.

Symbol	Millim	ieters	Inches			
Symbol	Min.	Max.	Min.	Max.		
Α	0.700	0.800	0.028	0.031		
A1	0.000	0.050	0.000	0.002		
A3	0.175	0.250	0.007	0.010		
b	0.200	0.350	0.008	0.014		
D	1.950	2.050	0.077	0.081		
D2	1.000	1.450	0.039	0.057		
E	1.950	2.050	0.077	0.081		
E2	0.500	0.850	0.020	0.033		
е	0.6	650	0.0)26		
L	0.300	0.400	0.012	0.016		

DFN-2X2-6L

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