



S2AB THRU S2MB

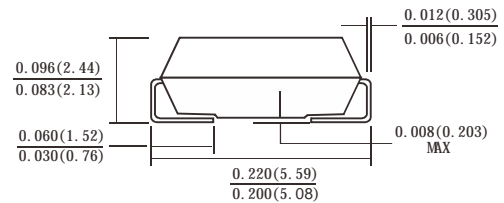
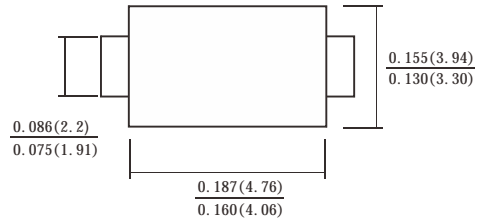
Surface Mount Standard Rectifiers

Features

- Low profile package
- Ideal for automated placement
- Glass passivated chip junctions
- Fast switching for high efficiency
- High forward surge capability
- High temperature soldering:
260°C/10 seconds at terminals
- Component in accordance to
RoHS 2002/95/1 and WEEE 2002/96/EC



SMB(DO-214AA)



Dimensions in inches and (millimeters)

Mechanical Date

- **Case:** JEDEC DO-214AA molded plastic body over glass passivated chip
- **Terminals:** Solder plated, solderable per J-STD-002B and JESD22-B102D
- **Polarity:** Laser band denotes cathode end

Maximum Ratings and Electrical Characteristics Rating at 25 °C

ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate current by 20%

Type Number	SYMBOL	S2AB	S2BB	S2DB	S2GB	S2JB	S2KB	S2MB	Unit
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Average Rectified Output Current @ $T_L = 100^\circ\text{C}$	$I_{F(AV)}$	2.0							A
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	50							A
Forward Voltage @ $I_F = 2.0\text{A}$	V_{FM}	1.0							V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$	I_R	5.0							uA
At Rated DC Blocking Voltage @ $T_A = 125^\circ\text{C}$		100							
I^2t Rating for fusing ($t < 8.3\text{ms}$)	I^2t	10.37							A ² s
Typical Junction Capacitance (Note 1)	C_J	30							pF
Typical Thermal Resistance Junction to Ambient (Note 2)	$R_{\theta JA}$	50							°C/W
Operating Temperature Range	T_J	-55 to +150							°C
Storage Temperature Range	T_{STG}	-55 to +150							°C

Note: 1. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

2. Thermal Resistance from Junction to Ambient at 0.375(9.5mm) lead length .



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Characteristic Curves ($T_A=25^\circ\text{C}$ unless otherwise noted)

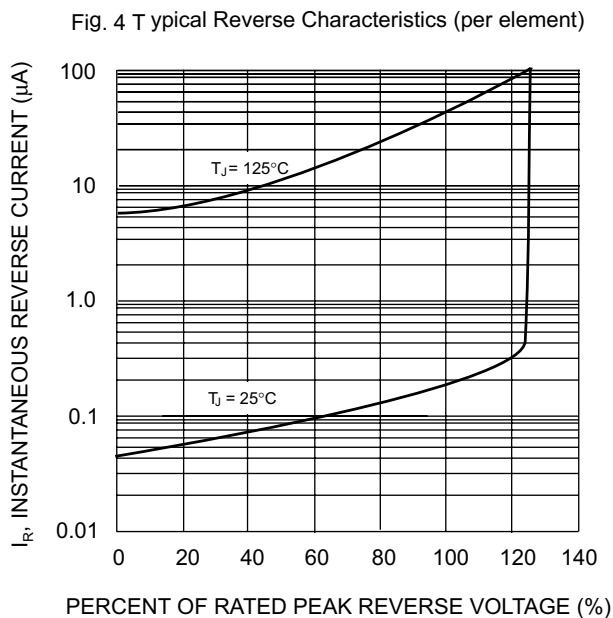
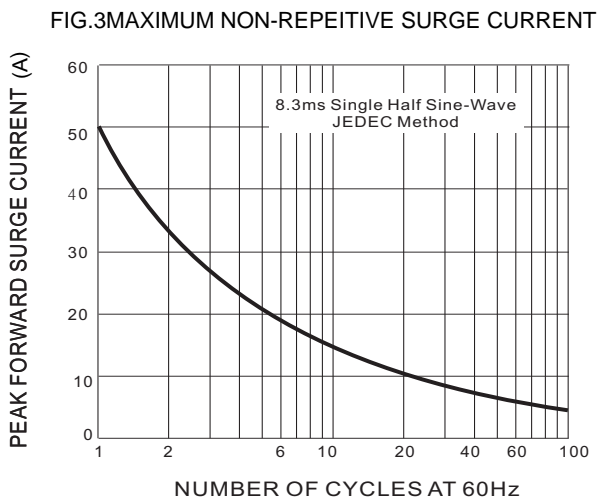
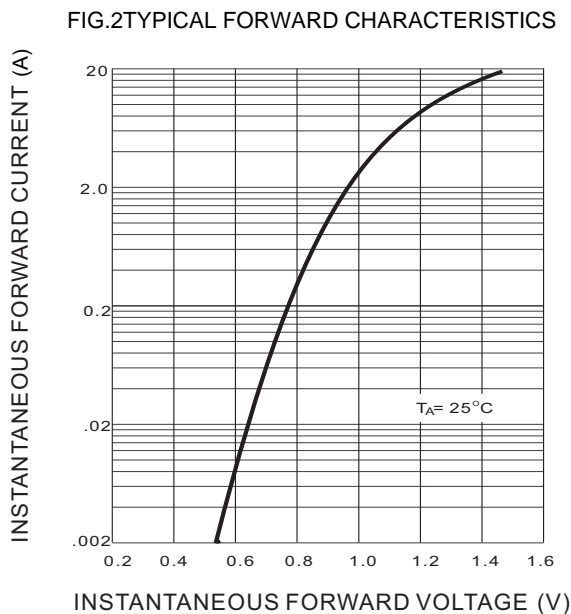
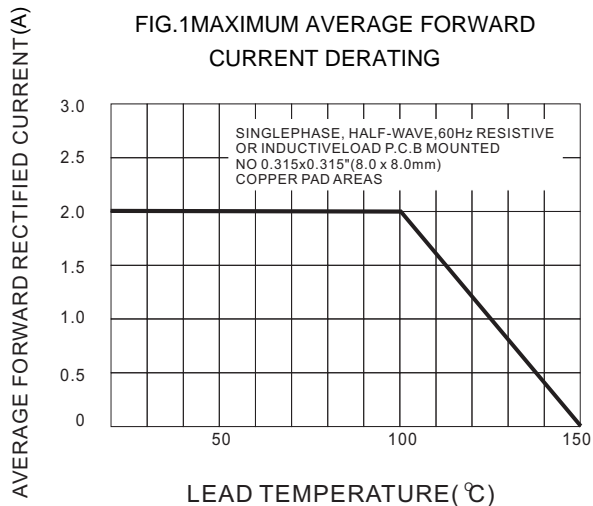
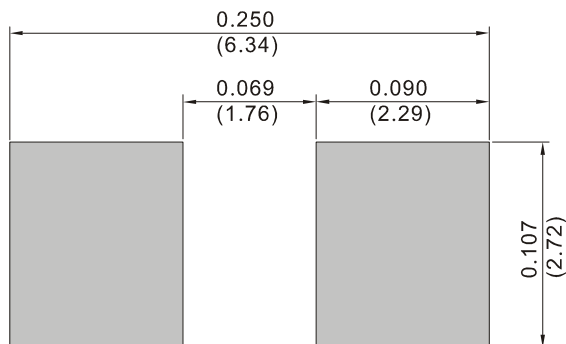


FIG.5 MOUNTING PAD LAYOUT



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