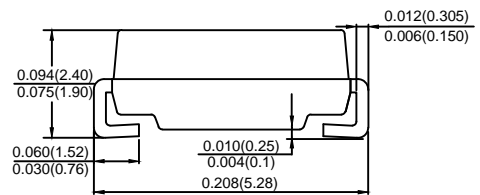
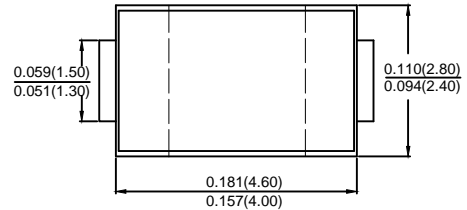




# SS36L

## Surface Mount Low VF Schottky Rectifiers

**SMA/DO-214AC**



Dimensions in inches and (millimeters)

### FEATURES

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

### MECHANICAL DATA

- Case: SMA
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.055g / 0.002oz

### Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

Parameter	Symbols	SS36L	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	60	V
Maximum RMS voltage	$V_{RMS}$	42	V
Maximum DC Blocking Voltage	$V_{DC}$	60	V
Maximum Average Forward Rectified Current at $T_c=100\text{ }^\circ\text{C}$	$I_{F(AV)}$	3	A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	$I_{FSM}$	80	A
Maximum Instantaneous Forward Voltage at 3 A	$V_F$	0.5	V
Maximum DC Reverse Current $T_a = 25\text{ }^\circ\text{C}$ at Rated DC Blocking Voltage $T_a = 100\text{ }^\circ\text{C}$	$I_R$	0.3 5.0	mA
Typical Junction Capacitance <sup>(1)</sup>	$C_j$	400	pF
Typical Thermal Resistance <sup>(2)</sup>	$R_{\theta JA}$	60	$^\circ\text{C/W}$
Operating Junction Temperature Range	$T_j$	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-55 ~ +150	$^\circ\text{C}$

(1) Measured at 1 MHz and applied reverse voltage of 4 V D.C

(2) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



Fig.1 Forward Current Derating Curve

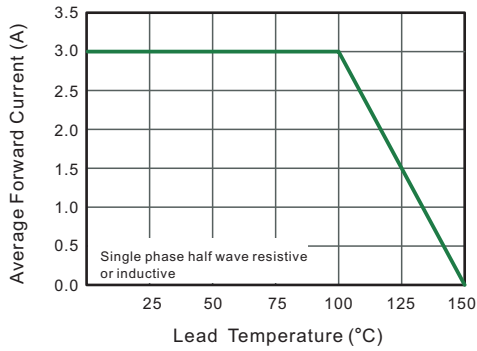


Fig.2 Typical Reverse Characteristics

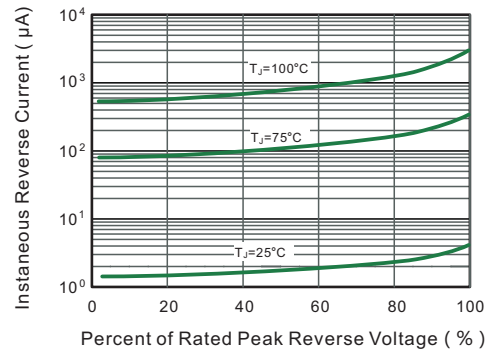


Fig.3 Typical Forward Characteristic

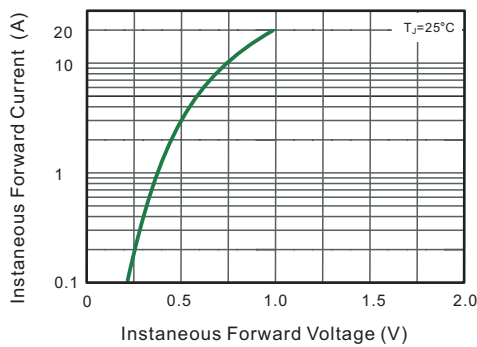


Fig.4 Typical Junction Capacitance

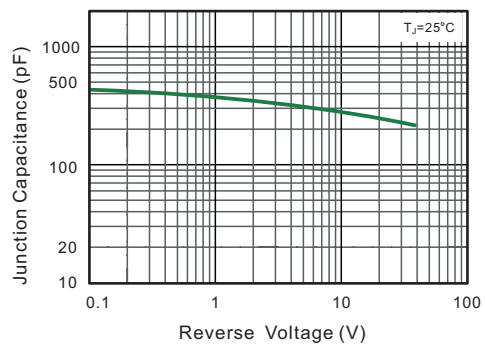


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

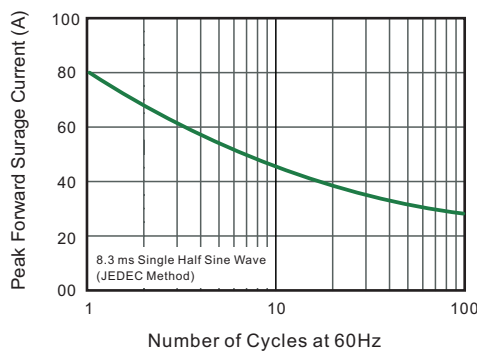
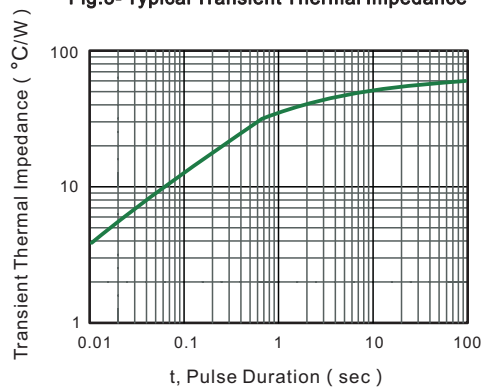


Fig.5- Typical Transient Thermal Impedance



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