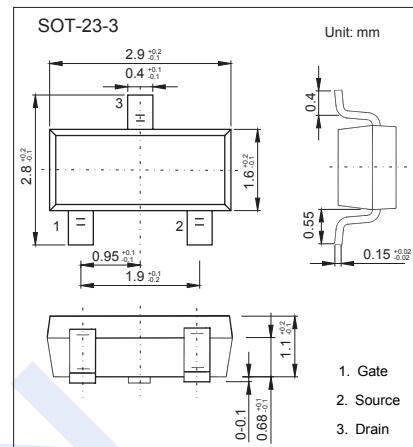
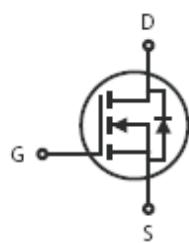


N-Channel Enhancement MOSFET

SI2300 (KI2300)

■ Features

- $V_{DS}=20V$
- $I_D=5.0A$
- $R_{DS(ON)}=25m\Omega @ V_{GS}=4.5V, I_D=5.0A$
- $R_{DS(ON)}=35m\Omega @ V_{GS}=2.5V, I_D=4.0A$
- $R_{DS(ON)}=55m\Omega @ V_{GS}=1.8V, I_D=1.0A$



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 10	
Continuous Drain Current $T_j=125^\circ C$	I_D	5.0	A
Pulsed Drain Current	I_{DM}	15	
Power Dissipation	P_D	1.25	W
Thermal Resistance Junction- to-Ambient	R_{thJA}	100	$^\circ C/W$
Junction Temperature	T_j	150	$^\circ C$
Storage Temperature Range	T_{stg}	-55 to 150	

SI2300 (KI2300)■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20\text{V}, V_{GS}=0\text{V}$			1	μA
Gate-Body Leakage	I_{GSS}	$V_{GS}=\pm 10\text{V}, V_{DS}=0\text{V}$			± 100	nA
Gate Threshold Voltage *	$V_{GS(\text{th})}$	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	0.4	0.7	1.0	V
Drain- Source on-state Resistance *	$R_{DS(\text{ON})}$	$V_{GS}=4.5\text{V}, I_D=5.0\text{A}$		20	25	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=4.0\text{A}$		27	35	$\text{m}\Omega$
		$V_{GS}=1.8\text{V}, I_D=1.0\text{A}$		39	55	$\text{m}\Omega$
Forward Transconductance *	g_{FS}	$V_{DS}=5\text{V}, I_D=5\text{A}$	5			S
Input Capacitance	C_{ISS}	$V_{DS} = 15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$		887		pF
Output Capacitance	C_{OSS}			144		pF
Reverse Transfer Capacitance	C_{RSS}			115		pF
Turn-On Delay Time	$t_{D(on)}$	$V_{DD}=10\text{V}, I_D=1\text{A}, V_{GS}=4.5\text{V}, R_L=10\Omega, R_{GEN}=6\Omega$		31.8		ns
Rise Time	t_r			14.5		ns
Turn-Off Delay Time	$t_{D(off)}$			50.3		ns
Fall Time	t_f			31.9		ns
Total Gate Charge	Q_g	$V_{DS} = 10\text{V}, I_D = 3.5\text{A}, V_{GS} = 4.5\text{V}$		16.8		nC
Gate-S ource Charge	Q_{gs}			2.5		nC
Gate-Drain Charge	Q_{gd}			5.4		nC
Drain-Source Diode Forward Current *	I_S				1.25	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}, I_S=1.25\text{A}$		0.825	1.2	V

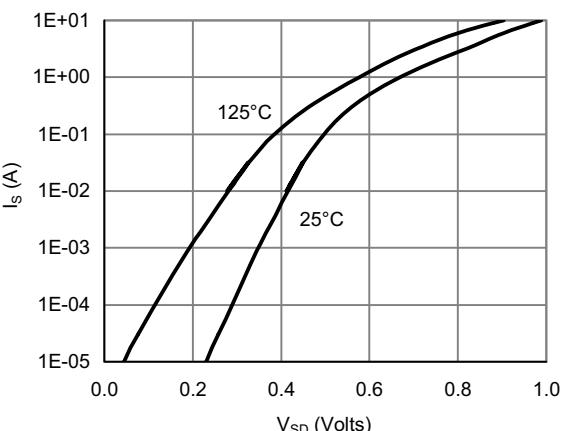
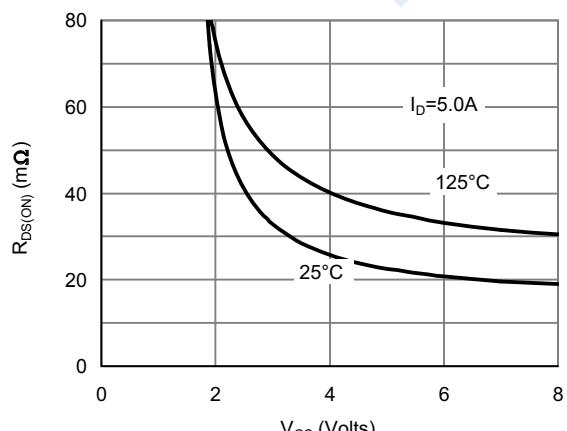
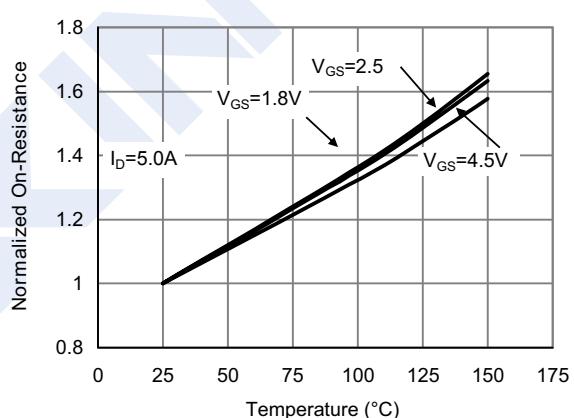
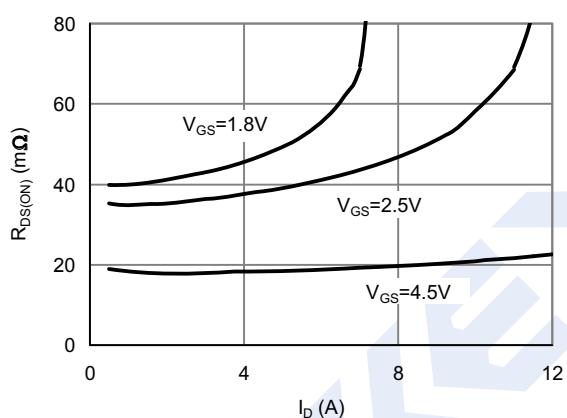
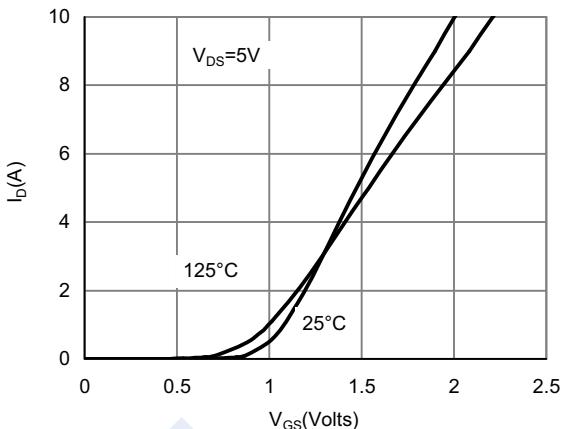
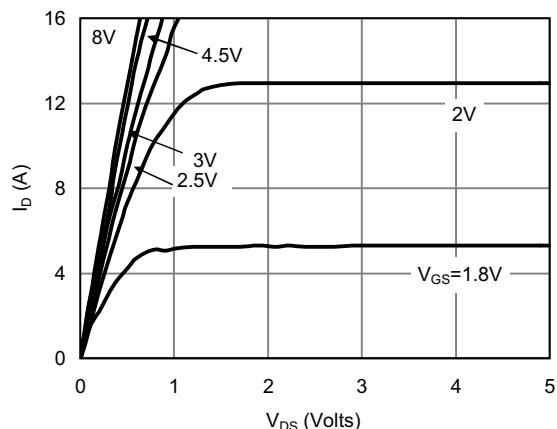
* Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$, Duty Cycle $\leq 2\%$

■ Marking

Marking	00A*
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SI2300 (KI2300)

■ Typical Characteristics



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