

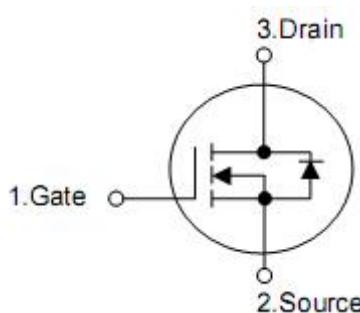
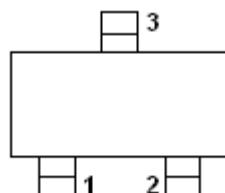
## 1. Description

The KIA3400 uses advanced trench technology to provide excellent  $R_{DS(on)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications. Standard Product KIA3400 is Pb-free(meets ROHS & Sony 259 specifications). KIA3400 is a Green Product ordering option. KIA3400 is electrically identical.

## 2. Features

- $V_{DS(V)}=30V$
- $R_{DS(on)}<40m\Omega(V_{GS}=10V, I_D=4.8A)$
- $R_{DS(on)}<42m\Omega(V_{GS}=4.5V, I_D=4.0A)$
- $R_{DS(on)}<55m\Omega(V_{GS}=2.5V, I_D=3.5A)$

## 3. Symbol



Pin	Function
1	Gate
2	Source
3	Drain

## 4. Absolute maximum ratings

( $T_A=25^\circ\text{C}$ ,unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-source voltage	$V_{DS}$	30	V
Gate-source voltage	$V_{GS}$	$\pm 12$	V
Continuous drain current <sup>A</sup>	$I_D$	4.8	A
Pulsed drain current <sup>B</sup>	$I_{DM}$	30	A
Total power dissipation <sup>A</sup>	$P_D$	1.4	W
$T_A=70^\circ\text{C}$		1	W
Junction and storage temperature range	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

## 5. Thermal characteristics

Parameter	Symbol	Typ	Max	Unit
Maximum junction-ambient <sup>A</sup> ( $t \leq 10\text{s}$ )	$R_{\theta JA}$	65	90	$^\circ\text{C/W}$
Maximum junction-ambient <sup>A</sup>	$R_{\theta JA}$	85	125	$^\circ\text{C/W}$
Maximum junction-Lead <sup>C</sup>	$R_{\theta JL}$	43	60	$^\circ\text{C/W}$

## 6. Electrical characteristics

( $T_A=25^\circ\text{C}$ ,unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain-source breakdown voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
Zero gate voltage drain current	$I_{\text{DSS}}$	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate- body leakage current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	-	-	100	nA
Gate threshold voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.6	-	-	V
On state drain current	$I_{\text{D(on)}}$	$V_{\text{GS}}=4.5\text{V}, V_{\text{DS}}=5\text{V}$	30	-	-	A
Static drain-source on-resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=4.8\text{A}$	-	-	40	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=4.0\text{A}$	-	-	42	
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=3.5\text{A}$	-	-	55	
Forward transconductance	$g_{\text{fs}}$	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=4.8\text{A}$	10	15	-	S
Diode forward voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=1\text{A}$	-	0.71	1.2	V
Maximum body-diode continuous current	$I_{\text{S}}$		-	-	2.5	A
Input capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	823	1030	$\text{pF}$
Output capacitance	$C_{\text{oss}}$		-	99	-	
Reverse transfer capacitance	$C_{\text{rss}}$		-	77	-	
Gate resistance	$R_g$	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	-	1.2	3.6	$\Omega$
Total gate charge	$Q_g$	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=4.5\text{V}, I_{\text{D}} = 5.8\text{A}$	-	9.7	12	$\text{nC}$
Gate-source charge	$Q_{\text{gs}}$		-	1.6	-	
Gate-drain charge	$Q_{\text{gd}}$		-	3.1	-	
Turn-on delay time	$t_{\text{d(on)}}$	$V_{\text{DS}}=15\text{V}, R_L=2.7\Omega, R_G=3\Omega, V_{\text{GS}}=10\text{V}$	-	3.3	5	$\text{ns}$
Rise time	$t_r$		-	4.8	7	
Turn-off delay time	$t_{\text{d(off)}}$		-	26.3	40	
Fall time	$t_f$		-	4.1	6	
Reverse recovery time	$t_{\text{rr}}$	$\text{IF}=5\text{A}, \text{dI}/\text{dt}=100\text{A}/\mu\text{s},$	-	16	20	$\text{nS}$
Reverse recovery charge	$Q_{\text{rr}}$		-	8.9	12'	$\text{nC}$

Note:A.The value of  $R_{\Theta_{\text{JA}}}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.Copper,in a still air environment with  $T_A=25^\circ\text{C}$ .The value in any given application depends on the user's specific board design.The current rating is based on the  $t \leq 10\text{s}$  thermal resistance rating.

B.Repetitive rating,pulse width limited by junction temperature.

C.The  $R_{\Theta_{\text{JA}}}$  the sum of the thermal inpedence from junction to lead  $R_{\Theta_{\text{jl}}}$  and lead to ambient.

D.The static characteristics in Figures 1 to 6,12,14 are obtained using 80 $\mu\text{s}$  pulses,duty cycle 0.5% max.

E.These tests are performed with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. Copper,in a still air environment with  $T_A=25^\circ\text{C}$ .The SOA curve provides a single pulse rating.

## 7. Test circuits and waveforms

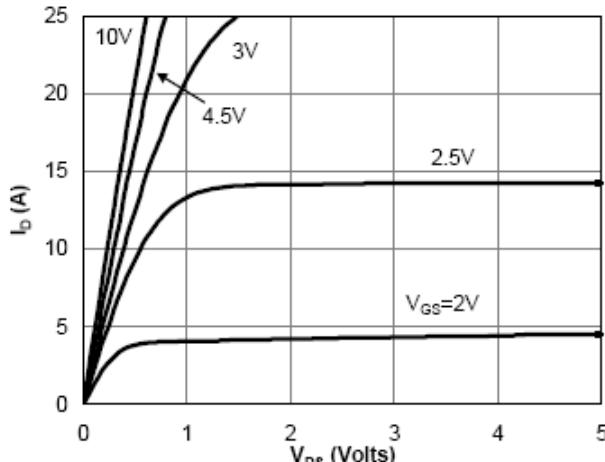


Fig 1: On-Region Characteristics

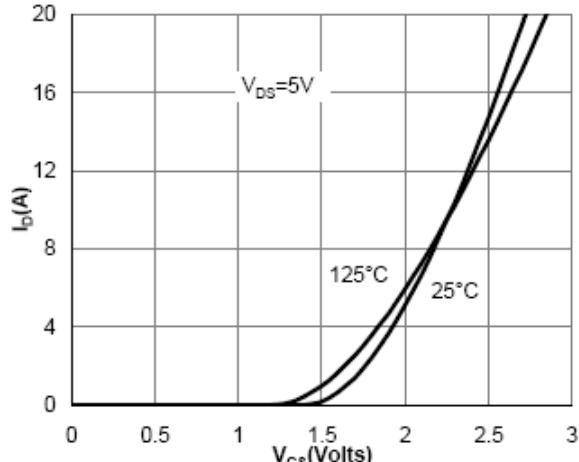


Figure 2: Transfer Characteristics

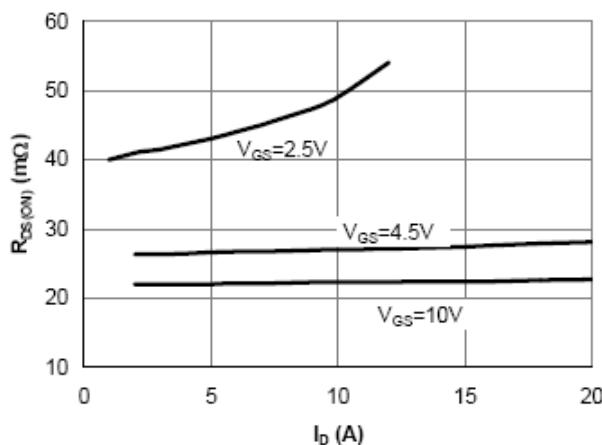


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

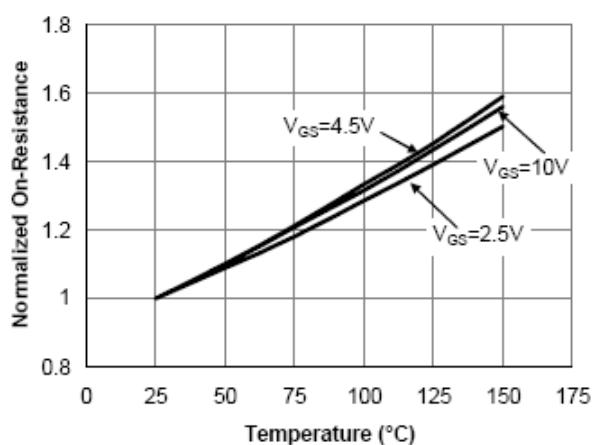


Figure 4: On-Resistance vs. Junction Temperature

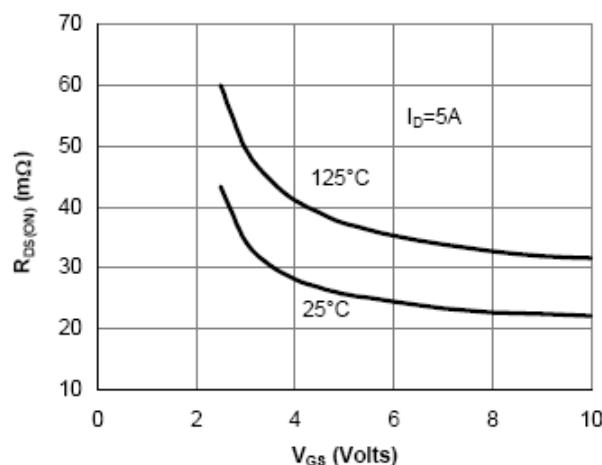


Figure 5: On-Resistance vs. Gate-Source Voltage

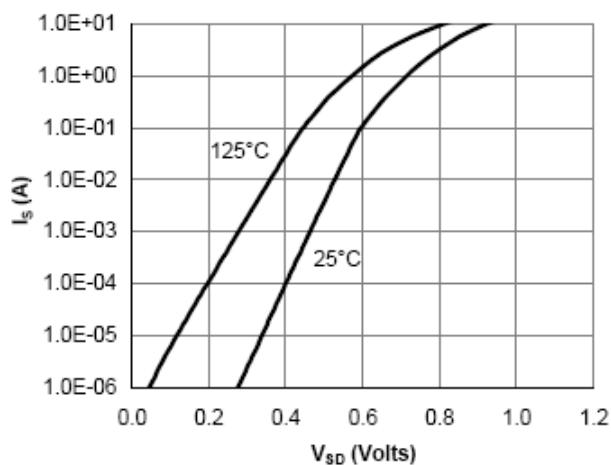


Figure 6: Body-Diode Characteristics

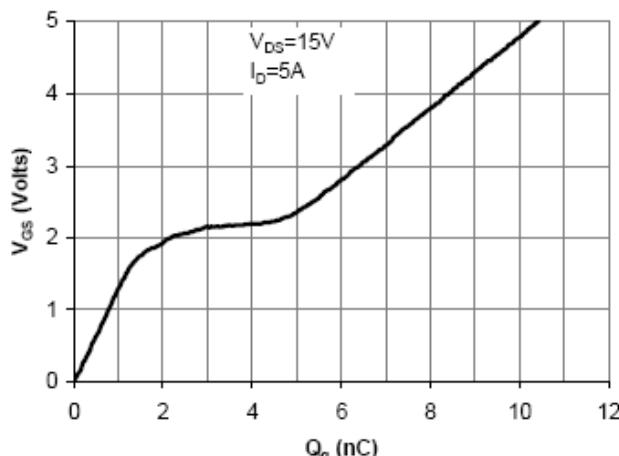


Figure 7: Gate-Charge Characteristics

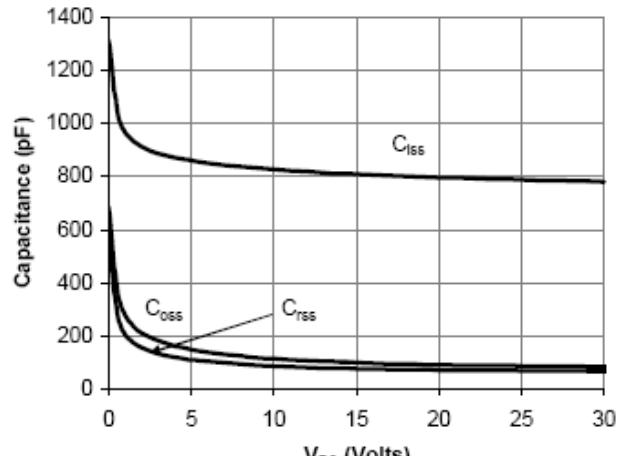


Figure 8: Capacitance Characteristics

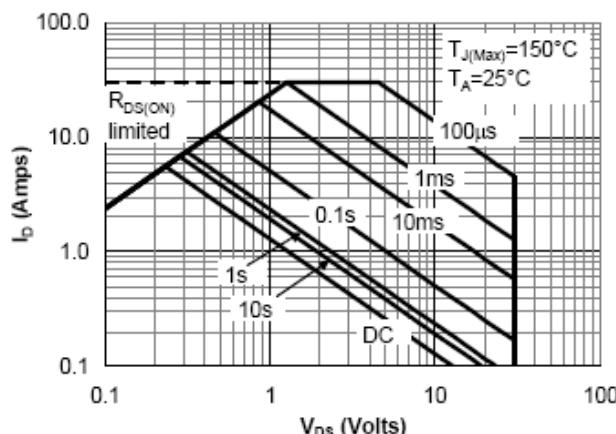


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

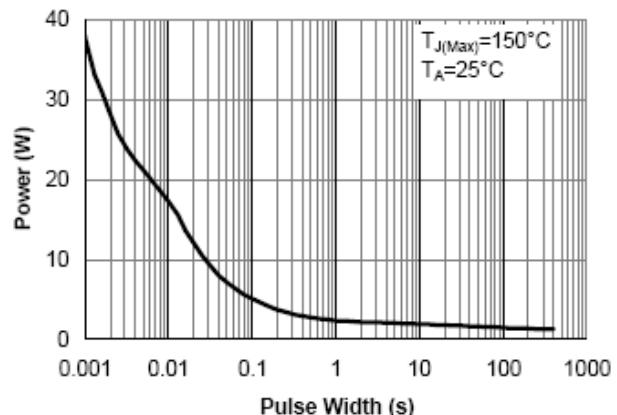


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

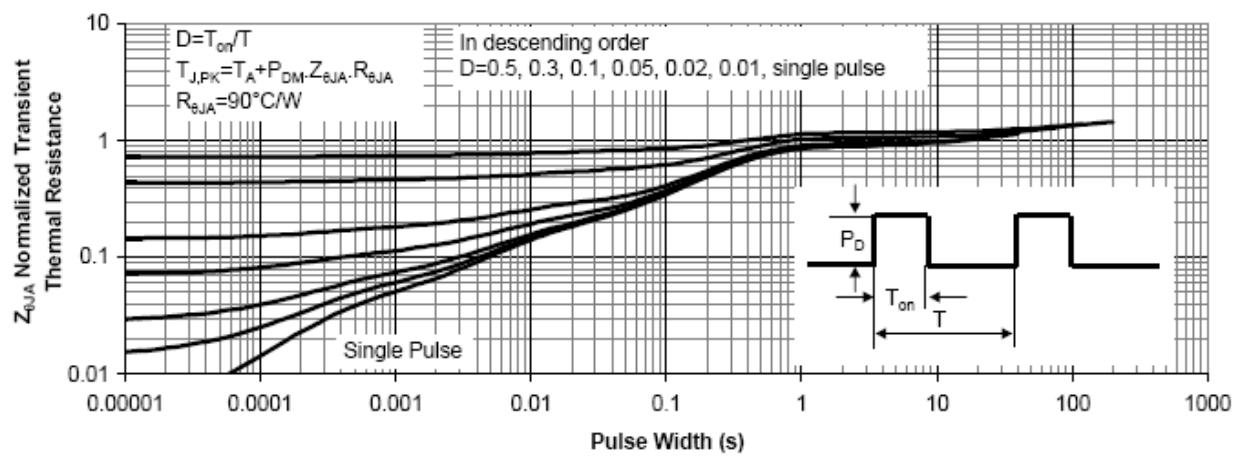


Figure 11: Normalized Maximum Transient Thermal Impedance

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