

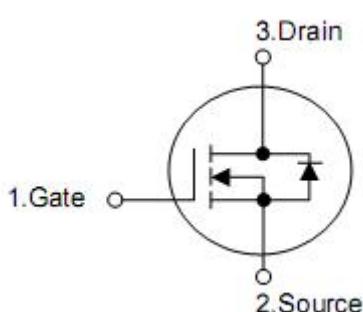
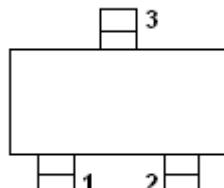
## 1. Description

The KIA3402 uses advanced trench technology to provide excellent  $R_{DS(on)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications. KIA3402(Green Product) is offered in a lead-free package.

## 2. Features

- $V_{DS(V)}=30V$
- $I_D=4.0A$
- $R_{DS(on)}<70m\Omega(V_{GS}=10V, I_D=4.0A)$
- $R_{DS(on)}<75m\Omega(V_{GS}=4.5V, I_D=2.3A)$
- $R_{DS(on)}<105m\Omega(V_{GS}=2.5V, I_D=1.5A)$

## 3. Symbol



| Pin | Function |
|-----|----------|
| 1   | Gate     |
| 2   | Source   |
| 3   | Drain    |

#### 4. Absolute maximum ratings

( $T_A=25^\circ\text{C}$ ,unless otherwise noted)

| Parameter                              |                        | Symbol         | Rating     | Units |
|--|------------------------|----------------|------------|-------|
| Drain-source voltage                   |                        | $V_{DS}$       | 30         | V     |
| Gate-source voltage                    |                        | $V_{GS}$       | $\pm 12$   | V     |
| Continuous drain current <sup>A</sup>  | $T_A=25^\circ\text{C}$ | $I_D$          | 4.0        | A     |
|  | $T_A=70^\circ\text{C}$ |                | 3.4        |       |
| Pulsed drain current <sup>B</sup>      |                        | $I_{DM}$       | 15         | A     |
| Total power dissipation <sup>A</sup>   | $T_A=25^\circ\text{C}$ | $P_D$          | 1.4        | W     |
|  | $T_A=70^\circ\text{C}$ |                | 1          | W     |
| Junction and storage temperature range |                        | $T_J, T_{STG}$ | -55 to 150 | °C    |

#### 5. Thermal characteristics

| Parameter   | Symbol          | Typ | Max | Unit |
|---|-----------------|-----|-----|------|
| Maximum junction-ambient <sup>A</sup> ( $t \leq 10\text{s}$ ) | $R_{\theta JA}$ | 70  | 90  | °C/W |
| Maximum junction-ambient <sup>A</sup>                         | $R_{\theta JA}$ | 100 | 125 | °C/W |
| Maximum junction-Lead <sup>C</sup>                            | $R_{\theta JL}$ | 63  | 80  | °C/W |

## 6. Electrical characteristics

( $T_A=25^\circ\text{C}$ ,unless otherwise noted)

| Parameter                             | Symbol                     | Test Conditions   | Min | Typ  | Max | Units            |
|---------------------------------------|----------------------------|---|-----|------|-----|------------------|
| Drain-source breakdown voltage        | $\text{BV}_{\text{DSS}}$   | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$                            | 30  | -    | -   | V                |
| Zero gate voltage drain current       | $I_{\text{DSS}}$           | $V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$                               | -   | -    | 1   | $\mu\text{A}$    |
| Gate- body leakage current            | $I_{\text{GSS}}$           | $V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$                           | -   | -    | 100 | nA               |
| Gate threshold voltage                | $V_{\text{GS}(\text{th})}$ | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$                        | 0.8 | 1    | 1.6 | V                |
| On state drain current                | $I_{\text{D}(\text{on})}$  | $V_{\text{GS}}=4.5\text{V}, V_{\text{DS}}=5\text{V}$                              | 10  | -    | -   | A                |
| Static drain-source on-resistance     | $R_{\text{DS}(\text{on})}$ | $V_{\text{GS}}=10\text{V}, I_{\text{D}}=4.0\text{A}$                              | -   | -    | 70  | $\text{m}\Omega$ |
|                                       |                            | $V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=2.3\text{A}$                             | -   | -    | 75  |                  |
|                                       |                            | $V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=1.5\text{A}$                             | -   | -    | 105 |                  |
| Forward transconductance              | $g_{\text{fs}}$            | $V_{\text{DS}}=5.0\text{V}, I_{\text{D}}=4.0\text{A}$                             | -   | 8.0  | -   | S                |
| Diode forward voltage                 | $V_{\text{SD}}$            | $V_{\text{GS}}=0\text{V}, I_{\text{S}}=1\text{A}$                                 | -   | 0.8  | 1.2 | V                |
| Maximum body-diode continuous current | $I_{\text{S}}$             |   | -   | -    | 2.5 | A                |
| Input capacitance                     | $C_{\text{iss}}$           | $V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$                | -   | 390  | -   | $\text{pF}$      |
| Output capacitance                    | $C_{\text{oss}}$           |   | -   | 54.5 | -   |                  |
| Reverse transfer capacitance          | $C_{\text{rss}}$           |   | -   | 41   | -   |                  |
| Gate resistance                       | $R_g$                      | $V_{\text{DS}}=0\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$                 | -   | 3    | -   | $\Omega$         |
| Total gate charge                     | $Q_g$                      | $V_{\text{DS}}=15\text{V}, V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=4.0\text{A}$   | -   | 4.34 | -   | $\text{nC}$      |
| Gate-source charge                    | $Q_{\text{gs}}$            |   | -   | 0.6  | -   |                  |
| Gate-drain charge                     | $Q_{\text{gd}}$            |   | -   | 1.38 | -   |                  |
| Turn-on delay time                    | $t_{\text{d}(\text{on})}$  | $V_{\text{DS}}=15\text{V}, R_L=3.75\Omega, R_G=6\Omega, V_{\text{GS}}=10\text{V}$ | -   | 3.3  | -   | $\text{ns}$      |
| Rise time                             | $t_r$                      |   | -   | 1    | -   |                  |
| Turn-off delay time                   | $t_{\text{d}(\text{off})}$ |   | -   | 21.7 | -   |                  |
| Fall time                             | $t_f$                      |   | -   | 2.1  | -   |                  |
| Reverse recovery time                 | $t_{\text{rr}}$            | $IF=4.0\text{A}, dI/dt=100\text{A}/\mu\text{s},$                                  | -   | 12   | -   | $\text{nS}$      |
| Reverse recovery charge               | $Q_{\text{rr}}$            |   | -   | 6.3  | -   | $\text{nC}$      |

Note:A.The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.Copper,in a still air environment with  $T_A=25^\circ\text{C}$ .The value in any given application depends on the user's specific board design.The current rating is based on the  $t \leq 10\text{s}$  thermal resistance rating.

B.Repetitive rating,pulse width limited by junction temperature.

C.The  $R_{\theta JA}$  the sum of the thermal impedance from junction to lead  $R_{\theta ji}$  and lead to ambient.

D.The static characteristics in Figures 1 to 6,12,14 are obtained using 80 $\mu\text{s}$  pulses,duty cycle 0.5% max.

E.These tests are performed with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. Copper,in a still air environment with  $T_A=25^\circ\text{C}$ .The SOA curve provides a single pulse rating.

## 7. Test circuits and waveforms

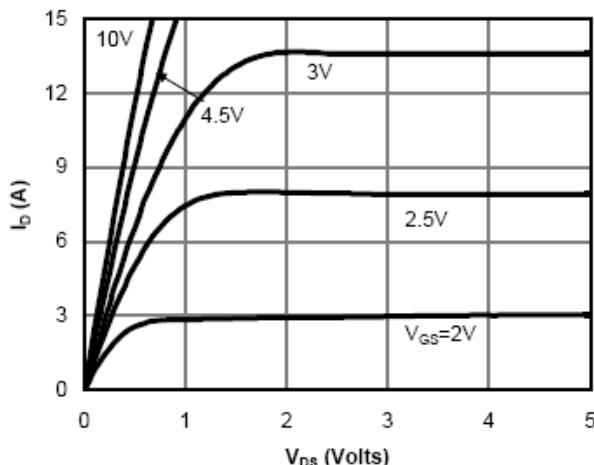


Fig 1: On-Region Characteristics

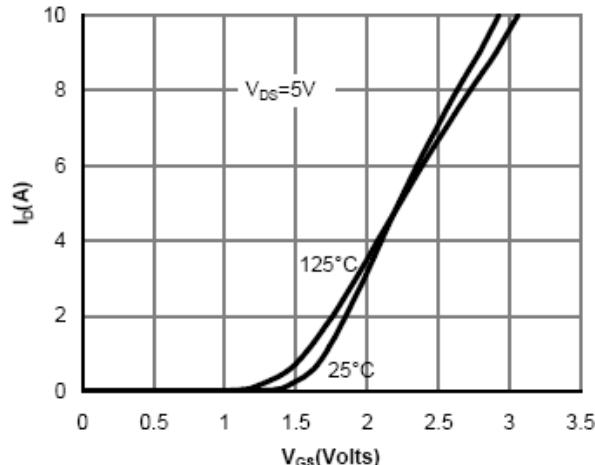


Figure 2: Transfer Characteristics

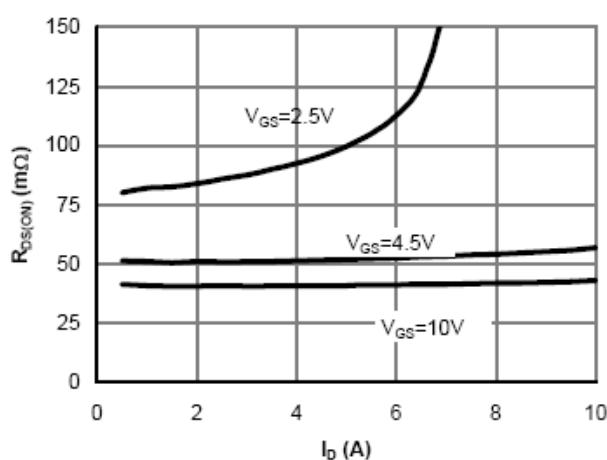


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

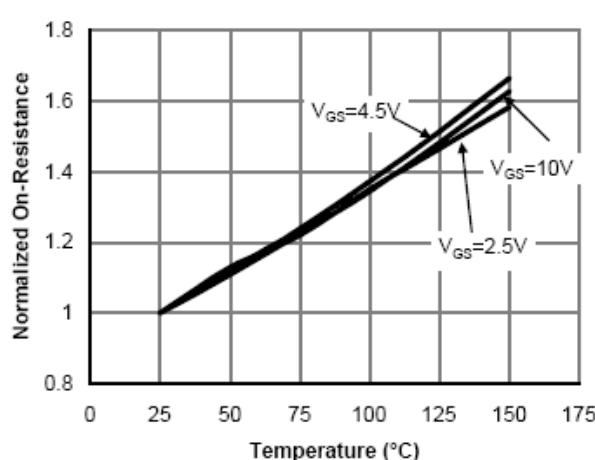


Figure 4: On-Resistance vs. Junction Temperature

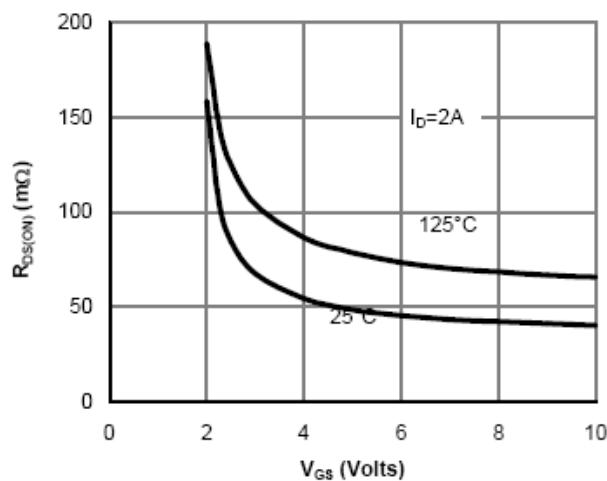


Figure 5: On-Resistance vs. Gate-Source Voltage

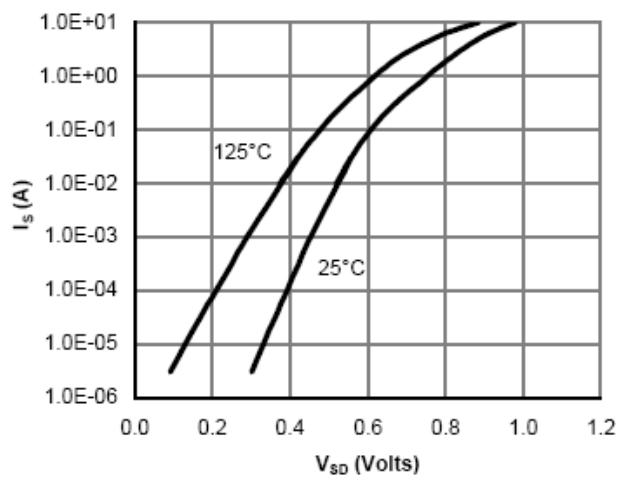


Figure 6: Body-Diode Characteristics

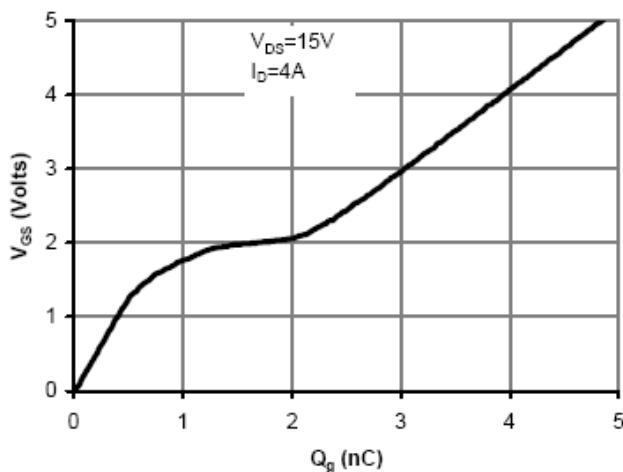


Figure 7: Gate-Charge Characteristics

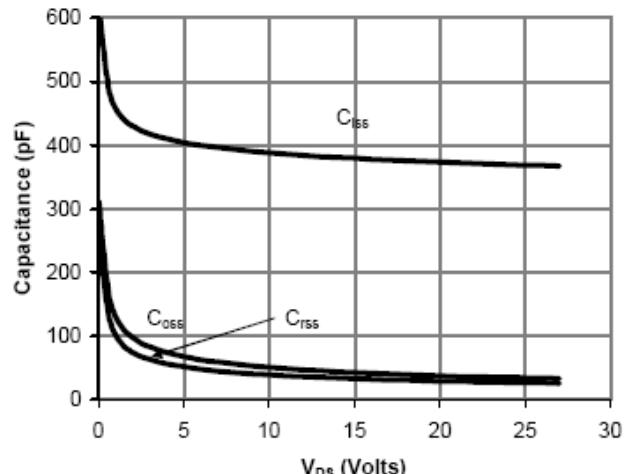


Figure 8: Capacitance Characteristics

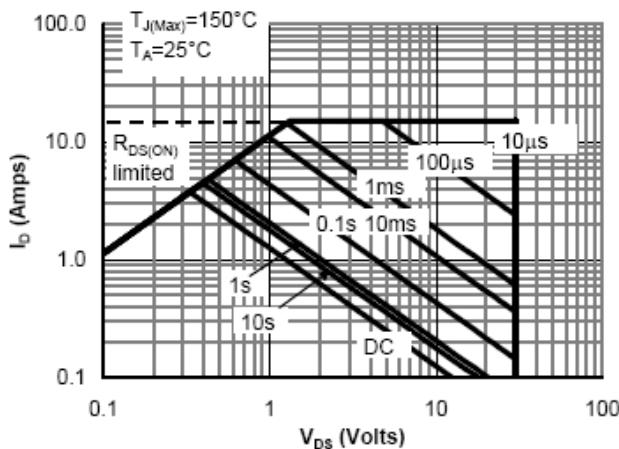


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

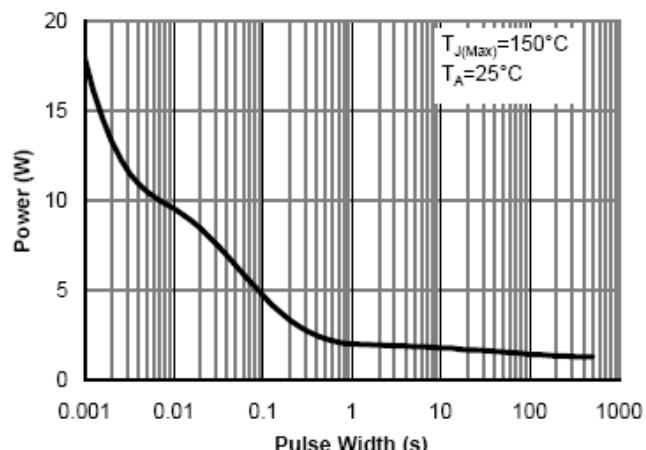


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

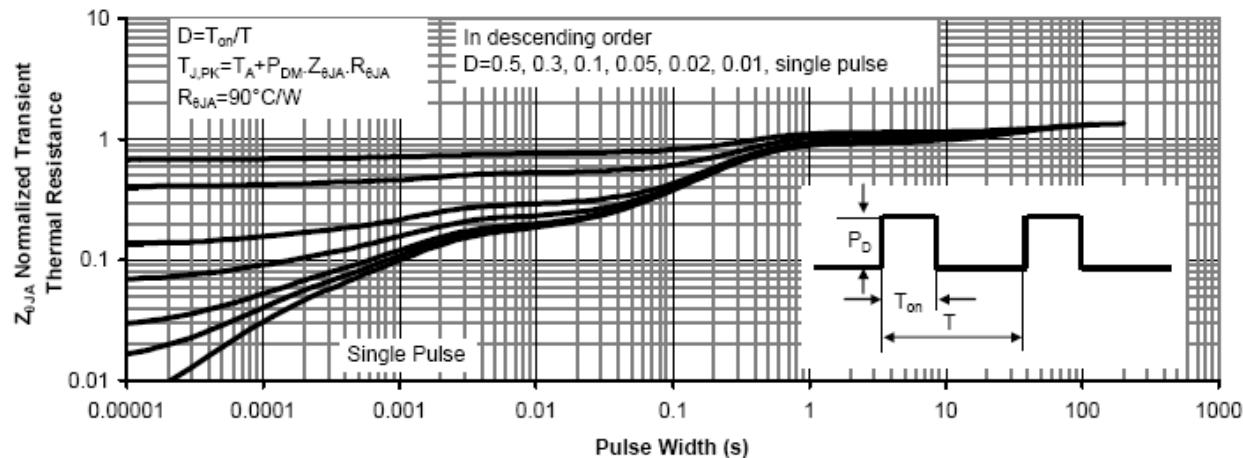


Figure 11: Normalized Maximum Transient Thermal Impedance

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