

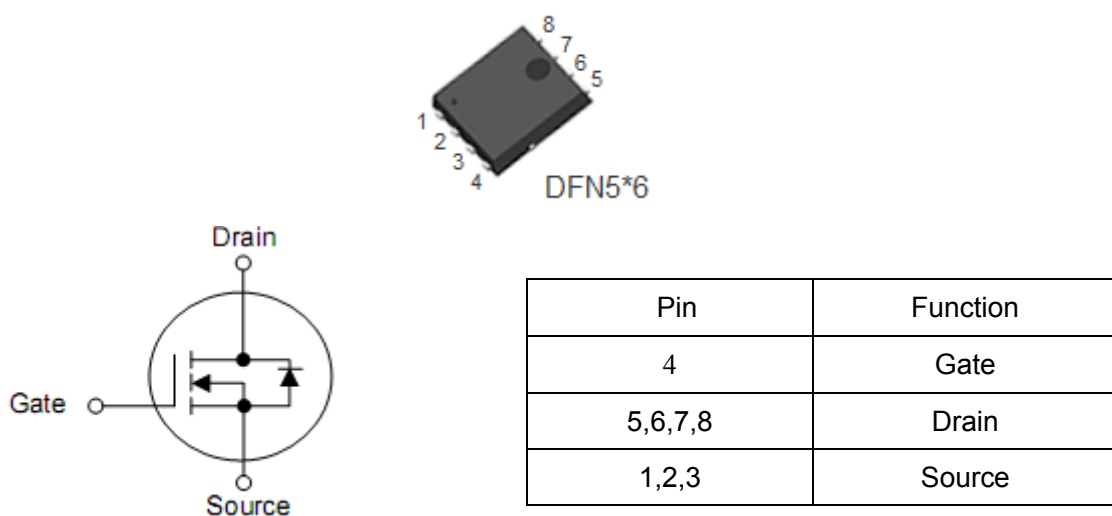
## 1. Features

- $R_{DS(ON)}=4.0m\Omega(\text{typ.})@V_{GS}=10V$
- Very Low On-resistance  $R_{DS(ON)}$
- Low  $C_{rss}$
- Fast switching
- 100% avalanche tested
- Improved  $dv/dt$  capability

## 2. Application

- PWM Application
- Load Switch
- Power Management

## 3. Pin configuration



## 4. Ordering Information

Part Number	Package	Brand
KNY3903A	DFN5*6	KIA

## 5. Absolute maximum ratings

$T_C=25^{\circ}\text{C}$  unless otherwise noted

Parameter	Symbol	Rating	Units
Drain-source voltage	$V_{DSS}$	30	V
Continuous drain current	$T_C=25^{\circ}\text{C}$ $I_D$	85	A
	$T_C=100^{\circ}\text{C}$ $I_D$	55	A
Pulsed drain current -Pulsed <sup>1)</sup>	$I_{DM}$	360	A
Gate-source voltage	$V_{GS}$	$\pm 20$	V
Single pulse avalanche energy <sup>2)</sup>	$E_{AS}$	90	mJ
Power dissipation ( $T_C=25^{\circ}\text{C}$ )	$P_D$	90	W
Operating junction and storage temperature range	$T_J, T_{STG}$	-55 to 150	$^{\circ}\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	300	$^{\circ}\text{C}$

<sup>1)</sup>Drain current limited by maximum junction temperature.

## 6. Thermal Data

Parameter	Symbol	Rating	Unit
Thermal resistance junction-case	$R_{\theta JC}$	1.67	$^{\circ}\text{C/W}$

## 7. Electrical characteristics

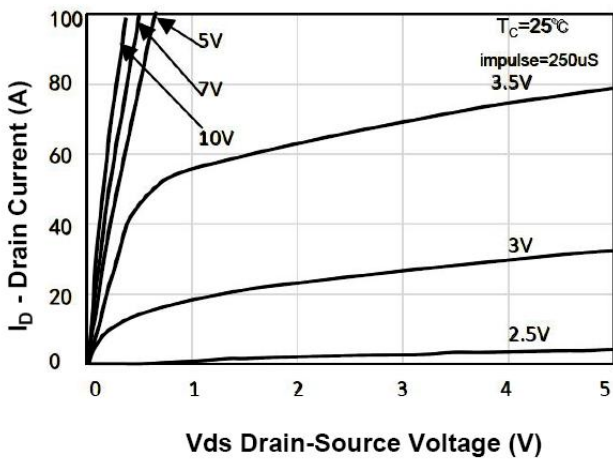
(T<sub>C</sub>=25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	-	-	V
Drain-source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	1	uA
		V <sub>DS</sub> =24V, T <sub>C</sub> =125°C	-	-	10	uA
Gate-source forward leakage	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate threshold voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.0	1.5	2.5	V
Drain-source on-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A	-	4.0	5.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =30A	-	6.0	7.5	mΩ
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V f=1MHz	-	1951	-	pF
Output capacitance	C <sub>oss</sub>		-	322	-	pF
Reverse transfer capacitance	C <sub>rss</sub>		-	240	-	pF
Turn-on delay time	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V R <sub>L</sub> =3Ω, I <sub>D</sub> =30A <sup>3)</sup>	-	12	-	ns
Rise time	t <sub>r</sub>		-	35	-	ns
Turn-off delay time	t <sub>d(off)</sub>		-	42	-	ns
Fall time	t <sub>f</sub>		-	15	-	ns
Total gate charge(10V)	Q <sub>g</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =30A V <sub>GS</sub> =10V <sup>3)</sup>	-	40	-	nC
Gate-source charge	Q <sub>gs</sub>		-	4	-	nC
Gate-drain charge	Q <sub>gd</sub>		-	12	-	nC
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	—	-	-	90	A
Maximum Pulsed Drain-Source Diode Forward Current	I <sub>SM</sub>	—	-	-	360	A
Diode forward voltage	V <sub>SD</sub>	I <sub>SD</sub> =30A, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	-	-	1.2	V
Reverse recovery time	T <sub>rr</sub>	I <sub>F</sub> =20A dI <sub>F</sub> /dt=100A/μs	-	16	-	ns
Reverse recovery charge	Q <sub>rr</sub>	I <sub>F</sub> =20A dI <sub>F</sub> /dt=100A/μs	-	5	-	nC

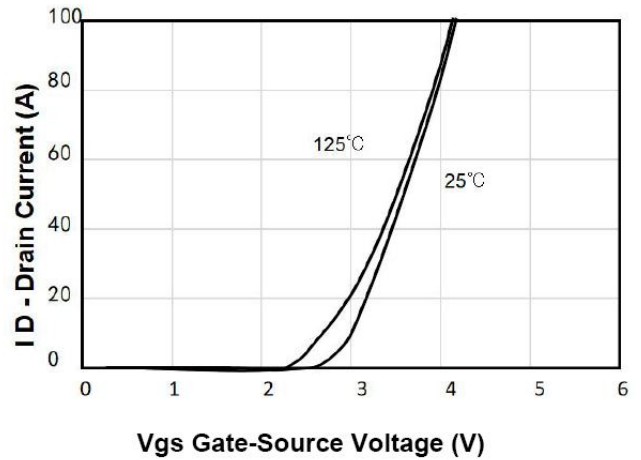
Note:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=15V, V<sub>G</sub>=10V, R<sub>G</sub>=25Ω, L=0.5mH, I<sub>AS</sub>=19A
3. Pulse Test: Pulse Width≤300us, Duty Cycle≤0.5%

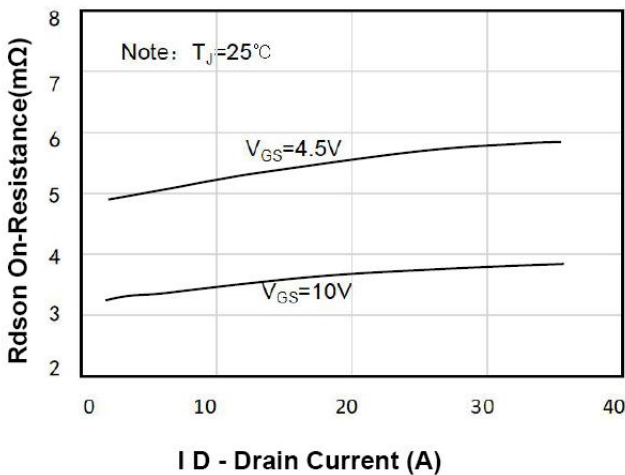
**8. Typical Electrical Characteristics**



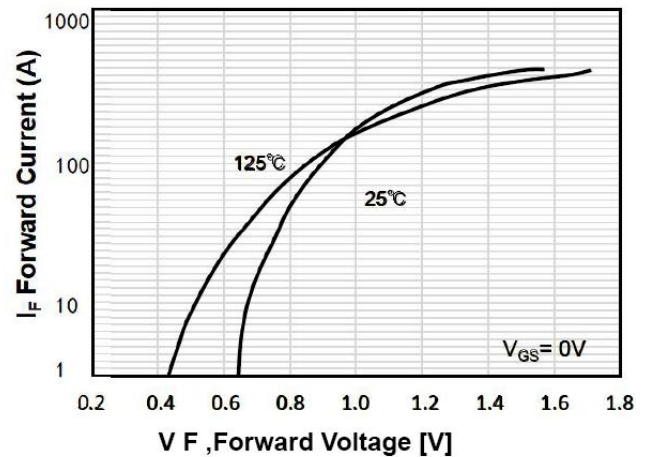
**Figure 1. On-Region Characteristics**



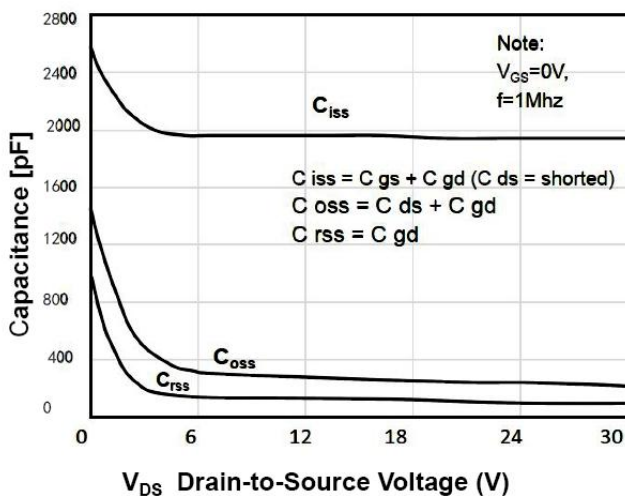
**Figure 2. Transfer Characteristics**



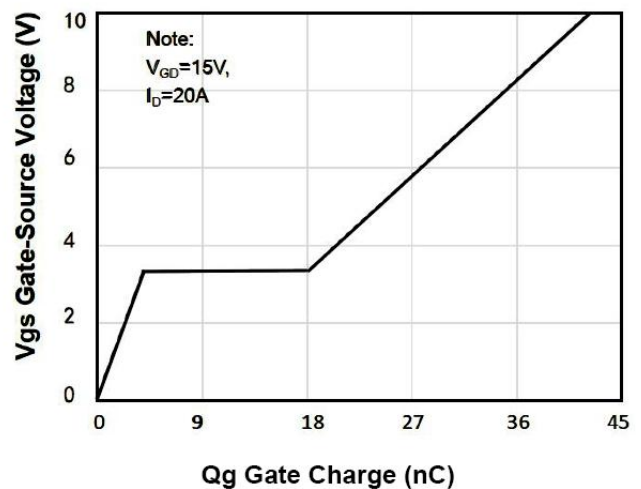
**Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage**



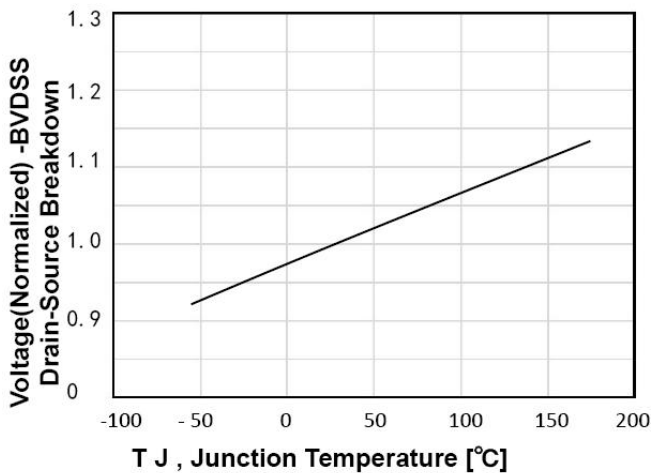
**Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature**



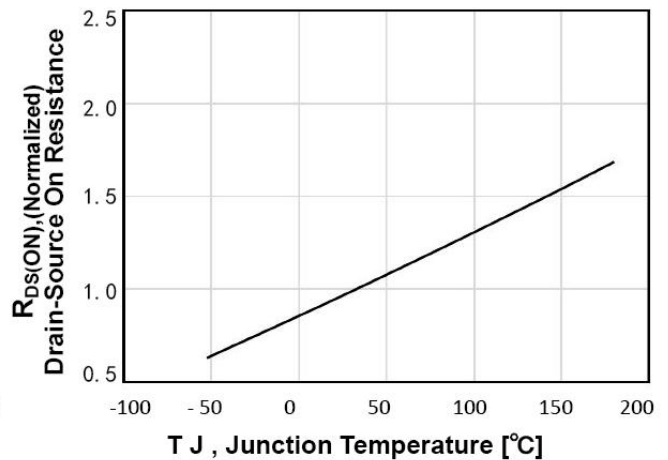
**Figure 5. Capacitance Characteristics**



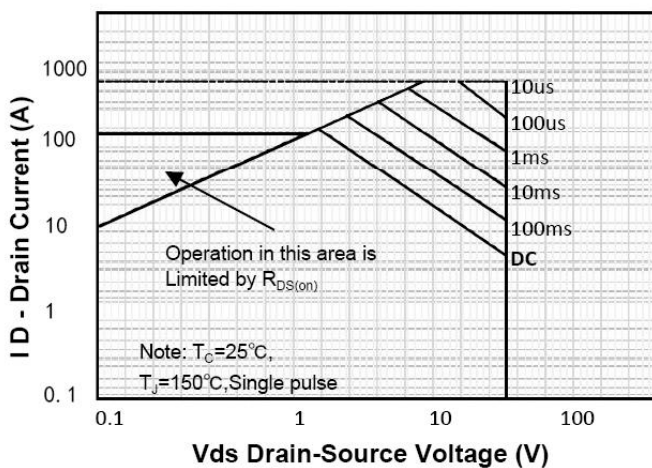
**Figure 6. Gate Charge Characteristics**



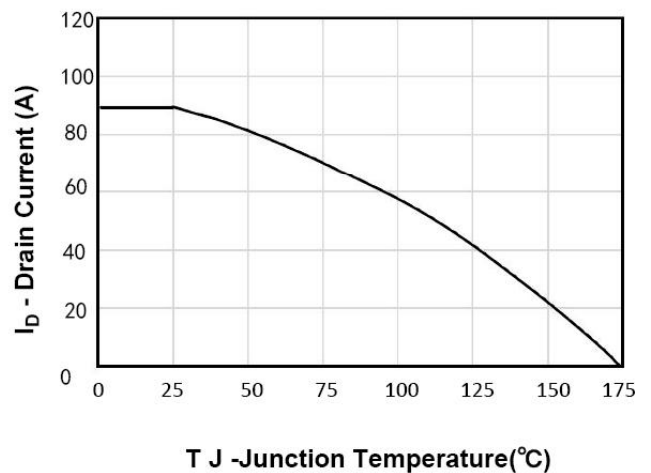
**Figure 7. Breakdown Voltage Variation vs Temperature**



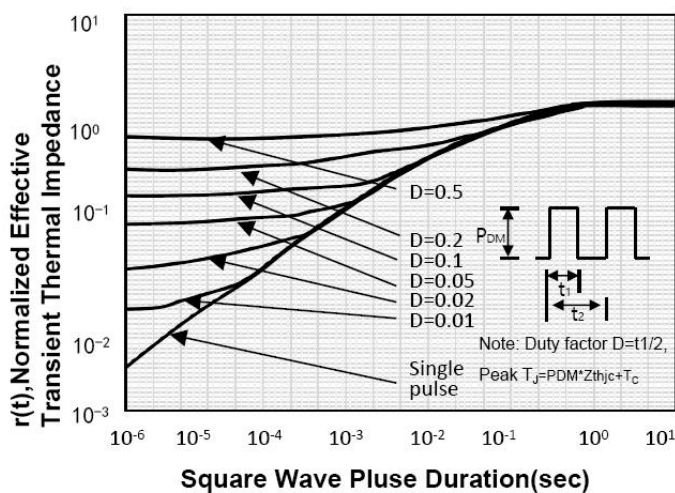
**Figure 8. On-Resistance Variation vs Temperature**



**Figure 9. Maximum Safe Operating Area**



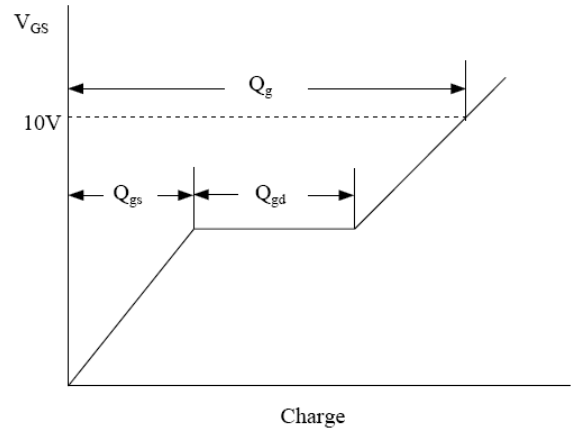
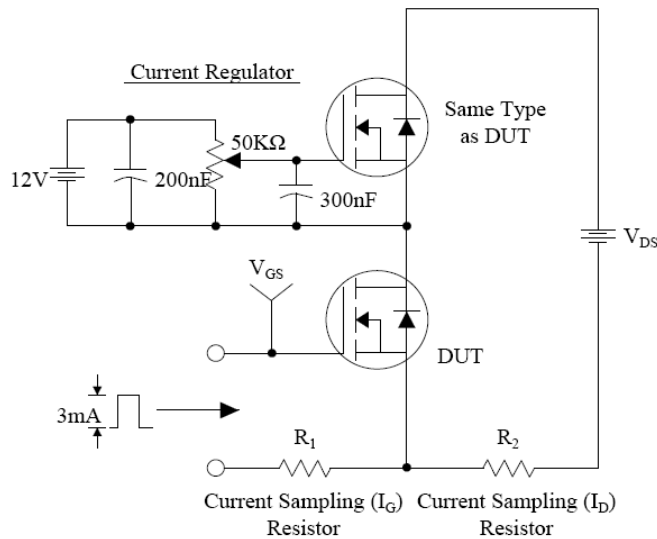
**Figure 10. Maximum PContinuous Drain Current vs Case Temperature**



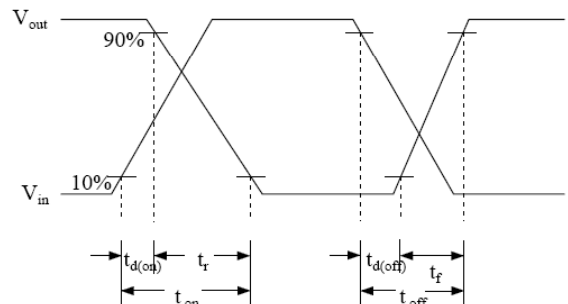
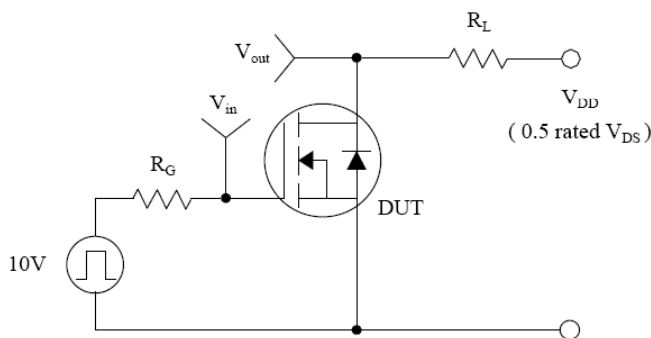
**Figure 11. Transient Thermal Response Curve**

**9. Test Circuits and Waveforms**

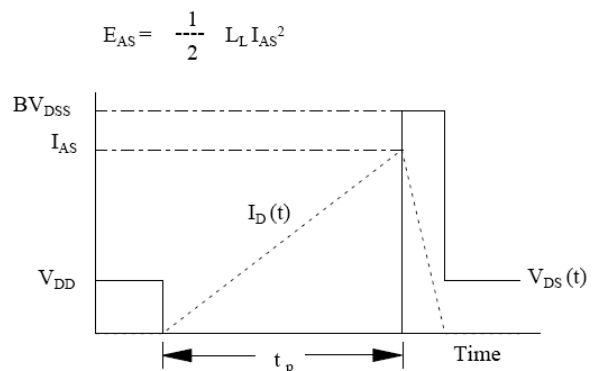
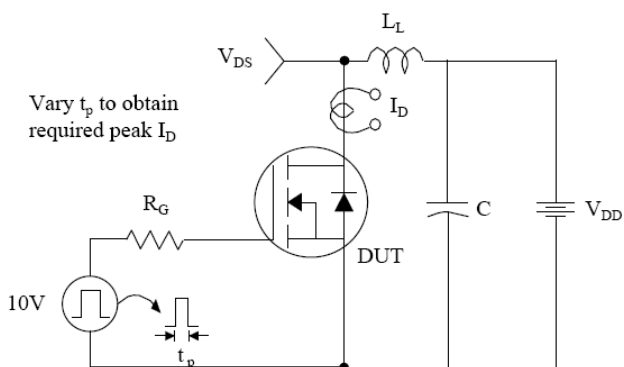
**Gate Charge Test Circuit & Waveform**



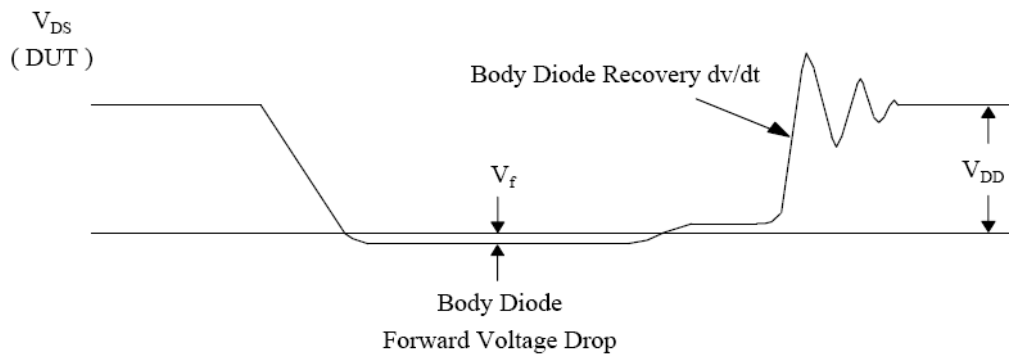
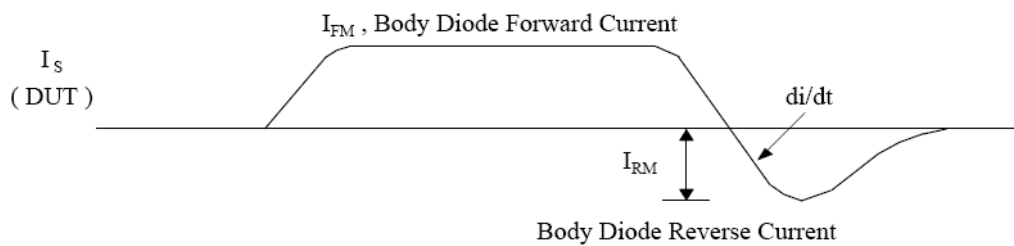
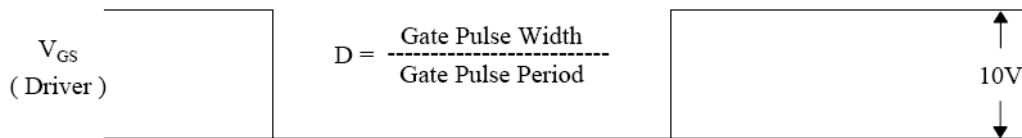
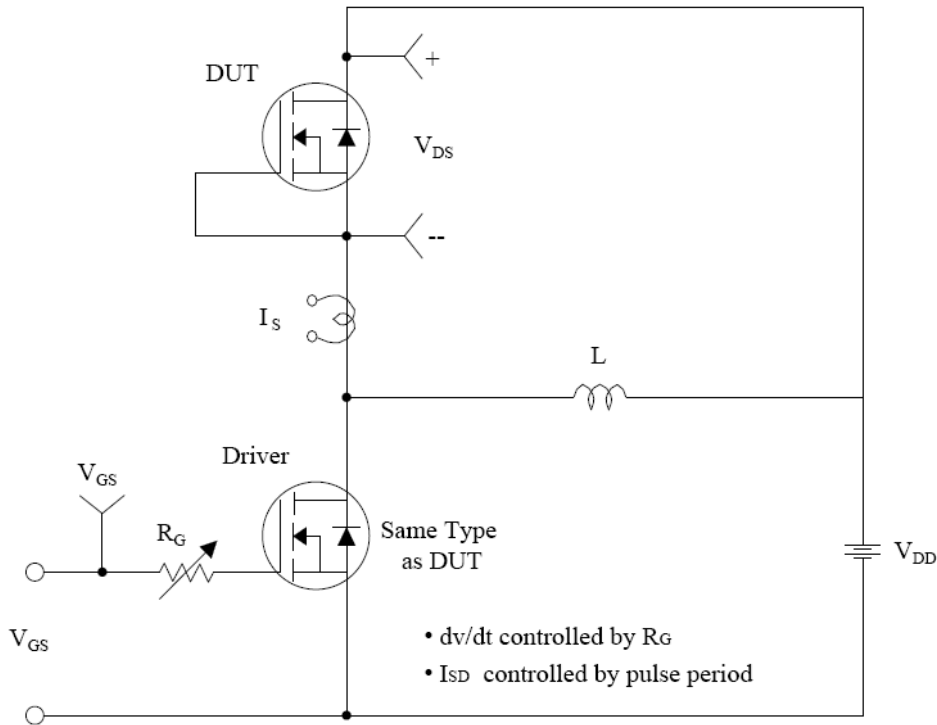
**Resistive Switching Test Circuit & Waveforms**



**Unclamped Inductive Switching Test Circuit & Waveforms**



### Peak Diode Recovery dv/dt Test Circuit & Waveforms



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