

PRELIMINARY SPEC Part Number: KT-3228ZG10ZS-RV Green



# ATTENTION OBSERVE PRECAUTIONS FOR HANDLING ELECTROSTATIC DISCHARGE SENSITIVE DEVICES

### Absolute Maximum Ratings at TA = 25°C

Parameter	Symbol	Value	Unit
DC Forward Current [1]	lF	350	mA
Peak Forward Current [2]	IFM	500	mA
Power dissipation	Pt	1.3	W
Operating Temperature	Тор	-40 To +100	°C
Storage Temperature	Tstg	-40 To +120	°C
Junction temperature[1]	TJ	120	°C
Thermal resistance [1] (Junction/ambient)	Rth j-a	75	°C/W
Thermal resistance [1] (Junction/solder point)	Rth j-s	28	°C/W

#### Notes

### Electrical / Optical Characteristics at TA = 25°C

Parameter	Symbol	Value	Unit
Forward Voltage IF = 350mA [Min.]		2.7	
Forward Voltage IF = 350mA [Typ.]	VF [2]	3.3	V
Forward Voltage IF = 350mA [Max.]		3.8	
Luminous Flux IF = 350mA [Typ.]	Ф۷	50	lm
Wavelength at peak emission IF = 350mA [Typ.]	λpeak	520	nm
Dominant Wavelength IF = 350mA [Typ.]	λ dom [1]	530	nm
Spectral bandwidth at 50% ΦREL MAX IF = 350mA [Typ.]	Δλ	35	nm
Temperature coefficient of $\lambda peak$ IF = 350mA, - 10°C $\leq$ T $\leq$ 100°C [Typ.]	TCλpeak	0.16	nm/°C
Temperature coefficient of $\lambda$ dom IF = 350mA, - 10°C $\leq$ T $\leq$ 100°C [Typ.]	TCλdom	0.14	nm/°C
Temperature coefficient of VF IF = $350\text{mA}$ , $-10^{\circ}\text{C} \leq \text{T} \leq 100^{\circ}\text{C}$ [Typ.]	TCv	-3.1	mV/°C

#### Notes:

1.Wavelength: +/-1nm.

2. Forward Voltage: + / - 0.1V.





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<sup>1.</sup> Results from mounting on PC board FR4 (pad size>100mm²), mounted on pc board-metal core PCB is recommend for lowest thermal resistance.

<sup>2. 1/10</sup> Duty Cycle, 0.1ms Pulse Width.



#### **Selection Guide**

Part No.	Dice		Viewing Angle [1]		
T untito		Code.	Min.	Max.	201/2
KT-3228ZG10ZS-RV	Green (AlInGaN)	CH-L	40	50	
		CJ-L	50	60	120°
		CK-L	60	70	

#### Notes

- 1.  $\theta$ 1/2 is the angle from optical centerline where the luminous intensity is 1/2 the optical centerline value.
- 2. Luminous intensity / luminous flux: +/-15%.

### **Package Dimension And Materials**

For package dimension please refer to page 10

Material as follows: Package : Ceramics

Encapsulating resin : Silicone resin
Electrodes : Ag plating

#### **Features**

- 1.Dimensions: 3.2mm X 2.8mm X 0.8mm.
- 2. Higher brightness .
- 3.Small package with high efficiency .
- 4. Surface mount technology .
- 5.ESD protection .
- 6. Moisture sensitivity level: level 2a.
- 7.RoHS compliant.

### Packaging:

- 1.The LEDs are packed in cardboard boxes after taping.
- 2. The label on the minimum packing unit shows: Part Number, Lot Number, Ranking, Quantity.
- 3. In order to protect the LEDs from mechanical shock, we pack them in cardboard boxes for transportation.
- 4. The LEDs may be damaged if the boxes are dropped or receive a strong impact against them, so precautions must be taken to prevent any damage.
- 5. The boxes are not water resistant and therefore must be kept away from water and moisture.
- 6. When the LEDs are transported, we recommend that you use the same packing methods as Kingbright's.

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### **Reliability Test Items And Conditions**

The reliability of products shall be satisfied with items listed below

Lot Tolerance Percent Defective (LTPD): 10%

No.	Test Item	Test Conditions	Test Times / Cycles	Number of Damaged
1	Continuous Operating Test	$T_a$ = 25 °C , $I_F$ = 350 mA Tested with standard circuit board <sup>*</sup>	1000 hrs	0/22
2	High Temperature Operating Test	T <sub>a</sub> = 100 °C , I <sub>F</sub> = 60 mA (note)	1000 hrs	0/22
3	Low Temperature Operating Test	$T_a$ = -40 °C , $I_F$ = 350 mA Tested with standard circuit board*	1000 hrs	0/22
4	High Temperature and Humidity Storage Operating Test	$T_a$ = 85 °C , RH = 85% , $I_F$ = 110 mA (note)	1000 hrs	0/22
5	Temperature Cycling Test	High temp: +100 °C 30 mins $\int$ R.T : 5 mins $\int$ Low temp : -40 °C 30 mins $\int$ R.T : 5 mins	10 cycles	0/22
6	Thermal Shock Test	High temp: +100 °C 5 mins	100 cycles	0/22
7	Soldering resistance Test	T <sub>sld</sub> = 260 °C , 10 secs	10 secs	0/22

Note : Thermal resistance of LED with Kingbright circuit board :  $Rthj-a = 75^{\circ}C/W$ 

### **Failure Criteria**

Item	Symbol	Test Conditions	Criteria for Judgement	
			Min.	Max.
Forward Voltage	VF	IF = 350mA	-	Initial Level x 1.1
Luminous Flux	Ф۷	IF = 350mA	Initial Level x 0.7	-

Note: The test is performed after the board is cooled down to the room temperature.

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### **JEDEC Moisture Sensitivity:**

Lovel Electrife		Soak Requirements				
Level Floor Life		Standard		Accelerated Equivalent		
	Time	Conditions	Time (hours)	Conditions	Time (hours)	Conditions
2a	4 weeks	≤ 30 °C / 60% RH	696 <sup>2</sup> + 5 / - 0	30 °C / 60% RH	120 + 1 / - 0	60 °C / 60% RH

#### **Moisture Sensitivity Levels**

Lavial	Floor Life		Soak Requirements			
Level			Standard		Accelerated Equivalent <sup>1</sup>	
	Time	Time Conditions		Conditions	Time (hours)	Conditions
1	Unlimited	≤ 30 °C / 85% RH	168 + 5 / - 0	85 °C / 85% RH		
2	1 year	≤ 30 °C / 60% RH	168 + 5 / - 0	85 °C / 60% RH		
2a	4 weeks	≤ 30 °C / 60% RH	696 <sup>2</sup> + 5 / - 0	30 °C / 60% RH	120 + 1 / - 0	60 °C / 60% RH
3	168 hours	≤ 30 °C / 60% RH	192 <sup>2</sup> + 5 / - 0	30 °C / 60% RH	40 + 1 / - 0	60 °C / 60% RH
4	72 hours	≤ 30 °C / 60% RH	96 <sup>2</sup> + 2 / - 0	30 °C / 60% RH	20 + 0.5 / - 0	60 °C / 60% RH
5	48 hours	≤ 30 °C / 60% RH	72 <sup>2</sup> + 2 / - 0	30 °C / 60% RH	15 + 0.5 / - 0	60 °C / 60% RH
5a	24 hours	≤ 30 °C / 60% RH	48 <sup>2</sup> + 2 / - 0	30 °C / 60% RH	10 + 0.5 / - 0	60 °C / 60% RH
6	Time on Label (TOL)	≤ 30 °C / 60% RH	TOL	30 °C / 60% RH		

#### Notes:

- 1.CAUTION The "accelerated equivalent" soak requirements shall not be used until correlation of damage response, including electrical, after soak and reflow is established with the "standard" soak requirements or if the known activation energy for diffusion is 0.4 0.48 eV. Accelerated soak times may vary due to material properties, e.g., mold compound, encapsulant, etc. JEDEC document JESD22-A120 provides a method for determining the diffusion coefficient.
- 2.The standard soak time includes a default value of 24 hours for semiconductor manufacturer's exposure time (MET) between bake and bag and includes the maximum time allowed out of the bag at the distributor's facility.

If the actual MET is less than 24 hours the soak time may be reduced. For soak conditions of 30 °C/60% RH the soak time is reduced by one hour For each hour the MET is less than 24 hours. For soak conditions of 60 °C/60% RH, the soak time is reduced by one hour for each five hours the MET is less than 24 hours.

If the actual MET is greater than 24 hours the soak time must be increased. If soak conditions are 30 °C/60% RH, the soak time is increased one Hour for each hour that the actual MET exceeds 24 hours. If soak conditions are 60 °C/60% RH, the soak time is increased one hour for each five Hours that the actual MET exceeds 24 hours.

3. Supplier may extend the soak times at their own risk.

#### **ESD Protection During Production**

Electric static discharge can result when static-sensitive products come in contact with the operator or other conductors.

The following procedures may decrease the possibility of ESD damage:

- 1. Minimize friction between the product and surroundings to avoid static buildup.
- 2.All production machinery and test instruments must be electrically grounded.
- 3. Operators must wear anti-static bracelets.
- 4. Wear anti-static suit when entering work areas with conductive machinery.
- 5.Set up ESD protection areas using grounded metal plating for component handling.
- 6.All workstations that handle IC and ESD-sensitive components must maintain an electrostatic potential of 150V or less.
- 7. Maintain a humidity level of 50% or higher in production areas.
- 8. Use anti-static packaging for transport and storage.
- 9.All anti-static equipment and procedures should be periodically inspected and evaluated for proper functionality.

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### **Handling Precautions**

Compare to epoxy encapsulant that is hard and brittle, silicone is softer and flexible. Although its characteristic significantly reduces thermal stress, it is more susceptible to damage by external mechanical force.

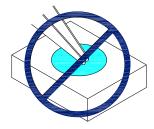
As a result, special handling precautions need to be observed during assembly using silicone encapsulated LED products. Failure to comply might leads to damage and premature failure of the LED.

1. Handle the component along the side surfaces by using forceps or appropriate tools.

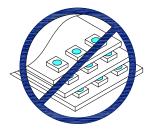


2. Do not directly touch or handle the silicone lens surface. It may damage the internal circuitry.

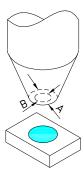




3. Do not stack together assembled PCBs containing exposed LEDs. Impact may scratch the silicone lens or damage the internal circuitry.



- 4. The outer diameter of the SMD pickup nozzle should not exceed the size of the LED to prevent air leaks. The inner diameter of the nozzle should be as large as possible.
- 5. A pliable material is suggested for the nozzle tip to avoid scratching or damaging the LED surface during pickup.
- 6. The dimensions of the component must be accurately programmed in the pick-and-place machine to insure precise pickup and avoid damage during production.



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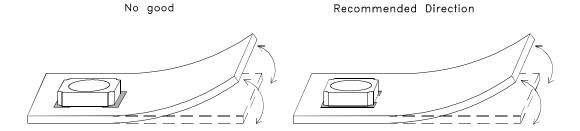
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### Designing the Position of LED on a Board.

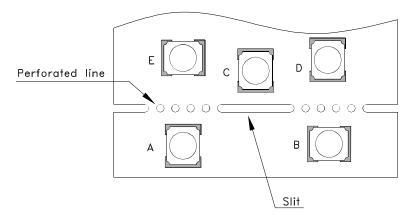
1.No twist/warp/bent/or other stress shall be applied to the board after mounting LED with solder to avoid a crack of LED package.

Refer to the following recommended position and direction of LED.



Appropriate LED mounting is to place perpendicularly against the stress affected side.

2.Depending on the position and direction of LED,the mechanical stress on the LED package can be changed. Refer to the following figure.



Stress: A>B=C>D>E

- 3.Do not split board by hand. Split with exclusive special tool.
- ${\it 4.} If an aluminum circuit board is used, a large stress by thermal shock might cause a solder crack.\\$

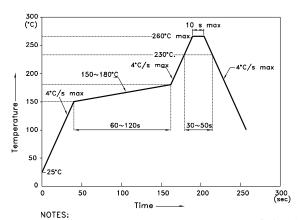
For this reason, it is recommended an appropriate verification should be taken before use.

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1.We recommend the reflow temperature 245°C(+/-5°C).The maximum soldering temperature should be limited to 260°C.

2.Don't cause stress to the epoxy resin while it is exposed

3. Number of reflow process shall be 2 times or less.

to high temperature.

#### **Heat Generation:**

1. Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when making the system design. The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of LED placement on the board ,as well as other components. It is necessary to avoid intense heat generation and operate within the maximum ratings given in this specification.

2.Please determine the operating current with consideration of the ambient temperature local to the LED and refer to the plot of Permissible Forward current vs. Ambient temperature on CHARACTERISTICS in this specification. Please also take measures to remove heat from the area near the LED to improve the operational characteristics on the LED.

3.The equation ① indicates correlation between T<sub>j</sub> and T<sub>a</sub> ,and the equation ② indicates correlation between T<sub>j</sub> and T<sub>s</sub>

 $T_j = Ta + Rthj-a *W \dots 1$ 

Tj = Ts + Rthj-s \*W ...... 2

Tj = dice junction temperature: °C

Ta = ambient temperature: °C

Ts = solder point temperature: °C

Rthj-a = heat resistance from dice junction temperature to ambient temperature : °C/ W

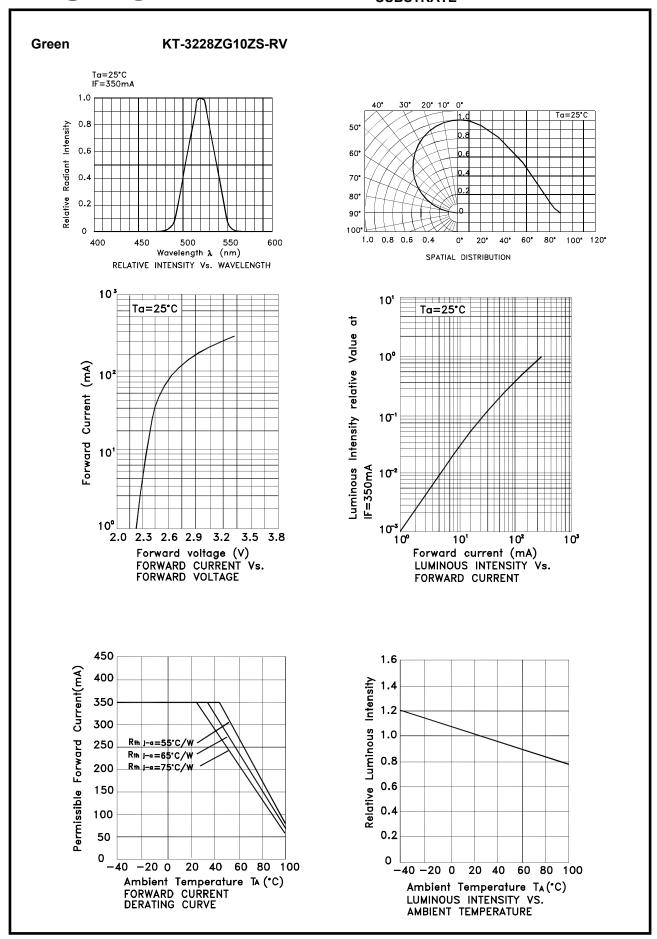
Rthj-s = heat resistance from dice junction temperature to Ts measuring point : °C/ W

W = inputting power (IFx VF) : W

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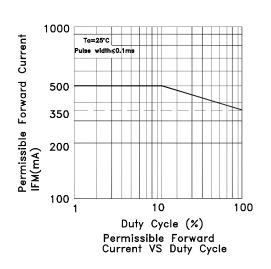
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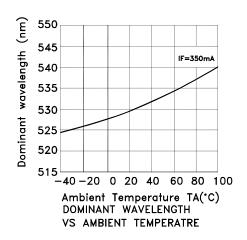


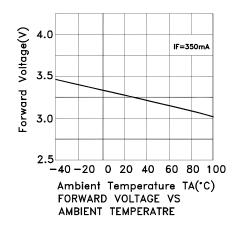


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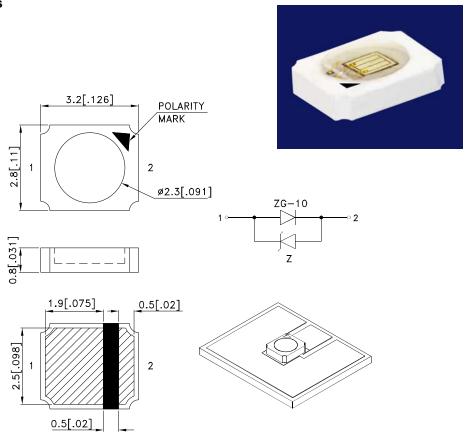








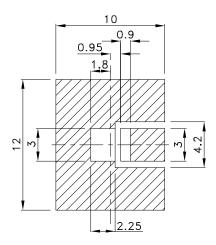
### **Package Dimensions**



#### Notes

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is  $\pm 0.25(0.01")$  unless otherwise noted.
- 3. Specifications are subject to change without notice.
- 4. The device has a single mounting surface. The device must be mounted according to the specifications.

# Recommended Soldering Pattern (Units : mm ; Tolerance: ± 0.1)



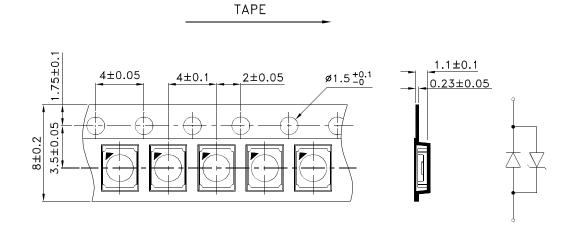
Solder Mask

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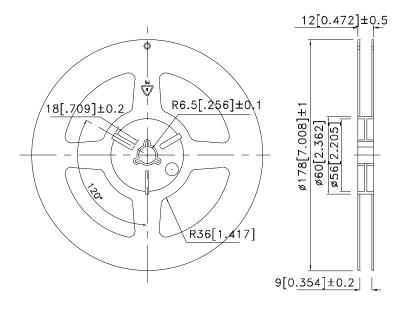
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# Tape Dimensions (Units: mm)

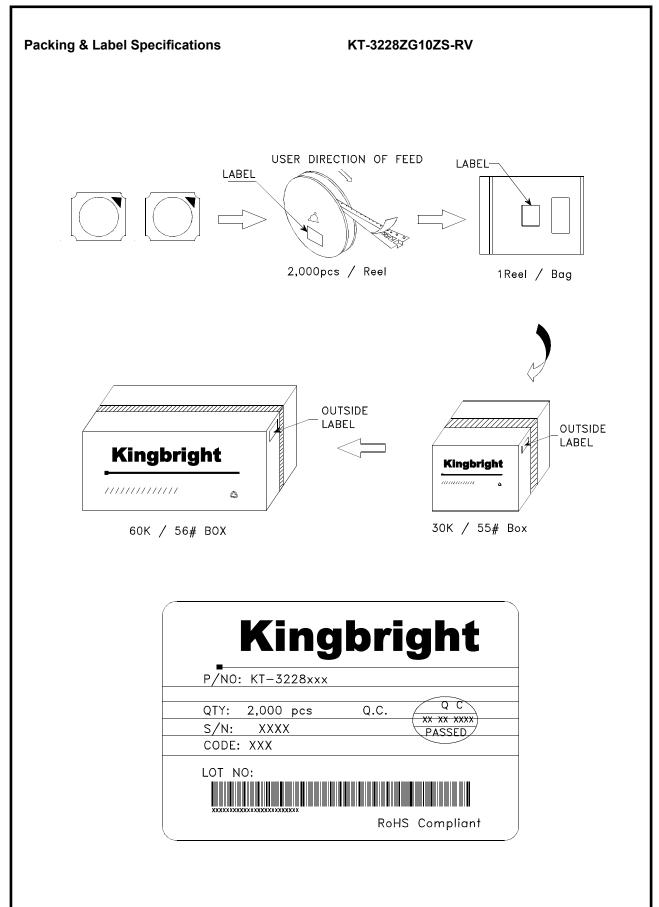


### **Reel Dimension**



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