

WP130WCP/2EGW

T-1 (3 mm) Bi-Level Circuit Board Indicator



DESCRIPTIONS

- The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode
- The Green source color devices are made with Gallium Phosphide Green Light Emitting Diode

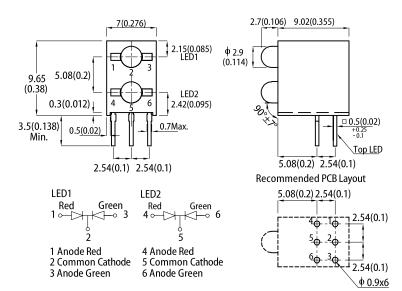
FEATURES

- · Bi-level right angle housing LED
- Pre-trimmed leads for pc board mounting
- Black case enhances contrast ratio
- High reliability
- Housing UL rating: 94V-0
- Housing material: Type 66 nylon
- · RoHS compliant

APPLICATIONS

- · Status indicator
- Illuminator
- Signage applications
- Decorative and entertainment lighting
- · Commercial and residential architectural lighting

PACKAGE DIMENSIONS



Notes

- 1. All dimensions are in millimeters (inches)
- Tolerance is ±0.25(0.01") unless otherwise noted. Lead spacing is measured where leads emerge from the package. 2.
- The specifications, characteristics and technical data described in the datasheet are subject to change 4. without prior notice

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	lv (mcd) @ 20mA ^[2]		Viewing Angle ^[1]	
			Min.	Тур.	201/2	
WP130WCP/2EGW	High Efficiency Red (GaAsP/GaP)	- White Diffused	12	30		
			*10	*24	60°	
	Green (GaP)		12	30		
			*12	*30		

Notes: 1. 01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.

Luminous intensity / luminous Flux: +/-15%.
 Luminous intensity value is traceable to CIE127-2007 standards

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ELECTRICAL / OPTICAL CHARACTERISTICS at Ta=25°C

Parameter	Symbol	Emitting Color	Value		11
Parameter			Тур.	Max.	– Unit
Wavelength at Peak Emission I_F = 20mA	λ_{peak}	High Efficiency Red Green	627 565	-	nm
Dominant Wavelength $I_F = 20 \text{mA}$	λ_{dom} ^[1]	High Efficiency Red Green	617 568	-	nm
Spectral Bandwidth at 50% Φ REL MAX I _F = 20mA	Δλ	High Efficiency Red Green	45 30	-	nm
Capacitance	С	High Efficiency Red Green	15 15	-	pF
Forward Voltage I _F = 20mA	V _F ^[2]	High Efficiency Red Green	2.0 2.2	2.5 2.5	V
Reverse Current (V _R = 5V)	I _R	High Efficiency Red Green	-	10 10	μA
Temperature Coefficient of λ_{peak} I_F = 20mA, -10°C $\leq T \leq 85^\circ C$	TC _{λpeak}	High Efficiency Red Green	0.13 0.1	-	nm/°C
Temperature Coefficient of λ_{dom} I_F = 20mA, -10°C \leq T \leq 85°C	TC _{λdom}	High Efficiency Red Green	0.06 0.06	-	nm/°C
Temperature Coefficient of V_F I_F = 20mA, -10°C \leq T \leq 85°C	TCv	High Efficiency Red Green	-1.9 -2	-	mV/°C

Notes:

1. The dominant wavelength (λd) above is the setup value of the sorting machine. (Tolerance $\lambda d : \pm 1$ nm.)

Forward voltage: ±0.1V.
 Wavelength value is traceable to CIE127-2007 standards.

4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Duranta	Symbol	Valu		
Parameter		High Efficiency Red	Green	Unit
Power Dissipation	P _D	75	62.5	mW
Reverse Voltage	V _R	5	5	V
Junction Temperature	Tj	125	110	°C
Operating Temperature	T _{op}	-40 to +85		°C
Storage Temperature	T _{stg}	-40 to +85		°C
DC Forward Current	I _F	30 25		mA
Peak Forward Current	I _{FM} ^[1]	160	140	mA
Electrostatic Discharge Threshold (HBM)	-	8000	8000	V
Thermal Resistance (Junction / Ambient)	R _{th JA} ^[2]	610	530	°C/W
Thermal Resistance (Junction / Solder point)	R _{th JS} ^[2]	370	330	°C/W
Lead Solder Temperature ^[3]		260°C For 3 Seconds		
Lead Solder Temperature [4]	260°C For 5 Seconds			

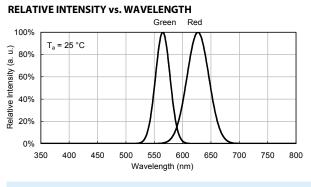
Notes:

Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. R_{In Js} Results from mounting on PC board FR4 (pad size ≥ 16 mm² per pad). 3. 2mm below package base. 4. 5mm below package base. 5. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

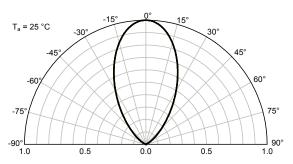
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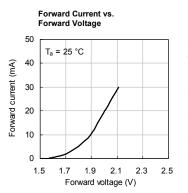
TECHNICAL DATA



SPATIAL DISTRIBUTION



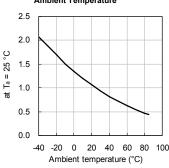
HIGH EFFICIENCY RED



Luminous Intensity vs. Forward Current 2.5 Luminous intensity normalised T_a = 25 °C 2.0 at 20mA 1.5 1.0 0.5 0.0 0 10 20 30 40 50 Forward current (mA)

Forward Current Derating Curve 50 (mA) Luminous intensity normalised Permissible forward current 40 30 20 10 0 -40 -20 0 20 40 60 80 100 Ambient temperature (°C)

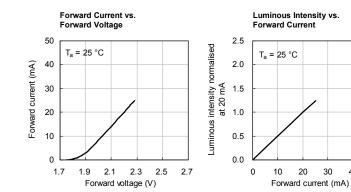
Luminous Intensity vs. Ambient Temperature



GREEN

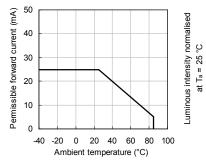
50

30 40

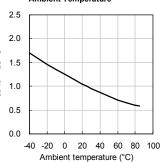




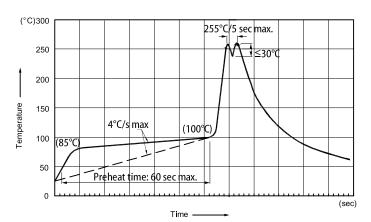




Luminous Intensity vs. Ambient Temperature



RECOMMENDED WAVE SOLDERING PROFILE



Notes:

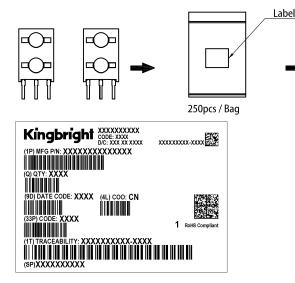
- 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
 Peak wave soldering temperature between 245°C ~ 255°Cfor 3 sec (5 sec max).
 Do not apply stress to the epoxy resin while the temperature is above 85°C.
 Fixtures should not incur stress on the component when mounting and during soldering process.
 SAC 305 solder alloy is recommended.

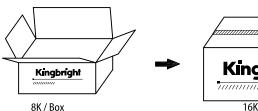
6. No more than one wave soldering pass

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PACKING & LABEL SPECIFICATIONS







PRECAUTIONS

Storage Conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature \leq 30°C and relative humidity < 60%.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

LED Mounting Method

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.

Lead-forming may be required to insure

the lead pitch matches the hole pitch.

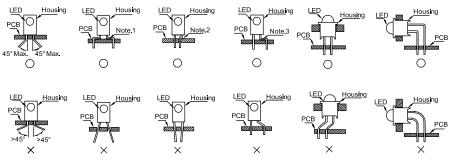
Refer to the figure below for proper lead

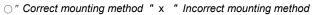
forming procedures.

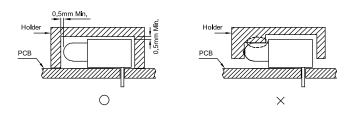
Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

Lead Forming Procedures

- 1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.
- 2. The tip of the soldering iron should never touch the lens epoxy.
- 3. Through-hole LEDs are incompatible with reflow soldering.
- 4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.







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