

WP73EK/GDA

4. 8mm Single-Level Circuit Board Indicator

DESCRIPTION

 The Green source color devices are made with Gallium Phosphide Green Light Emitting Diode

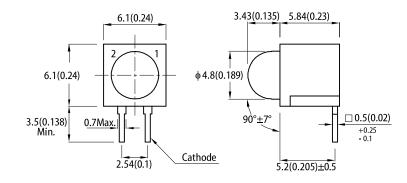
FEATURES

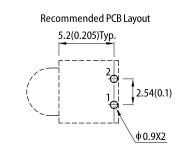
- · Pre-trimmed leads for pc board mounting
- Black case enhances contrast ratio
- · High reliability-life measured in years
- Housing UL rating: 94V-0
- Housing material: Type 66 nylon
- RoHS compliant

APPLICATIONS

- Status indicator
- Illuminator
- Signage applications
- · Decorative and entertainment lighting
- · Commercial and residential architectural lighting

PACKAGE DIMENSIONS





Notes

- All dimensions are in millimeters (inches).
 Tolerance is ±0.25(0.01") unless otherwise noted.
- Lead spacing is measured where the leads emerge from the package.
 The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	lv (mcd) @ 10mA ^[2]		Viewing Angle ^[1]
Fait Number			Min.	Тур.	201/2
WP73EK/GDA	Green (GaP)	Green Diffused	10	30	40°

Notes

- 41/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
 2. Luminous intensity / luminous flux: +/-15%.
 3. Luminous intensity value is traceable to CIE127-2007 standards.

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ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Emitting Color	Value		Unit
Falametei	Symbol		Typ. Max.		
Wavelength at Peak Emission I_F = 10mA	λ_{peak}	Green	565	-	nm
Dominant Wavelength I _F = 10mA	λ_{dom} ^[1]	Green	568	-	nm
Spectral Bandwidth at 50% Φ REL MAX I _F = 10mA	Δλ	Green	30	-	nm
Capacitance	С	Green	15	-	pF
Forward Voltage I _F = 10mA	V _F ^[2]	Green	2	2.4	V
Reverse Current (V_R = 5V)	I _R	Green	-	10	μA

Notes:

The dominant wavelength (λd) above is the setup value of the sorting machine. (Tolerance λd : ±1nm.)
 Forward voltage: ±0.1V.
 Wavelength value is traceable to CIE127-2007 standards.
 Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at $T_A=25^{\circ}C$

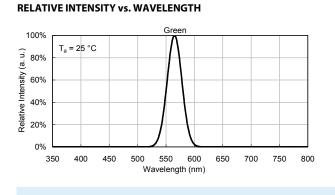
Parameter	Symbol	Value	Unit	
Power Dissipation	P _D	62.5	mW	
Reverse Voltage	V _R	5	V	
Junction Temperature	Tj	110	°C	
Operating Temperature	T _{op}	-40 to +85	°C	
Storage Temperature	T _{stg}	-40 to +85	°C	
DC Forward Current	lF	25	mA	
Peak Forward Current	I _{FM} ^[1]	140	mA	
Electrostatic Discharge Threshold (HBM)	-	8000	V	
Lead Solder Temperature ^[2]		260°C For 3 Seconds		
Lead Solder Temperature [3]		260°C For 5 Seconds		

Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. 2mm below package base. 3. 5mm below package base. 4. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

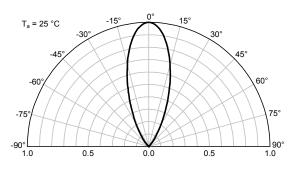
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WP73EK/GDA

TECHNICAL DATA



SPATIAL DISTRIBUTION



GREEN

50

40

30

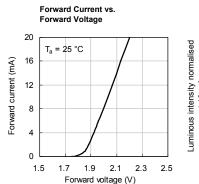
20

10

0

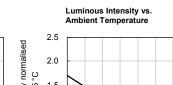
(mA)

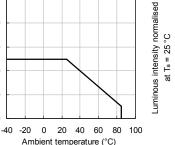
Permissible forward current

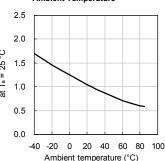


Luminous Intensity vs. Forward Current 2.5 T_a = 25 °C 2.0 at 10 mA 1.5 1.0 0.5 0.0 0 4 8 12 16 20 Forward current (mA)

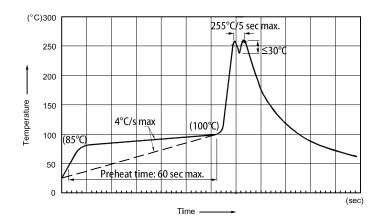
Forward Current Derating Curve







RECOMMENDED WAVE SOLDERING PROFILE

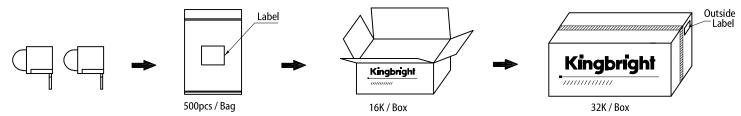


Notes

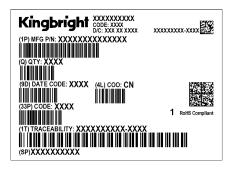
- Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C

- temperature of 200°C 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max). 3. Do not apply stress to the epoxy resin while the temperature is above 85°C. 4. Fixtures should not incur stress on the component when mounting and during soldering process. 5. SAC 305 solder alloy is recommended. 6. We mere then endowing additional solutions and the second solutions are solutions are solutions are solutions are solutions are solutions and the second solutions are solutions are solutions are solutions are solutions and the second solutions are solut
- 6. No more than one wave soldering pass

PACKING & LABEL SPECIFICATIONS



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PRECAUTIONS

Storage Conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature \leq 30°C and relative humidity < 60%.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

LED Mounting Method

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.

Lead-forming may be required to insure

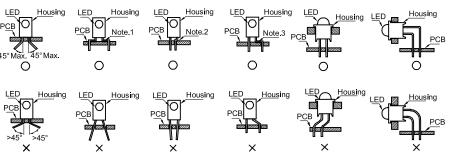
the lead pitch matches the hole pitch.

Refer to the figure below for proper lead forming procedures.

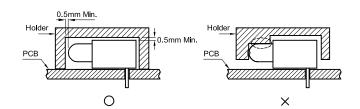
Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

Lead Forming Procedures

- 1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.
- 2. The tip of the soldering iron should never touch the lens epoxy.
- 3. Through-hole LEDs are incompatible with reflow soldering.
- 4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



○ " Correct mounting method " x " Incorrect mounting method



PRECAUTIONARY NOTES

- 2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
- When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
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^{1.} The information included in this document reflects representative usage scenarios and is intended for technical reference only.

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