

WP934EB/2SRD

T-1 (3mm) Bi-Level Circuit Board Indicator



DESCRIPTIONS

- The Super Bright Red source color devices are made with Gallium Aluminum Arsenide Red Light Emitting Diode
- · Electrostatic discharge and power surge could damage the LEDs
- · It is recommended to use a wrist band or anti-electrostatic glove when handling the LEDs
- · All devices, equipments and machineries must be electrically grounded

FEATURES

- · Pre-trimmed leads for pc mounting
- Black case enhances contrast ratio
- · High reliability life measured in years
- Housing UL rating: 94V-0
- Housing material: Type 66 nylon
- RoHS compliant

APPLICATIONS

- Status indicator
- Illuminator
- Signage applications
- · Decorative and entertainment lighting
- Commercial and residential architectural lighting

ATTENTION

Observe precautions for handling electrostatic discharge sensitive devices



- Notes: 1. All dimensions are in millimeters (inches)
- Tolerance is ±0.25(0.01") unless otherwise noted.
 Lead spacing is measured where the leads emerge from the package.
- The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

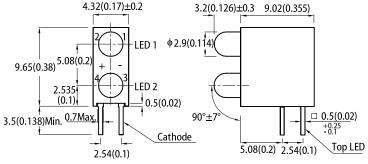
SELECTION GUIDE

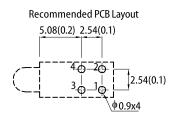
| Part Number | Emitting Color | Long Time | lv (mcd) @ 20mA ^[2] | | Viewing Angle ^[1] | |
|--------------|------------------------------|--------------|--------------------------------|------|------------------------------|--|
| Fart Number | (Material) | Lens Type | Lens Type Min. Typ. | | 201/2 | |
| WP934EB/2SRD | Super Bright Red (GaAIAs) | Red Diffused | 150 | 400 | 50% | |
| | | | *50 | *100 | 50° | |

Notes

- 1. 91/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
 2. Luminous intensity / luminous flux: +/-15%.
- * Luminous intensity value is traceable to CIE127-2007 standards.

PACKAGE DIMENSIONS





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ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

| Devemeter | Cumhal | Envirting Only a | Value | | Unit |
|---|--------------------------------|------------------|-----------|-----|-------|
| Parameter | Symbol | Emitting Color | Typ. Max. | | |
| Wavelength at Peak Emission I_F = 20mA | λ_{peak} | Super Bright Red | 655 | - | nm |
| Dominant Wavelength I _F = 20mA | λ_{dom} ^[1] | Super Bright Red | 640 | - | nm |
| Spectral Bandwidth at 50% Φ REL MAX I_{F} = 20mA | Δλ | Super Bright Red | 20 | - | nm |
| Capacitance | С | Super Bright Red | 45 | - | pF |
| Forward Voltage I _F = 20mA | V _F ^[2] | Super Bright Red | 1.85 | 2.5 | v |
| Reverse Current ($V_R = 5V$) | I _R | Super Bright Red | - | 10 | μA |
| Temperature Coefficient of λ_{peak} I_F = 20mA, -10°C $\leq T \leq 85^\circ C$ | TC_{\lambdapeak} | Super Bright Red | 0.14 | - | nm/°C |
| Temperature Coefficient of λ_{dom} I_F = 20mA, -10°C $\leq T \leq 85^\circ C$ | $TC_{\lambda dom}$ | Super Bright Red | 0.05 | - | nm/°C |
| Temperature Coefficient of $~V_F$ I_F = 20mA, -10°C \leq T \leq 85°C | TCv | Super Bright Red | -1.9 | - | mV/°C |

Notes:

The dominant wavelength (λd) above is the setup value of the sorting machine. (Tolerance λd : ±1nm.)
 Forward voltage: ±0.1V.
 Wavelength value is traceable to CIE127-2007 standards.
 Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

| Parameter | Symbol | Value | Unit | |
|--|-----------------------------------|---------------------|------|--|
| Power Dissipation | P _D | 75 | mW | |
| Reverse Voltage | V _R | 5 | V | |
| Junction Temperature | Tj | 115 | °C | |
| Operating Temperature | T _{op} | -40 to +85 | °C | |
| Storage Temperature | T _{stg} | -40 to +85 | °C | |
| DC Forward Current | I _F | 30 | mA | |
| Peak Forward Current | I _{FM} ^[1] | 155 | mA | |
| Electrostatic Discharge Threshold (HBM) | - | 3000 | V | |
| Thermal Resistance (Junction / Ambient) | R _{th JA} ^[2] | 580 | °C/W | |
| Thermal Resistance (Junction / Solder point) | R _{th JS} ^[2] | 330 | °C/W | |
| Lead Solder Temperature [3] | | 260°C For 3 Seconds | | |
| Lead Solder Temperature [4] | | 260°C For 5 Seconds | | |

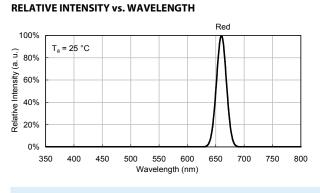
Notes:

Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. R_{In JA}, R_{th JS} Results from mounting on PC board FR4 (pad size ≥ 16 mm² per pad). 3. 2mm below package base. 4. 5mm below package base. 5. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

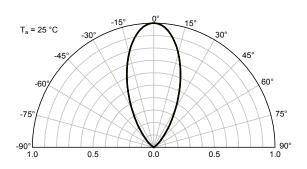
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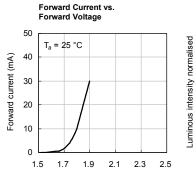
WP934EB/2SRD

TECHNICAL DATA



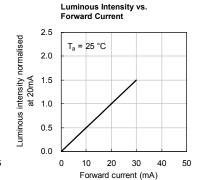
SPATIAL DISTRIBUTION

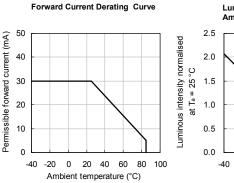


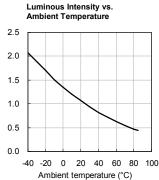


Forward voltage (V)

SUPER BRIGHT RED

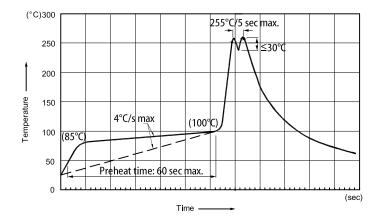






RECOMMENDED WAVE SOLDERING PROFILE

PACKING & LABEL SPECIFICATIONS



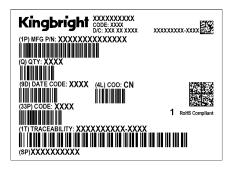
Notes

- Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C

- Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
 Do not apply stress to the epoxy resin while the temperature is above 85°C.
 Fixtures should not incur stress on the component when mounting and during soldering process. SAC 305 solder alloy is recommended.
 No more than one wave soldering pass.

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PRECAUTIONS

Storage Conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. LEDs should be stored with temperature $\leq 30^{\circ}$ C and relative humidity $< 60^{\circ}$.
- 3. Product in the original sealed package is recommended to be assembled within 72 hours of opening. Product in opened package for more than a week should be baked for 30 (+10/-0) hours at 85 ~ 100°C.

LED Mounting Method

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.

Lead-forming may be required to insure

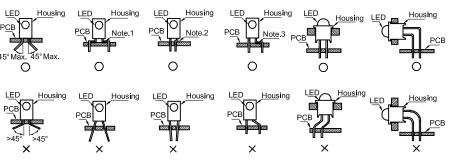
the lead pitch matches the hole pitch.

Refer to the figure below for proper lead forming procedures.

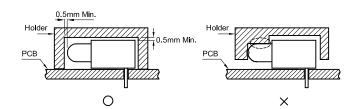
Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

Lead Forming Procedures

- 1. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.
- 2. The tip of the soldering iron should never touch the lens epoxy.
- 3. Through-hole LEDs are incompatible with reflow soldering.
- 4. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



○ " Correct mounting method " x " Incorrect mounting method



PRECAUTIONARY NOTES

- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to 2 the latest datasheet for the updated specifications.
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