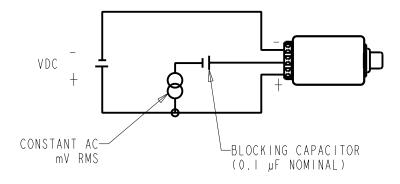


2100-2900	111.0	117.0	
3500-4400	98.5		
4200-5200	103.5	109.5	

- 4. RESPONSE AND DISTORTION MEASUREMENTS MADE USING THE ELECTRICAL TEST CONDITIONS SHOWN BELOW.
- 5. INDIVIDUAL SPECIFICATIONS.

PORT	MAX. DC SUPPLY CURRENT mA		DISTORTION		ELECTRICAL TEST CONDITIONS	
LOCATION	VDC= 1.3±.02	VDC= 1.5±.02	MAX. %	FREQ Hz	CONSTANT AC mV RMS	VDC
120	0.75	1.50	5	800	32±1	1.3

6. TEST CONDITIONS.



П	Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION
						0
	В	C10111228	5-28-10	Active		l K
	А	C10103328	11-14-05	7, 4, 1, 1, 4	ט	
				INSPECTION ACCEPTANCE/REJECTION	DR. BY	DATE
C	RITERIA, (CORRELATION ()F TEST EQUIPMENT WITH KN	OWLES IS ALSO REQUIRED FOR	MANA	11 11 A A E

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

ELIMINAT	MMM	11-14-0		
			CK. BY	DATE
TITLE:	RECEIVER	EP-24075-000	GJP	11-16-0
	NECETVEN	Li 24013 000	APP. BY	DATE
	PERFORMANCE SPECIFICATION	SHT 2 I	GIP	11-16-0

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