

STORAGE: -40°C TO 63°C

TOTAL HARMONIC DISTORTION\*
DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (V rms)	DC BIAS (V)	LIMIT (%)
900	0.180	0	5
1350	0.360	0	10

TABLE 2

## TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.180 V rms, 0 mA V DC BIAS	
SOURCE IMPEDANCE	< I Ohm	
TUBING		
COUPLER CAVITY	2 CM <sup>3</sup> , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 60318-5)	

TABLE 3

## ELECTRICAL

DC RESISTANCE	200 Ohms ± 10%*
IMPEDANCE @ 500 Hz	266 Ohms ± 15%*
IMPEDANCE @ IkHz	35  Ohms ±  5%*

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.\*

Revision C.O. #

NOTE: SPECIFICATIONS FOLLOWED BY AN ASTERISK(\*) ARE 100% TESTED.

E D	C10111950 C10110508	12-20-10	Active		Ε
	_IMITS ARE US		INSPECTION ACCEPTANCE/REJECTION	DR. BY	DATE

Implementation Date

## KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION			MMM CK. BY	8 -   8 - 0 6 DATE	
TITLE:	RECEIVER	FK-30040-000	GJP APP. BY	8 - 1 8 - 0 6	
	PERFORMANCE SPECIFICATION	SHT 2.1	GJP	8-18-06	

RELEASE LEVEL

REVISION

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