

SHT I.I

NOTES:

I. A POSITIVE GOING VOLTAGE AT TERMINAL 2, RELATIVE TO TERMINAL I, CAUSES A DECREASE IN PRESSURE AT THE SOUND OUTLET.

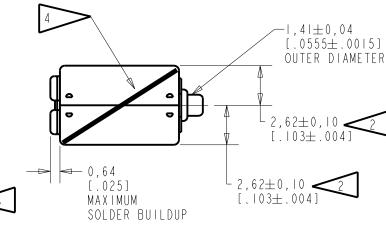
➤ LOCATED FROM TWO SURFACES FOR CUSTOMER CONVENIENCE. ONLY APPLICABLE FROM ONE SURFACE, NOT TO BE USED TOGETHER. HORIZONTAL LOCATION FOR TERMINAL CENTERED $TO \pm 0.17 [.007].$

➤RECEIVERS WIRED IN ELECTRICAL PARALLEL WHEN CONNECTED TO TERMINAL I AND TERMINAL 2.

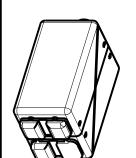
➤ RED STRIP FROM CORNER TO CORNER, EITHER SIDE.

 $.41\pm0.04$

 $[.0555 \pm .0015]$







NOMINAL WEIGHT .71 GRAMS

DIMENSIONS IN MILLIMETERS [INCHES]

 $2,04\pm0,10$

 $[.081\pm.004]$

 7.87 ± 0.07

 4.09 ± 0.07 $[.161\pm.003]$

 $5,23\pm0,10$

-TERMINAL 2

(POSITIVE)

 $[.206\pm.004]$

TITLE:

 $[.310 \pm .003]$

A SCALE:	C10112225	4- 3-	7(01110	DR. BY	DATE
В	C10112707	8-26-11	l Active		(,
С	C10114023	8 - 30 - 12	A 1 .		^
Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

TERMINAL I-

(NEGATIVE)

 $1,58\pm0,05$

LASER MARK-

THIS SURFACE

 $[.062\pm .002]$

 $1,94 \pm 0,10$

 $1,50\pm0,05$

 $[.059\pm.002]$

 $[.077\pm.004]$

•••			
20 1107 2011 5 2011		KL	4 - 3 -
DO NOT SCALE DRAWING			DATE
RECEIVER	HODTEC-31230-000	GJP	4 - 3 -
NECETALIN	1100120 31230 000	APP. BY	DATE
OUTLINE DRAWING	SHT I.I	G IP	4-13-11

	FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
	200	-3.5	-0.5	+2.5
	500	-2.0	114.7	+2.0
	810-1010	+4.5	+7.5	+10.5
	1895-2305	+0.8	+3.8	+6.8
	3060-3740	-4.5	-I.5	+1.5
	4030-4530	- 0 .	-7.1	-4.1
ſ	5050-6170	-18.5	-15.5	-12.5

TABLE I

TOTAL HARMONIC DISTORTION*

DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (V rms)	DC BIAS (mA)	LIMIT (%)
I/3 PEAK	0.148	0	5
1/2 PEAK	0.148	0	5
I/3 PEAK	0.295	0	8
I/2 PEAK	0.295	0	8

TABLE 2

CAPACITANCE @ 10 MHz 13.8 pF TYP.

TABLE 4

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT. *

MECHANICAL

PORT LOCATION: 12S

SOLDER TYPE: SAC305

TEMPERATURE

OPERATING: SENSITIVITY WILL NOT VARY MORE THAN +1/-3 dB FROM -17°C TO 63°C STORAGE: -40°C TO 63°C

TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.148 V rms, O mA DC BIAS
SOURCE IMPEDANCE	Ohm</td
TUBING	8mm X Imm ID + 28mm X I.5mm ID EAR HOOK SIMULATOR + 25mm X 2mm ID TUBE + 18mm X 3mm ID TUBE
COUPLER CAVITY	2 cm ³ , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 60318-5)

TABLE 3

Revision C.O. # Implementation Date RELEASE LEVEL REVISION C10114023 8-30-12 Active C10112707 8-26-11

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

	A C10112225 4-13-11			
	WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION			DATE
				4- 3-
				DATE
	RECEIVER	HODTEC-31230-000	GJP	4- 3-
	WEOLI VEN	1.05.20 3.200 000	APP. BY	DATE
	PERFORMANCE SPECIFICATION	SHT 2.1	G IP	4-13-11

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