

. • •			
250	- 3 . 0	-0.5	+2.0
500	-2.0	0.0	+2.0
1000	-2.0	95.0	+2.0
3000-3800 PEAK	+7.0	+ 0 . 0	+ 3.0
4100-5100 VALLEY	0.0		
4850-5850 PEAK	+3.0	+6.0	+9.0

TABLE I.

TOTAL HARMONIC DISTORTION*

DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	DRIVE (V RMS)	DC BIAS (MA)	LIMIT (%)
1130	0.283 V	0	5
1700	0.283 V	0	5
500	1.50 V	0	8

TABLE 2.

TEST CONDITIONS

NOMINAL SOURCE VOLTAGE	0.283 Vrms, 0 Vdc BIAS
SOURCE IMPEDANCE	< Ω
TUBING	10 mm (.394) LONG, mm (.039) D.
COUPLER CAVITY	2 CC SIMULATED ANSI S3.7 TYPE HA-3, (IEC 126)

TABLE 3.

POLARITY*

POSITIVE SIGNAL APPLIED TO TERMINAL 2 WILL PRODUCE A DECREASE IN SOUND PRESSURE AT THE SOUND OUTLET.

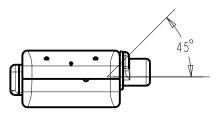
ISOLATION: THE CASE WILL BE ELECTRICALLY ISOLATED FROM THE COLL CIRCUIT*

MAGNETIC RADIATION

WORST CASE: FIELD WILL BE LESS THAN LEVEL STATED BELOW AT AMPLIFIER CLIPPING (.920 V).

124 dB re lµA/m

DISTANCE OF 6.3 mm FROM CENTER OF RECEIVER ANGLE OF 45 DEGREES FROM TUBE



MECHANICAL

PORT LOCATION: 12C

SOLDER TYPE: SAC305

TEMPERATURE

OPERATING: SENSITIVITY WILL NOT VARY MORE THAN

+1/-3 dB FROM -17°C TO 63°C

STORAGE: -40°C TO 63°C

Revision	C.O. #	Implementation Date	RELEASE LEVEL	REVISION
С	C10104863	10-16-06	_	
В	C10103956	2 - 23 - 06	Released	(
А	C10102730	6 - 1 6 - 0 5)

KNOWLES ELECTRONICS ITASCA, ILLINOIS U.S.A.

WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR DATE MMM 6-16-05 ELIMINATION OF EQUIPMENT AND TEST METHOD VARIATION CK. BY DATE TITLE: RECEIVER WBHC-23910-000 GJP 6-17-05 APP. BY DATE PERFORMANCE SPECIFICATION SHT 2.1

GJP

6-17-05

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