General Specifications



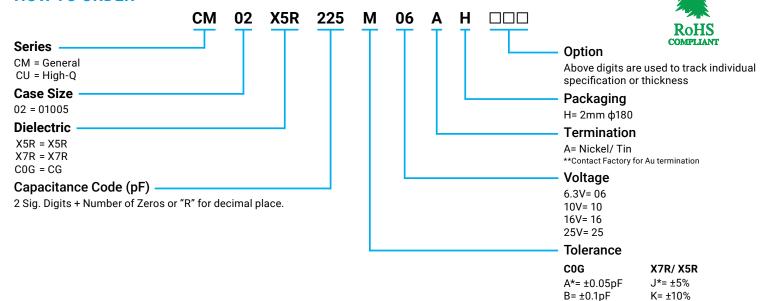


GENERAL DESCRIPTION

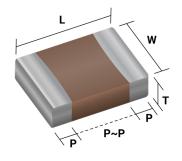
Offered in a complete range of products for both general and specialized applications and designed to meet a wide variety of needs. We have a worldwide network in order to supply our global customer bases quickly and efficiently. All of our products are highly reliable due to their monolithic structure of high-purity and superfine uniform ceramics and their integral internal electrodes.

Using Kyocera's latest manufacturing technology and materials with high dielectric constants, we produce extremely compact components with exceptional specifications. Our stringent quality control if every phase of production from material procurement to shipping ensures consistent manufacturing and superior quality.

HOW TO ORDER



DIMENSIONS



PACKAGING CODE

20kp		Р	8	3	2	2
100Pcs	Taping	Material	Taping	Width	Pitch	
	Code	Material	Code	Width	Code	Width
	Р	Paper	8	8 mm	2	2 mm

C= ±0.25pF

J= ±5%

*: Option

 $M = \pm 20\%$

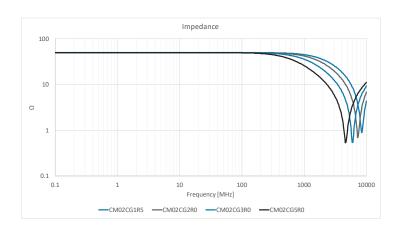
	Size	Cod	de	Dimension		Dimension (mm)						
ı	Size	EIA	JIS	Code	L	W	Т	P min.	P max.	P to P min.	ф180 Reel	
	02	01005	0402	Α	0.4±0.02	0.2±0.02	0.2±0.02	0.07	0.14	0.13	20kp(P8/2)	

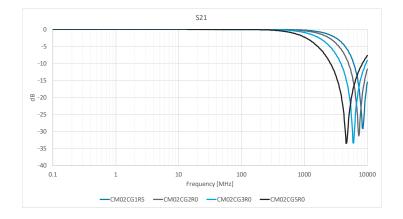


General Specifications

COG / NPO DIELECTRIC

(EIA	Size Code)				M02 005)		
()	Voltage /dc)		16			25	
	erance	В	С	J	В	С	J
	tance (pF)	±0.1pF	±0.25pF	±5%	±0.1pF	±0.25pF	±5%
R20	0.2						
R50	0.5					Α	
1R0	1.0						
1R5	1.5	Α					
2R0	2.0	A			Α		
3R0	3.0		Α		A		
4R0	4.0		A			A	
5R0	5.0					A	
6R0	6.0						
7R0	7.0						
8R0	8.0						
9R0	9.0						
100	10						
120	12						A
150	15			Α			
180	18						Α
220	22						
270	27						
330	33						
390	39						
470	47			Α			
560	56			_ A			
680	68						
820	82						
101	100						
121	120						
151	150						
181	180						
221	220			Α			





^{*}Please Contact for capacitance values other than standard



COG / NPO CAP CHART: Alphabets denotes dimensions.

Please refer to the below table for details.

	Dimension		Dimension (mn	٥)			Packaging				
Size	Dimension Code		Difficusion (IIIII	'')		ф 180 Reel					
	Code	L	W	Т	Code	Quantity	Taping Material	Taping Width	Cavity Pitch		
02	Α	0.4±0.02	0.2±0.02	0.2±0.02	Н	20,000	Paper	8mm	2mm		

< Standard Capacitor Value: E12 Series>





X5R DIELECTRIC

	ELECTRI					
_	Size			CM02		
•	Code)			(01005)		
	Voltage /dc)	6.3		10	16	
Tole	rance	K	M	М	K	М
Capa	acitace	±10%	±20%	±20%	±10%	±20%
101	100 pF					
151	150 pF					
221	220 pF					
331	330 pF					
471	470 pF					
681	680 pF				A8	A8
102	1000 pF				Ao	Ao
152	1500 pF					
222	2200 pF					
472	4700 pF					
682	6800 pF					
103	10000 pF					
153	15000 pF					
223	22000 pF					
333	33000 pF	A8	A8			
473	47000 pF					
104	0.10 µF			A8		
224	0.22 µF	////A\&///	\///kg///			
474	0.47 μF		////79////			
105	1.0 µF					
225	2.2 µF					
475	4.7 µF					
106	10 μF					
156	15 µF					
226	22 µF					

< Standard Capacitor Value> Cap Value < 0.1µF: E6 Series Cap value ≥ 0.1µF: E3 Series

X5R Tan δ Code	Tan δ
3	5.0% max.
4	7.0% max.
5	7.5 % max.
7	10.0% max.
8	12.5% max.
9	15.0% max.
10	20.0% max.

X7R DIELECTRIC

	Size (EIA Code)						
Rated Volt	16						
Capac	10						
101	100 pF						
151	150 pF						
221	220 pF						
331	330 pF						
471	470 pF						
681	680 pF						
102	1000 pF						
152	152 1500 pF						
222	2200 pF	A8					

< Standard Capacitor Value> Cap Value < 0.1µF: E6 Series

CM Standard Spec. 1

X7R Tan δ Code	Tan δ
2	3.5% max.
3	5.0% max.
5	7.5% max.

12.5% max.

X7R/ X5R CAP CHART: Two digit denotes dimensions and $\tan \delta$ code

Please refer to the below table for detail.

	Dimension		Dimension (mn	• •	Packaging							
Size	Dimension Code		Difficusion (IIIII	')		ф 180 Reel						
	Code	L	W	Т	Code	Quantity	Taping Material	Taping Width	Cavity Pitch			
02	Α	0.4±0.02	0.2±0.02	0.2±0.02	Н	20,000	Paper	8mm	2mm			

CM Standard Spec. 1

CM Standard Spec. 2



CM/CU (Standard Spec. 1) Specifications and Test Methods

Test Items			Т	est Conditions			Specifications			
Capacitano	e Value (C)	Capacitance		Frequency	V	'olt	Within Tolerance			
	n	C≤1000pF	1	MHz ±10%	0.5 to	5 Vrms	"C≥30pF : Q≥1000			
	Q	C≤1000pF		l kHz ±10%			C<30pF : Q≥400+20C"			
		Apply the rated v								
Insulation Re	esistance (IR)	temperature and			charge curren	t of the	Over $10000M\Omega$ or $500M\Omega$ nµF, whichever is less.			
		capacitor must n								
		Apply *3 times th				ne charge and				
Dielectric	Resistance	discharge curren		citor must not ex	ceed 50mA.		No defect			
		*CU02C△R20-12	20/25V: twice							
	arance	Microscope			No defect					
	n Strength	Apply a sideward					No defect			
Bending	Strength	Glass epoxy PCB	3: Fulcrum spa	acing: 90mm, du	ration time 10	Seconds.	No Significant damage with 1mm bending.			
	Appearance	"Vibration Freque	ency: 10-55 (H	łz)			No defect			
Vibration	ΔC	Amplitude: 1.5m					Within Tolerance			
Test	Q	Sweeping Condit			in X, Y and Z		"C≥30pF : Q≥1000			
	7	Directions: 2 hou	ırs each, 6 hoı	urs total"			C<30pF : Q≥400+20C"			
	Appearance	"Soak the Sample	o in 260°C + 5	°C colder for 10	+0 5 seconds	and place in	No defect			
		normal temperat								
	ΔC	(Pre-heating con-		uity. Measure ti	241 2 110urs.	Within ± 2.5% or ± 0.25 pF, whichever is large				
Soldering	_	(i re neuting con	Order	Temperature	Time	1	"C≥30pF : Q≥1000			
Heat	Q		1	80-100°C	2 min	-	C<30pF : Q≥400+20C"			
Resistant			2	150-200°C	2 min	-	•			
	IR						Over $10000M\Omega$ or $500M\Omega$ n μ F, whichever is less.			
	Withstanding	The charge and o				exceed	B :			
	Voltage	50mA for IR and	Resist without problem							
			S	Soak Condition:						
Solder	rability	Sn-3AG-0.5Cu 245 ±5°C 3 ±0.5 sec. Sn63 Solder 235 ±5°C 2 ±0.5 sec.					Solder Coverage : 95% min.			
00.00	.abty						Colder Coverage : 50% min.			
	A						No defect			
	Appearance	(Cycle) Room Temperatı	ıra (2min)							
	ΔC	Lowest Operating	a Tomporatur	o (20 min)			Within ± 2.5% or ± 0.25 pF, whichever is larger			
Temperature	Q	Room Temperati	y remperatur ura (2 min)	e (30 mm.)_		"C≥30pF: Q≥1000				
Cycle	IR	Highest Operatin		re (30 min)			C<30pF: Q≥400+20C"			
Cycle		After 5 cycles, m					Over $10000M\Omega$ or $500M\Omega$ n μ F, whichever is less.			
	Withstanding	The charge and o			citor must not	exceed	Resist without problem			
	Voltage	50mA for IR and			0.10000	Nesist without problem				
	Appearance					dia:	No defect			
Moisture	Δ C	After applying the					Within ± 7.5% or ± 0.75 pF, whichever is larger			
Resistant		of 40°C± 2°C and temperature and					"C≥30pF : Q≥200			
Load	Q	The charge and o					C<30pF : Q≥100+10C/3"			
Loau	IR	50mA for IR mea		rent or the capa	citoi must not	exceed	Over 500MΩ or 25MΩnμF, whichever is less.			
	IIX.	COTTO TO THE CO					•			
	Appearance						No defect			
		After applying *twice the rated voltage in the condition of 125±3°C for Within + 3					Within ± 3% or ± 0.3 pF,			
Hierb	1000-1012 hours, measure the sample after 24 ± 2 hours in normal						whichever is larger			
High-		temperature and				C≥30pF : Q≥350				
Temperature	capacitor must not exceed 50mA for IR measurement.									
Load	Q	** Applied voltag					10pF <c<30pf 2<="" :="" q≥275+5c="" th=""></c<30pf>			
	the chart below.					C<10pF: Q≥200+10C				
	IR						Over 1000MΩ or 50MΩnμF,			
							whichever is less.			

Please Ask for individual specification for the hatched range in previous chart. Voltage to be applied in the High Temperature Load (Applied Voltage is the multiple of the rated voltage)

Applied Voltage	Rated Voltage	Products		
X 1.0	16V	CM02CΔ221		
X 1.2	24V	CM02C∆R20-120		



CM Series (Standard Spec. 1 & 2) Specifications and Test Methods

Test	Items			Test Co	ondition	s			Specifications Standard Spec. 1	Specifications Standard Spec. 2
Capacitano	ce Value (C)		Me	easure after	r heat tre	atment			Within Tolerance	Within Tolerance
			pec. 1				oec. 2			
Та	n δ	C≤10 µF 1 kF C≤10 µF 120 l	tz ± 10%	Volt 1.0 ± 0.2 V _{rms} 0.5 ± 0.2 V _{rms}	C≤10 µ	F 1 kHz	± 10%	Volt 1.0 ± 0.2 V _{rms} 0.5 ± 0.2 V _{rms} 0.5 ± 0.2 V _{rms}	Refer to capacitance chart	Refer to capacitance chart
Insulation Re	esistance (IR)	The charge and discharge current of the capacitor must not exceed 50mA. Apply the rated voltage for 1 minute, and measure it in normal temperature and humidity. The charge and discharge current of the capacitor must not exceed 50mA.							Over 10000MΩ or 500MΩ•μF, whichever is less.	Over 50MΩ - μf
Dielectric	Resistance	Apply 2.5 times the rated voltage for 1-5 seconds. The charge and discharge current of the capacitor must not exceed 50mA.							No defect	No defect
Appea	arance	Microscope							No defect	No defect
Termination	on Strength	Apply a sidewar PCB-mounted s		of 100g (1N) to				No defect	No defect
Bending	Strength	Glass epoxy PCB: Fulcrum spacing: 90mm, duration time 10 seconds.						No Significant damage with 1mm bending.	No Significant damage with 1mm bending.	
	Appearance Take the initial value after heat treatment. Vibration Frequency: 10-55 (Hz)							No defect	No defect	
Vibration Test	ΔC	Amplitude: 1.5m Sweeping Cond Directions: 2 ho	tion: 10_					mnerature	Within Tolerance	Within Tolerance
	Tan δ	and humidity. Measure the sai			•	acc iii iioii	mar ter	Within Tolerance Within Tolerance		
	Appearance	Take the initial v							No defect	No defect
	ΔC	Soak the Sampl			er for 10:	±0.5 secoi	Within ± 7.5%	Within ± 7.5%		
	Tan δ	normal tempera Measure after h						Within Tolerance	Within Tolerance	
Soldering Heat	IR	(Pre-heating conditions) Order Temperature Time							Over 10000MΩ or 500MΩ•μF, whichever is less.	Over 50MΩ - μF
Resistant	Withstanding Voltage	The charge and for IR and Withs	1 2 discharge	80-1 150-2 e current of	00°C 200°C the capa	2 min 2 min citor mus	t not e	xceed 50mA	Resist without problem	Resist without problem
Solde	rability	Soak Condition:			245 ± 5°C 235 ± 5°C	3 ± 0.5 sec. 3 ± 0.5 sec.	_		Solder Coverage : 90% min.	Solder Coverage : 90% min.
	Appearance	Take initial value	after hea	at treatmen	t.				No defect	No defect
	ΔC	(Cycle) Room Temperat	a (Omain						Within ± 7.5%	Within ± 7.5%
	Tan δ	Lowest Operatir	a Tempe	ı.)_ rature (30 n	nin)				Within Tolerance	Within Tolerance
Temperature Cycle	IR	Room Temperat Highest Operati	ure (3 mi	n.) `					Over 10000MΩ or 500MΩ•μF, whichever is less.	Over 50MΩ - μF
	Withstanding Voltage	After 5 cycles, n The charge and for IR and Withs	discharge	e current of	the capa		t not e	xceed 50mA	Resist without problem	Resist without problem
	Appearance	Take the initial v							No defect	No defect
Moisture	ΔC	for 500-512 hou	rs in the c	condition of	40°C± 2°	C and 90	to 95%	6 RH, place	Within ± 12.5%	Within ± 12.5%
Resistant Load	Tan δ	in normal tempe treatment. The o							200% max. of initial value	200% max. of initial value
Load	IR	exceed 50mA fo	r IR meas	surement.					Over 500MΩ or 25MΩ=μF, whichever is less.	Over 10MΩ - μF
	Appearance	Take the initial v							No defect	No defect
High-	ΔC	the sample after charge and disc	heat trea	atment in no	ormal ten	nperature	and hu	ımidity. The	Within ± 12.5%	Within ± 12.5%
Temperature	Tan δ	IR measuremen		ieni oi the i	capacitoi	must not	CACEE	a Julia Iul	200% max. of initial value	200% max. of initial value
Load	IR	*X5R Spec 2:Ap X7R/X7R Spec 1 Applied Voltage	oly 1.0 tin : Apply 1.	.5 times wh	en the ra	ted Voltag	je is 10	OV or less.	Over 1000MΩ or 50MΩ=μF, whichever is less.	Over 10MΩ - μF
Heat Tr	eatment	Expose sample	to temper	ature of 14	10-150°C	for 1 hour	and le	eave the sampl	e in normal temperature and humid	ity for 24 ± 2 hours.

Voltage to be applied in the High Temperature Load (Applied Voltage is the multiple of the rated voltage)

Applied Voltage	Rated Voltage	Products
X 1.0	10V	CM02X5R104
X 1.3	6.3V	CM02X5R153-104
X 1.5	16V	CM02X5R101-103, CM02X7R222

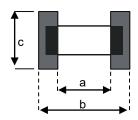
Applied Voltage	Rated Voltage	Products
X 1.0	6.3V	CM02X5R224,CM02X5R474



Test Conditions and Standards

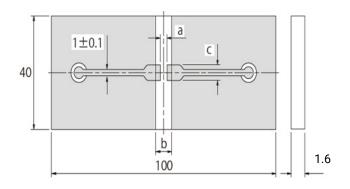


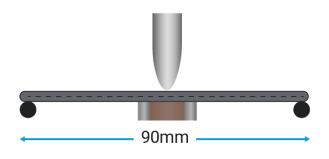
Substrate for Adhesion Strength Test, Vibration Test, Soldering Heat Resistance Test, Temperature Cycle Test, Load Humidity Test, High-Temperature with Loading Test.



			Unit: mm
Size (EIA Code)	A	В	С
02 (01005)	0.15	0.5	0.20

SUBSTRATE FOR BENDING TEST

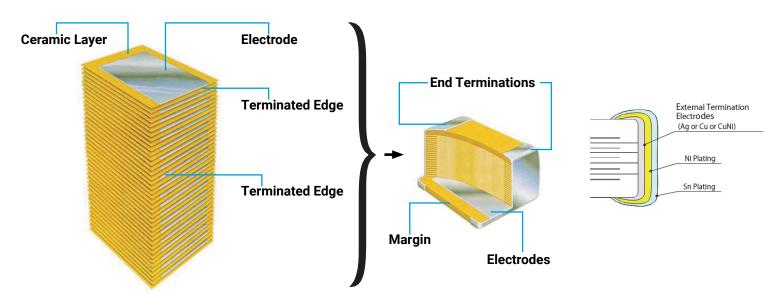




Testing Board: Glass Epoxy Board (CE4 or FR4)

Testing Board Thickness: 1.6 ± 0.2 mm* Circuit Thickness: 0.04 ± 0.01 mm

STRUCTURE

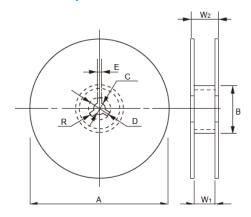


- · Please contact your local AVX Sales office or distributor for specifications not covered in this catalog.
- · Capacitance range is subject to change without notice
- Please contact sales representative to confirm compatibility with your application.





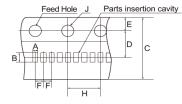
TAPE & REEL QUANTITIES

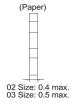


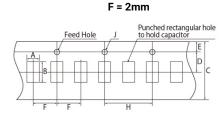
Code Reel	A	A B		D	
7- inch Reel (Code: H)	180 +0 -2.0	ф 60 min.	13 ±0.5	21 ±0.8	
Code Reel	E	W ₁	W ₂	R	
7- inch Reel (Code: H)	2.0 ±0.5	10.5 ±1.5	16.5 max.	1.0	

CARRIER TAPE

F = 1mm



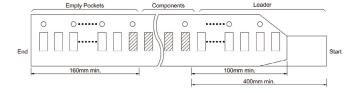






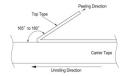
Size	Δ.	В	0	D	-	_	G	ш		Carrie	r Tape
(EIA Code)	A	В	C	, D	_	F	G		3	Width	Material
02 (01005)*	0.25 ± 0.03	0.45 ± 0.03	8.0 ± 0.3	3.5 ± 0.05	1.75 ± 0.1	2.0 ± 0.05	-	4.0 ± 0.1	1.5 + 0.1	8mm	Paper

DETAIL OF LEADER AND TRAILER



ADHESIVE TAPE

- 1. The exfoliative strength when peeling off the top tape from the carrier tape by the method of the following figure shall be *0.1 to 0.5N.
- 2. When the top tape is peeled off, the adhesive stays on the top tape.
- 3. Chip capacitors will be in a state free without being stuck on the thermal adhesive tape.2



CARRIER TAPE

- 1. Chip will not fall off from carrier tape or carrier tape will not be damaged by bending than within a radius of 25mm.
- 2. The chip are inserted continuously without any empty pocket.
- 3. Chip will not be mis-mounted because of too big clearance between components and cavity. Also the waste of carrier tape will not fill a nozzle hole of mounting machine.







DIMENSIONS FOR RECOMMENDED TYPICAL LAND

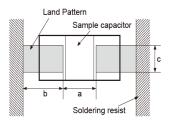
Since the amount of solder (size of fillet) to be used has direct influence on the capacitor after mounting, the sufficient consideration is necessary. When the amounts of solder is too much, the stress that a capacitor receives becomes larger. It may become the cause of a crack in the capacitor. When the land design of printed wiring board is considered, it is necessary to set up the form and size of land pattern so that the amount of solder is suitable.

GENERAL

"Size	Dime	nsion	Recommended Land Dimensions				
(EIA Code)"	L	W	а	b	С		
02 (01005)	0.4± 0.02	0.2± 0.02	0.13 to 0.20	0.12 to 0.18	0.20 to 0.23		

^{*} Recommended land dimensions may differ depending on dimensional tolerance.

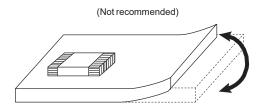
(General)

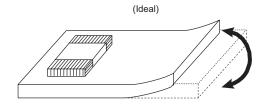


MOUNTING DESIGN

The chip could crack if the PCB warps during processing after the chip has been soldered.

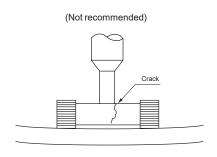
RECOMMENDED CHIP POSITION ON PCB TO MINIMIZE STRESS FROM PCB WARPAGE

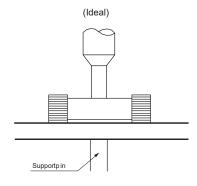




MOUNTING

- 1. If the position of the vacuum nozzle is too low, a large force may be applied to the chip capacitor during mounting, resulting in cracking.
- 2. During mounting, set the nozzle pressure to a static load of 1 to 3 N.
- 3. To minimize the shock of the vacuum nozzle, provide a support pin on the back of the PCB to minimize PCB flexure.





4. Bottom position of pick up nozzle should be adjusted to the top surface of a substrate when camber is corrected.

RESIN MOLD

- 1. If a large amount of resin is used for molding the chip, cracks may occur due to contraction stress during curing. To avoid such cracks, use a low shrinkage
- 2. The insulation resistance of the chip will degrade due to moisture absorption. Use a low moisture absorption resin.
- 3. Check carefully that the resin does not generate a decomposition gas or reaction gas during the curing process or during normal storage. Such gases may crack the chip capacitor or damage the device itself.



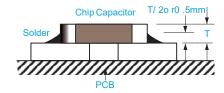
Surface Mounting Information



SOLDERING METHOD

The recommended fillet height shall be 1/2 of the thickness of capacitors or 0.5mm. When mounting two or more capacitors in the common land, it is necessary to separate the land with the solder resist strike so that it may become the exclusive land of each capacitor.

IDEAL SOLDER HEIGHT



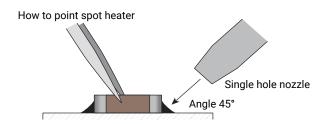
Item	Prohibited	Recommended example : Separation by solder resist
Multiple parts mount		Solder Resist
Mount with leaded parts	Leaded parts	Solder Resist Leaded Parts
Wire soldering after mounting	Soldering Iron Wire	Solder Resist
Side by side layout	Solder Resist	Solder Resist

DESIGN OF PRINTED CIRCUIT AND SOLDERING

- 1. Ceramic is easily damaged by rapid heating or cooling. If some heat shock is unavoidable, preheat enough to limit the temperature difference (Delta T) to within 150 degree Celsius.
- 2. The product size 1.6×0.8mm to 3.2×1.6mm can be used in reflow and wave soldering, and the product size of bigger than 3.2×1.6mm, or smaller than 1.6 ×0.8mm can be used in reflow. Circuit shortage and smoking can be created by using capacitors which are used neglecting the above caution.
- 3. Please see our recommended soldering conditions.
- 4. In case of using Sn-Zn Solder, please contact us in advance.
- 5. The following condition is recommended for spot heater application.

RECOMMENDED SPOT HEATER CONDITION

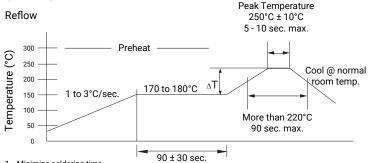
Item	Condition
Distance	5mm min.
Angle	45°
Projection Temp.	400℃ max.
Flow Rate	Set at the minimum
Nozzle Diameter	2φ to 4φ (Single hole type)
Application time	10 sec max.



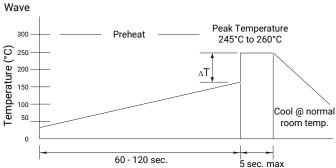
Surface Mounting Information



RECOMMENDED TEMPERATURE PROFILE (Sn-3Ag-0.5Cu)

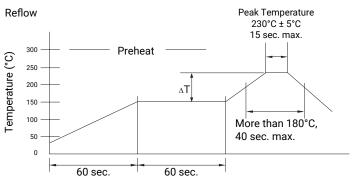


- 1. Minimize soldering time.
- Ensure that allowable temperature difference does not exceed 150°C.
- Ensure that allowable temperature difference does not exceed 130°C for 3.2×2.5mm size or larger.
- MLCC can withstand the above reflow conditions up to 3times.
- 5. N2atmosphere is recommended for reflow of products of 0.4mm×0.2mm size or smaller.

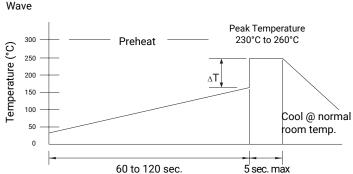


- Ensure that the chip capacitor is preheated adequately.
- Ensure that the temperature difference (ΔT) does not exceed 150°C.
- Cool naturally after soldering.MLCC can withstand the above reflow conditions up to 3times.
- Wave soldering is not applicable for chips with size of 3.2×2.5mm or larger of 1.0×0.5mm or smaller and capacitor arrays

RECOMMENDED TEMPERATURE PROFILE (63n Solder)



- 1. Minimize soldering time.
- Ensure that the temperature difference (ΔT) does not exceed 150°C.
- Ensure that the temperature difference (Δ T) does not exceed 130°C for 3.2×2.5mm size or larger. MLCC can withstand the above reflow conditions up to 3times.



- 1. Ensure that the chip capacitor is preheated adequately.
- Ensure that the temperature difference (ΔT) does not exceed 150°C.
 Cool naturally after soldering.
- Wave soldering is not applicable for chips with size of 3.2×2.5mm or larger of 1.0×0.5mm or smaller and capacitor arrays

Precautions



CIRCUIT DESIGN

- 1. Once application and assembly environments have been checked, the capacitor may be used in conformance with the rating and performance which are provided in both the catalog and the specifications. Use exceeding that which is specified may result in inferior performance or cause a short, open, smoking, or flaming to occur, etc.
- 2. Please consult the manufacturer in advance when the capacitor is used in devices such as: devices which deal with human life, i.e. medical devices; which are highly public orientated; and devices which demand a high standard of liability. Accident or malfunction of devices such as medical devices, space equipment and devices having to do with atomic power could generate grave consequence with respect to human lives or, possibly, a portion of the public. Capacitors used in these devices may require high reliability design different from that of general-purpose capacitors.
- 3. Please use the capacitors in conformance with the operating temperature provided in both the catalog and the specifications. Be especially cautious not to exceed the maximum temperature. In the situation the maximum temperature set forth in both the catalog and specifications is exceeded, the capacitor's insulation resistance may deteriorate, power may suddenly surge and short-circuit may occur. The capacitor has a loss and may self-heat due to equivalent series resistance when alternating electric current is passed there through. As this effect becomes especially pronounced in high frequency circuits, please exercise caution. When using the capacitor in a (self-heating) circuit, please make sure the surface of the capacitor remains under the maximum temperature for usage. Also, please make certain temperature rises remain below 20°C.
- 4. Please keep voltage under the rated voltage which is applied to the capacitor. Also, please make certain the peak voltage remains below the rated voltage when AC voltage is super-imposed to the DC voltage. In the situation where AC or pulse voltage is employed, ensure average peak voltage does not exceed the rated voltage. Exceeding the rated voltage provided in both catalog and specifications may lead to defective withstanding voltage or, in worst case situations, may cause the capacitor to smoke or flame.
- 5. When the capacitor is to be employed in a circuit in which there is continuous application of a high frequency voltage or a steep pulse voltage, even though it is within the rated voltage, please inquire to the manufacturer. In the situation the capacitor is to be employed using a high frequency AC voltage or an extremely fast rising pulse voltage, even though it is within the rated voltage, it is possible capacitor reliability will deteriorate.
- 6. It is a common phenomenon of high-dielectric products to have a deteriorated amount of static electricity due to the application of DC voltage. Due caution is necessary as the degree of deterioration varies depending on the quality of capacitor materials, capacity, as well as the load voltage at the time of operation.
- 7. Do not use the capacitor in an environment where it might easily exceed the respective provisions concerning shock and vibration specified in the catalog and specifications. In addition, it is a common piezo phenomenon of high dielectric products to have some voltage due to vibration or to have noise due to voltage change. Please contact sales in such case.
- If the electrostatic capacity value of the delivered capacitor is within the specified tolerance, please consider this when designing the respective product in order that the assembled product function appropriately.
- 9. Please contact us upon using conductive adhesives.

STORAGE

- If the component is stored in minimal packaging (a heat-sealed or zippered plastic bag), the bag should be kept closed. Once the bag has been opened, reseal it or store it in a desiccator.
- 2. Keep storage place temperature +5 to +40 $^{\circ}$ C, humidity 20 to 70% RH. See JIS C 6 0721-3-1, class 1K2 for other climatic conditions.
- The storage atmosphere must be free of corrosive gas such as sulfur dioxide and chlorine. Also, avoid exposing the product to saline moisture. If the product is exposed to such atmospheres, the terminals will oxidize and solderability will be effected.
- 4. Precautions 1) to 3) apply to chip capacitors packaged in carrier tapes.
- 5. The solderability is assured for 6 months from our shipping date if the above storage precautions are followed.





General CM02 SeriesSize (JIS Code): 01005(0402)# Packaging Code (Packaging quantity): H(20,000pcs.)

Dielectric code			Voltage [V]	Part Number			Dimension		# Packaging Code
CΔ	Capacitance	□:Tolerance			Q	L [mm]	W [mm]	T [mm]	(quantity)
	1.0pF			CM02C Δ 1R0 □ 25A#	420	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	1.5pF			CM02C Δ 1R5 □ 25A#	430	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	2.0pF	B: ± 0.1pF		CM02C Δ 2R0 □ 25A#	440	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	3.0pF	C: ± 0.25pF	25	CM02C ∆ 3R0 □ 25A#	460	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	4.0pF			CM02C Δ 4R0 □ 25A#	480	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	5.0pF			CM02C Δ 5R0 □ 25A#	500	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	6.0pF	C: ± 0.25pF		CM02C Δ 6R0 □ 25A#	520	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	7.0pF		0.5	CM02C Δ 7R0 □ 25A#	540	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	8.0pF	C: ± 0.25pF	25	CM02C Δ 8R0 □ 25A#	560	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	9.0pF			CM02C Δ 9R0 □ 25A#	580	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	10pF			CM02C Δ 100 □ 25A#	600	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	12pF			CM02C ∆ 120 □ 25A#	640	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
CG	15pF	J: ± 5%	25	CM02C ∆ 150 □ 25A#	700	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	18pF			CM02C ∆ 180 □ 25A#	760	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	22pF			CM02C Δ 220 □ 25A#	840	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	27pF	J: ± 5%		CM02C Δ 270 □ 16A#	940	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	33pF			CM02C Δ 330 □ 16A#	1000	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	39pF			CM02C Δ 390 □ 16A#	1000	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	47pF			CM02C ∆ 470 □ 16A#	1000	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	56pF		16	CM02C Δ 560 □ 16A#	1000	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	68pF			CM02C ∆ 680 □ 16A#	1000	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	82pF			CM02C Δ 820 □ 16A#	1000	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	100pF			CM02C ∆ 101 □ 16A#	1000	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	220pF			CM02C Δ 221 □ 16A#	1000	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	100pF			CM02X5R101 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	150pF			CM02X5R151 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	220pF			CM02X5R221 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	330pF	-		CM02X5R331 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	470pF			CM02X5R471 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	680pF	K: ± 10%		CM02X5R681 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	1000pF	M: ± 20%	16	CM02X5R102 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	1500pF			CM02X5R152 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	2200pF			CM02X5R222 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	4700pF			CM02X5R472 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	H
X5R	6800pF			CM02X5R682 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
2.0.1	10000pF			CM02X5R103 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	15000pF			CM02X5R153 □ 06A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	22000pF	K: ± 10%		CM02X5R223 □ 06A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	33000pF	M: ± 20%	6.3	CM02X5R333 □ 06A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	47000pF			CM02X5R473 □ 06A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	0.10µF	M: ± 20%	10	CM02X5R104 □ 10A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	H
	·	K: ± 10%							
	0.10µF	M: ± 20%	6.3	CM02X5R104 □ 06A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	0.22µF	M: ± 20%	6.3	CM02X5R224M06A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	0.47µF		0.3	CM02X5R474M06A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
X7R	2200µF	K: ± 10%	16	CM02X7R222 □ 16A#	12.5	0.4 ± 0.02	0.2 ± 0.02	0.2 ± 0.02	Н
	·	M: ± 20%							





General CM02 SeriesSize (JIS Code): 01005(0402)# Packaging Code (Packaging quantity): H(20,000pcs.)

Dielectric code			Voltage [V]			Dimension	# Packaging Code	
CΔ	Capacitance	□:Tolerance		Part Number	L [mm]	W [mm]	T [mm]	(quantity)
	R50			CU02C Δ R50 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	1.0pF			CU02C ∆ 1R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	1.5pF			CU02C ∆ 1R5 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	2.0pF			CU02C ∆ 2R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	3.0pF			CU02C ∆ 3R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	4.0pF	C: ± 0.25pF	25	CU02C Δ 4R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
CG	5.0pF			CU02C Δ 5R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	6.0pF			CU02C Δ 6R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	7.0pF			CU02C Δ 7R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	8.0pF			CU02C Δ 8R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	9.0pF			CU02C Δ 9R0 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	10pF	J: ± 5%	25	CU02C ∆ 100 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	12pF			CU02C ∆ 120 □ 25AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	R20	"B: ± 0.1pF C: ± 0.25pF "		CU02C Δ R20 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	R50			CU02C Δ R50 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	1R0			CU02C Δ 1R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	1R5		16	CU02C Δ 1R5 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	2R0		16	CU02C Δ 2R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	3R0			CU02C Δ 3R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	4R0			CU02C Δ 4R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	5R0			CU02C Δ 5R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	6R0			CU02C ∆ 6R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	7R0			CU02C Δ 7R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	8R0	C: ± 0.25pF	16	CU02C Δ 8R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	9R0			CU02C Δ 9R0 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	10pF			CU02C Δ 100 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
VED	12pF	J: ± 5%		CU02C Δ 120 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
X5R	15pF		16	CU02C ∆ 150 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	18pF			CU02C Δ 180 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н
	22pF			CU02C ∆ 220 □ 16AH	0.4± 0.02	0.2± 0.02	0.2± 0.02	Н

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C1206C101J1GAC C1608C0G2A221J C1608X7R1E334K C2012C0G2A472J 2220J2K00562KXT KHC201E225M76N0T00
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CGA2B2C0G1H6R8D