IDC Low Inductance Capacitors (RoHS)

0306/0612/0508 IDC (InterDigitated Capacitors)

GENERAL DESCRIPTION

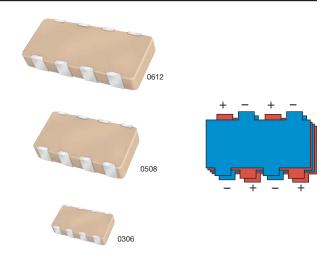
Inter-Digitated Capacitors (IDCs) are used for both semiconductor package and board level decoupling. The equivalent series inductance (ESL) of a single capacitor or an array of capacitors in parallel determines the response time of a Power Delivery Network (PDN). The lower the ESL of a PDN, the faster the response time. A designer can use many standard MLCCs in parallel to reduce ESL or a low ESL Inter-Digitated Capacitor (IDC) device. These IDC devices are available in versions with a maximum height of 0.95mm or 0.55mm.

IDCs are typically used on packages of semiconductor products with power levels of 15 watts or greater. Inter-Digitated Capacitors are used on CPU, GPU, ASIC, and ASSP devices produced on 0.13µ, 90nm, 65nm, and 45nm processes. IDC devices are used on both ceramic and organic package substrates. These low ESL surface mount capacitors can be placed on the bottom side or the top side of a package substrate. The low profile 0.55mm maximum height IDCs can easily be used on the bottom side of BGA packages or on the die side of packages under a heat spreader.

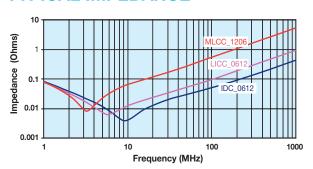
IDCs are used for board level decoupling of systems with speeds of 300MHz or greater. Low ESL IDCs free up valuable board space by reducing the number of capacitors required versus standard MLCCs. There are additional benefits to reducing the number of capacitors beyond saving board space including higher reliability from a reduction in the number of components and lower placement costs based on the need for fewer capacitors.

The Inter-Digitated Capacitor (IDC) technology was developed by AVX. This is the second family of Low Inductance MLCC products created by AVX. IDCs are a cost effective alternative to AVX's first generation low ESL family for high-reliability applications known as LICA (Low Inductance Chip Array).

AVX IDC products are available with a lead-free finish of plated Nickel/Tin.



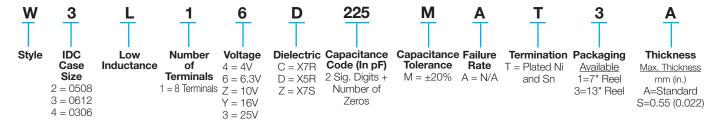
TYPICAL IMPEDANCE







HOW TO ORDER



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

PERFORMANCE CHARACTERISTICS

Capacitance Tolerance	±20% Preferred
Operation	X7R = -55°C to +125°C
Temperature Range	X5R = -55°C to +85°C
	X7S = -55°C to +125°C
Temperature Coefficient	±15% (0VDC), ±22% (X7S)
Voltage Ratings	4, 6.3, 10, 16, 25 VDC
Dissipation Factor	≤ 6.3V = 6.5% max; 10V = 5.0% max; ≥ 16V = 3.5% max
Insulation Resistance (@+25°C, RVDC)	100,000M Ω min, or 1,000M Ω per μF min.,whichever is less

Dielectric Strength	No problems observed after 2.5 x RVDC for 5 seconds at 50mA max current
CTE (ppm/C)	12.0
Thermal Conductivity	4-5W/M K
Terminations Available	Plated Nickel and Solder

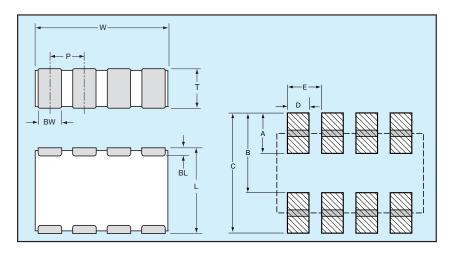


IDC Low Inductance Capacitors (RoHS)

0306/0612/0508 IDC (InterDigitated Capacitors)

SIZE	03	06		Th	in 05	808				0508	3			Thin	0612)			0612			THICK 0612					
Max. mm	0.0				0.55.					0.95					55			0.95			1.22						
Thickness (in.)	(0.0)			_	(0.022)	_		<u> </u>	_	(0.037)			<u> </u>	(0.0	_			_	(0.037)			(0.048)					
WVDC	4	6.3	4	6.3	10	16	25	4	6.3	10	16	25	4	6.3	10	16	4	6.3	10	16	25	4	6.3	10	16		
Cap (µF) 0.010																											
0.022																											
0.033																											
0.047																											
0.068																											
0.10																											
0.22																											
0.33																											
0.47																											
0.68																											
1.0																											
1.5																											
2.2																											
3.3																											

PHYSICAL DIMENSIONS AND PAD LAYOUT



Consult factory for additional requirements



PHYSICAL CHIP DIMENSIONS millimeters (inches)

SIZE	W	L	BW	BL	Р
0306	1.60 ± 0.20	0.82 ± 0.10	0.25 ± 0.10	0.20 ± 0.10	0.40 ± 0.05
	(0.063 ± 0.008)	(0.032 ± 0.006	(0.010 ± 0.004)	(0.008± 0.004)	(0.015 ± 0.002)
0508	2.03 ± 0.20	1.27 ± 0.20	0.30 ± 0.10	0.25 ± 0.15	0.50 ± 0.05
	(0.080 ± 0.008)	(0.050 ± 0.008)	(0.012 ± 0.004)	(0.010± 0.006)	(0.020 ± 0.002)
0612	3.20 ± 0.20	1.60 ± 0.20	0.50 ± 0.10	0.25 ± 0.15	0.80 ± 0.10
	(0.126 ± 0.008)	(0.063 ± 0.008)	(0.020 ± 0.004)	(0.010 ± 0.006)	(0.031 ± 0.004)

PAD LAYOUT DIMENSIONS

SIZE	Α	В	С	D	Е		
0306	0.38	0.89	1.27	0.20	0.40		
	(0.015)	(0.035)	(0.050)	(0.008)	(0.015)		
0508	0.64	1.27	1.91	0.28	0.50		
	(0.025)	(0.050)	(0.075)	(0.011)	(0.020)		
0612	0.89	1.65	2.54	0.45	0.80		
	(0.035)	(0.065)	(0.010)	(0.018)	(0.031)		



IDC Low Inductance Capacitors (SnPb)

0306/0612/0508 IDC with Sn/Pb Termination

GENERAL DESCRIPTION

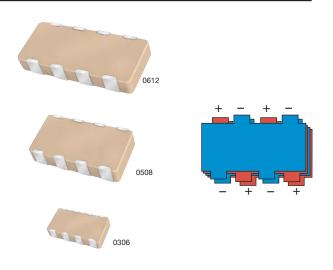
Inter-Digitated Capacitors (IDCs) are used for both semiconductor package and board level decoupling. The equivalent series inductance (ESL) of a single capacitor or an array of capacitors in parallel determines the response time of a Power Delivery Network (PDN). The lower the ESL of a PDN, the faster the response time. A designer can use many standard MLCCs in parallel to reduce ESL or a low ESL Inter-Digitated Capacitor (IDC) device. These IDC devices are available in versions with a maximum height of 0.95mm or 0.55mm.

IDCs are typically used on packages of semiconductor products with power levels of 15 watts or greater. Inter-Digitated Capacitors are used on CPU, GPU, ASIC, and ASSP devices produced on 0.13µ, 90nm, 65nm, and 45nm processes. IDC devices are used on both ceramic and organic package substrates. These low ESL surface mount capacitors can be placed on the bottom side or the top side of a package substrate. The low profile 0.55mm maximum height IDCs can easily be used on the bottom side of BGA packages or on the die side of packages under a heat spreader.

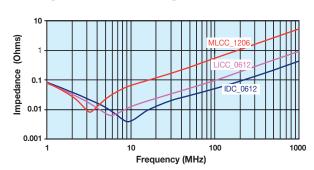
IDCs are used for board level decoupling of systems with speeds of 300MHz or greater. Low ESL IDCs free up valuable board space by reducing the number of capacitors required versus standard MLCCs. There are additional benefits to reducing the number of capacitors beyond saving board space including higher reliability from a reduction in the number of components and lower placement costs based on the need for fewer capacitors.

The Inter-Digitated Capacitor (IDC) technology was developed by AVX. This is the second family of Low Inductance MLCC products created by AVX. IDCs are a cost effective alternative to AVX's first generation low ESL family for high-reliability applications known as LICA (Low Inductance Chip Array).

AVX IDC products are available with a lead termination for high reliability military and aerospace applications that must avoid tin whisker reliability issues.

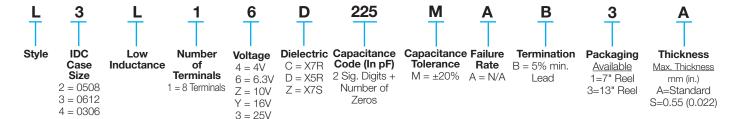


TYPICAL IMPEDANCE



Not RoHS Compliant

HOW TO ORDER



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

PERFORMANCE CHARACTERISTICS

Capacitance Tolerance	±20% Preferred
Operation	X7R = -55°C to +125°C
Temperature Range	X5R = -55°C to $+85$ °C
	X7S = -55°C to +125°C
Temperature Coefficient	±15% (0VDC), ±22% (X7S)
Voltage Ratings	4, 6.3, 10, 16, 25 VDC
Dissipation Factor	\leq 6.3V = 6.5% max;
	10V = 5.0% max;
	≥ 16V = 3.5% max
Insulation Resistance	100,000MΩ min, or 1,000MΩ per
(@+25°C, RVDC)	μF min.,whichever is less

Dielectric Strength	No problems observed after 2.5 x RVDC for 5 seconds at 50mA max current
CTE (ppm/C)	12.0
Thermal Conductivity	4-5W/M K
Terminations Available	Plated Nickel and Solder

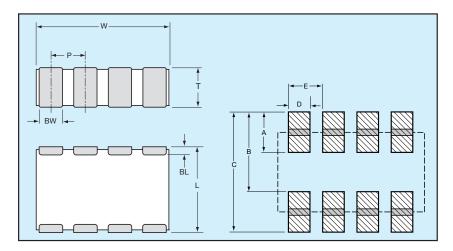


IDC Low Inductance Capacitors (SnPb)

0306/0612/0508 IDC with Sn/Pb Termination

SIZE	03	06		Th	in 05	08				0508	3			Thin	0612				0612			1	HICH	C 061	2
Max. mm	0.				0.55.					0.95					55			0.95			1.22				
Thickness (in.)	(0.0)			_	(0.022)				_	(0.037)				(0.0				_	(0.037)			(0.048)			
WVDC	4	6.3	4	6.3	10	16	25	4	6.3	10	16	25	4	6.3	10	16	4	6.3	10	16	25	4	6.3	10	16
Сар (µF) 0.010																									
0.022																									
0.033																									
0.047																									
0.068																									
0.10																									
0.22																									
0.33																									
0.47																									
0.68																									
1.0																									
1.5																									
2.2																									
3.3																									

PHYSICAL DIMENSIONS AND PAD LAYOUT



Consult factory for additional requirements



PHYSICAL CHIP DIMENSIONS millimeters (inches)

SIZE	W	L	BW	BL	Р
0306	1.60 ± 0.20	0.82 ± 0.10	0.25 ± 0.10	0.20 ± 0.10	0.40 ± 0.05
	(0.063 ± 0.008)	(0.032 ± 0.006	(0.010 ± 0.004)	(0.008± 0.004)	(0.015 ± 0.002)
0508	2.03 ± 0.20	1.27 ± 0.20	0.30 ± 0.10	0.25 ± 0.15	0.50 ± 0.05
	(0.080 ± 0.008)	(0.050 ± 0.008)	(0.012 ± 0.004)	(0.010± 0.006)	(0.020 ± 0.002)
0612	3.20 ± 0.20	1.60 ± 0.20	0.50 ± 0.10	0.25 ± 0.15	0.80 ± 0.10
	(0.126 ± 0.008)	(0.063 ± 0.008)	(0.020 ± 0.004)	(0.010 ± 0.006)	(0.031 ± 0.004)

PAD LAYOUT DIMENSIONS

SIZE	А	В	С	D	Е		
0306	0.38	0.89	1.27	0.20	0.40		
	(0.015)	(0.035)	(0.050)	(0.008)	(0.015)		
0508	0.64	1.27	1.91	0.28	0.50		
	(0.025)	(0.050)	(0.075)	(0.011)	(0.020)		
0612	0.89	1.65	2.54	0.45	0.80		
	(0.035)	(0.065)	(0.010)	(0.018)	(0.031)		



X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Capacitor Arrays & Networks category:

Click to view products by Kyocera AVX manufacturer:

Other Similar products are found below:

CKCL22C0G1H150K CKCL22C0G1H680K CKCL22X5R0J105M CKCL22X5R1A474M CKCL22X7R1H103M CKCL44C0G1H151K

CKCL44X7R1C223M CKCM25C0G1H470K CKCM25C0G1H680K CKCM25X5R0J474M CKCM25X5R1C223M CKCM25X7R1H222M

CKCM25X5R1A473M CKCM25X7R1H472M CKCM25X5R0J105M CKCL44X5R1A473M CKCL22X7R1H223M CKCL22X7R1H102M

CKCL22X5R1C224M CKCL22C0G1H221K CKCL22C0G1H151K 2255-126-15636 FE2HX476M500LGL 6124CG470J500NT

KGA05AS70J104MH NCA1206X7R103K16TRPF CA064C103M5RACTU CA064C330K5GACTU 20108D3X332K5E 20108D1X103K5E

CA064C103K5RACTU CA064C104K4RACTU C1632C223M5RAC3020 CA064C102K5RACTU CLLC1AX7S0G474M050AC

CLLE1AX7R1A104M CKCL22X7R1H473M 20115D1C271K5P AC0612JRNPO9BN101 AC0612JRNPO9BN221 AC0612JRNPO9BN271

AC0612JRNPO9BN471 CLLC1AX7S0G225M050AC 20609TC101J101ME AC0612KRX7R7BB473 CA064C221K5GACTU

20110D1X103K5E W2A25C103KAT2A 20105D1X104K5P 20110D4X104K5E