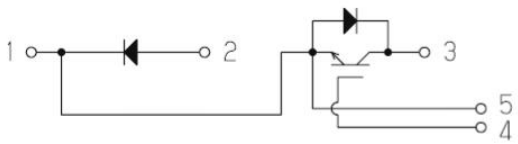
KYOCERA

IGBT Module
100 A／1200V

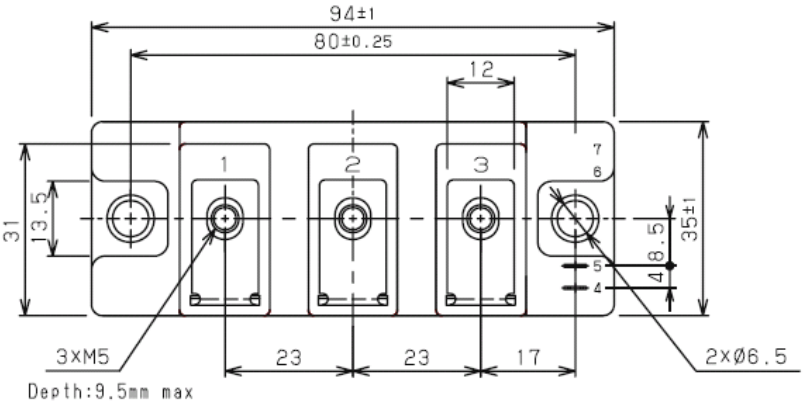
PCFMB100W12

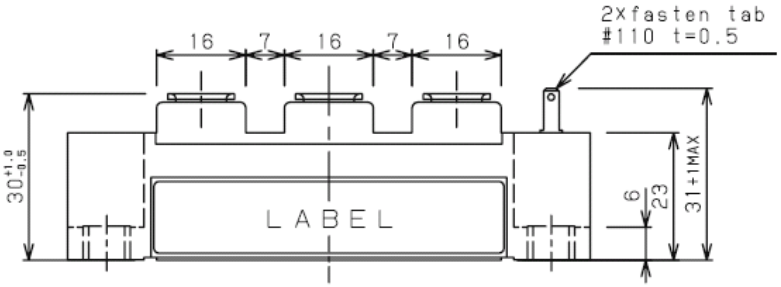
□ 回路図 ： *CIRCUIT*



□ 概略図 ： *SCHEMATIC DIAGRAM*

Dimension: [mm]





□ 最大定格 ： *MAXIMUM RATINGS* (at Tc=25℃ unless otherwise specified)

	Item		Symbol	Condition	Rated Value	Unit
IGBT	コレクタ・エミッタ間電圧 Collector-Emitter Voltage		VCES	G-E Short	1200	V
	ゲート・エミッタ間電圧 Gate-Emitter Voltage		VGES	C-E Short	±20	V
	コレクタ電流 Collector Current		IC	DC Tc=85℃	100	A
			ICP	Pulse ≦ 1ms	200	
	コレクタ損失 Collector Power Dissipation		PC	Tj=175℃	483	W
				Tj=150℃	403	
FWD	繰り返しピーク逆電圧 Repetitive peak reverse voltage		VRRM		1200	V
	順電流 Forward Current		IF		100	A
			IFM	Pulse ≦ 1ms	200	
最大接合温度 Maximum Junction Temperature			TjMAX	瞬時動作(過負荷) Instantaneous Overload	175	℃
接合温度 Junction Temperature Range			Tj		−40～+150	℃
保存温度 Storage Temperature Range			Tstg		−40～+125	℃
絶縁耐圧 Isolation Voltage			VISO	Terminal to Base AC,1minute	2,500	V (RMS)
締め付けトルク Mounting Torque		Module Base to Heatsink	Ftor	M6	3	N・m
		Busbar to Main Terminal		M5	2	

□ 電気的特性 : *ELECTRICAL CHARACTERISTICS* (at T_j=25℃ unless otherwise specified)

Characteristic			Symbol	Test Condition		Min.	Typ.	Max.	Unit
IGBT	コレクタ遮断電流 Collector-Emitter Cut-Off Current		ICES	VCE= 1200V,VGE= 0V		—	—	1.0	mA
	ゲート漏れ電流 Gate-Emitter Leakage Current		IGES	VGE= ±20V,VCE= 0V		—	—	1.0	μA
	コレクタ・エミッタ間飽和電圧 Collector-Emitter Saturation Voltage		VCE(sat.)	IC= 100A,VGE= 15V (chip level)	Tj=25℃	—	1.50	2.00	V
					Tj=125℃	—	1.70	—	
					Tj=150℃	—	1.80	—	
	ゲートしきい値電圧 Gate-Emitter Threshold Voltage		VGE(th.)	VCE= 10V,IC= 3.3mA		4.8	—	7.0	V
	入力容量 Input Capacitance		Cies	VCE= 25V,VGE= 0V,f= 1MHz		—	10.0	—	nF
	出力容量 Output Capacitance		Coes			—	0.30	—	
	帰還容量 Reverse Transfer Capacitance		Cres			—	0.23	—	
	ゲート電荷量 Gate Charge		Qg	Vcc=600V, Ic=100A, VGE=-15～+15V		—	1100	—	nC
	スイッチング時間 Switching Time	上昇時間 Rise Time	tr	Vcc= 600V Ls=38nH Ic= 100A Inductive Load RG= 7.5Ω VGE= ±15V Tj= 150℃		—	60	—	ns
		ターンオン遅延時間 Turn-on Delay Time	td(on)			—	110	—	
		下降時間 Fall Time	tf			—	190	—	
		ターンオフ遅延時間 Turn-off Delay Time	td(off)			—	500	—	
FWD	順電圧 Peak Forward Voltage		VF	IF= 100A,VGE=0V (chip level)	Tj=25℃	—	2.00	2.60	V
					Tj=125℃	—	1.98	—	
					Tj=150℃	—	1.95	—	
	逆回復時間 Reverse Recovery Time		trr	Vcc= 600V Ls=38nH Ic= 100A Inductive Load RG= 7.5Ω VGE= ±15V Tj= 150℃		—	170	—	ns
内部配線抵抗 Internal Lead Resistance			RCC'+EE'	主端子—チップ間 ／ 1素子 Main Terminal - Chip / Per 1 Arm		—	—	1	mΩ
内部インダクタンス Stray Inductance			LsCE	メイン端子3－2間 Main Terminal 3 - Main Terminal 2		—	30	—	nH

□ 熱的特性 : *THERMAL CHARACTERISTICS*

Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit
熱抵抗 Thermal Resistance	IGBT	Rth(j-c)	Junction to Case Per 1 Arm (Tc測定点:チップ直下)	—	—	0.31	℃／W
	FWD			—	—	0.72	
接触熱抵抗 Thermal Resistance	IGBT	Rth(c-f)	Case to heatsink Per 1 Arm Paste=1W/(m･℃)	—	0.10	—	
	FWD			—	0.17	—	

□ 特性図 ：*CHARACTERISTICS CURVES*

Fig.1- Output Characteristics (Typical)

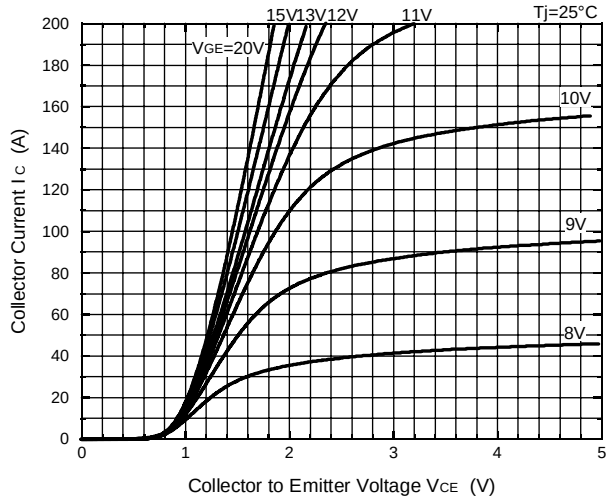


Fig.2- Saturation Voltage Characteristics (Typical)

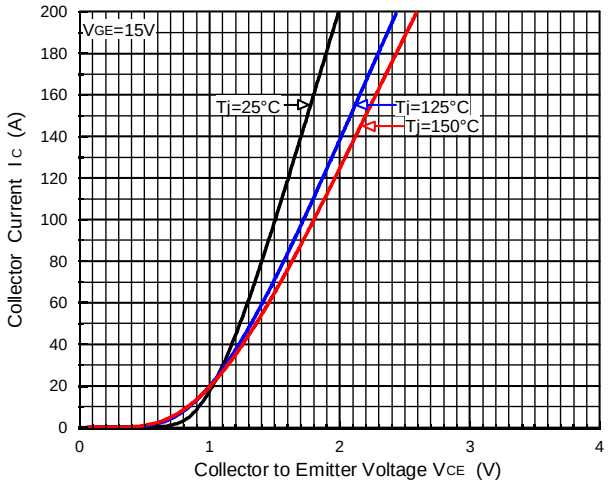


Fig.3- Transfer Characteristics (Typical)

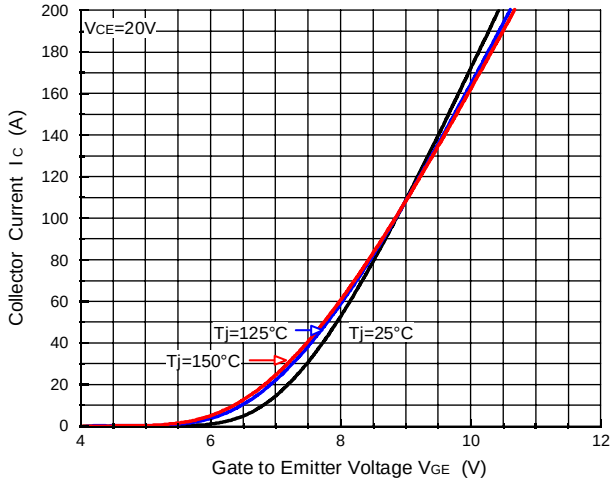


Fig.4- Gate Charge vs. Collector to Emitter Voltage (Typical)

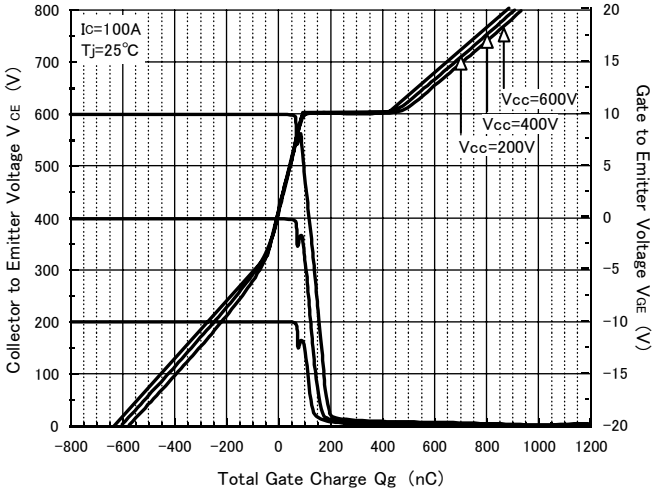


Fig.5- Collector Current vs. Switching Time

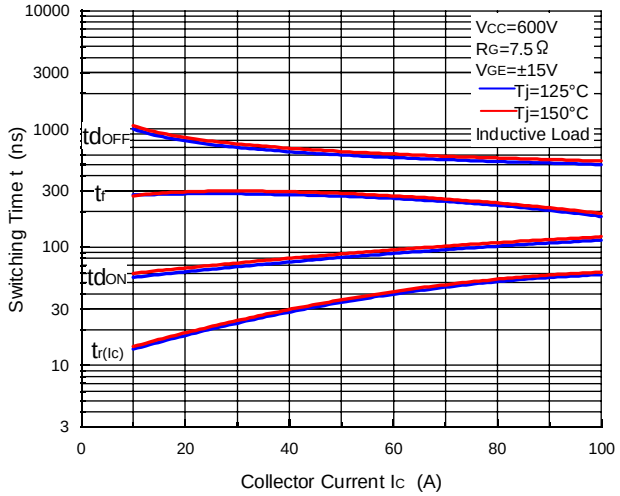
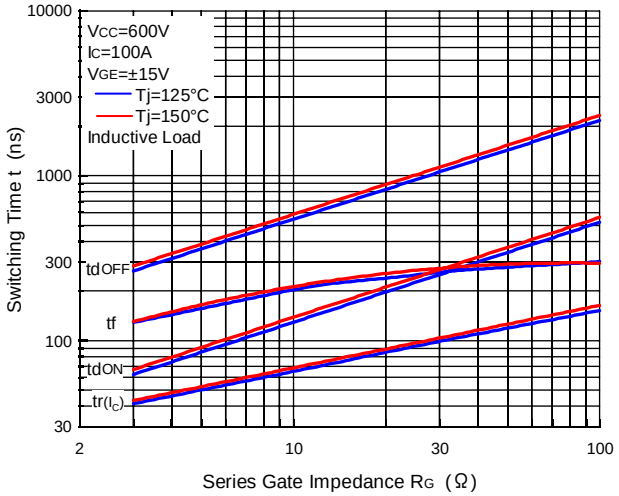


Fig.6- Series Gate Impedance vs. Switching Time



□ 特性図 ：*CHARACTERISTICS CURVES*

Fig.7- Collector Current vs. Switching Loss

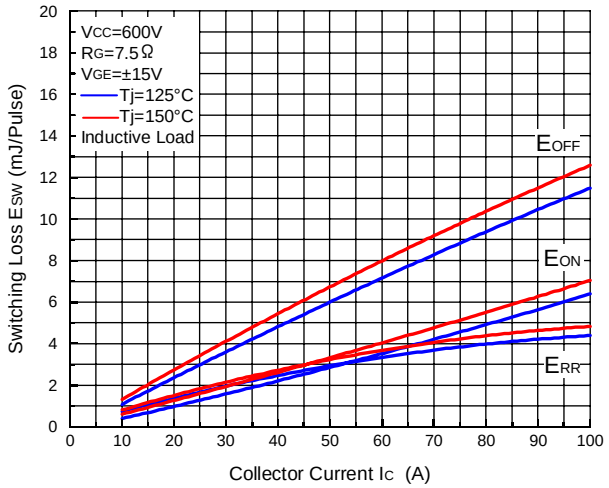


Fig.8- Series Gate Impedance vs. Switching Loss

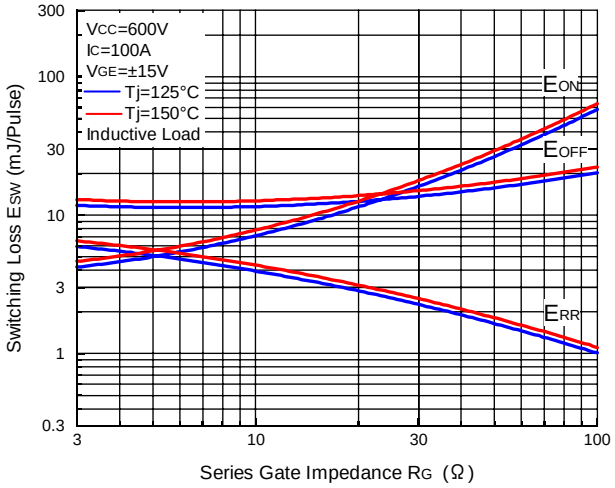


Fig.9- Forward Characteristics of FWD (Typical)

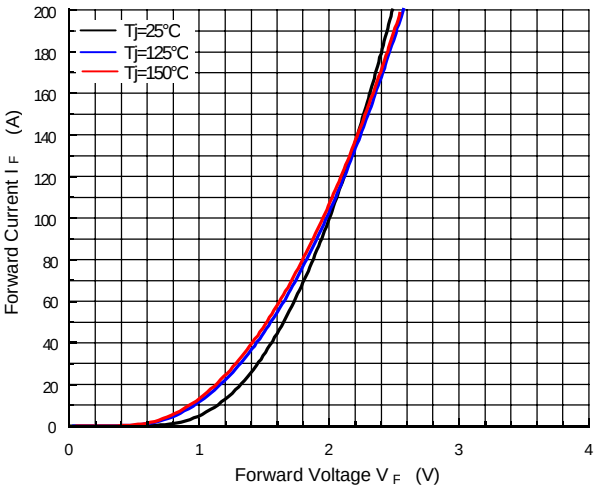


Fig.10- Reverse Recovery Characteristics of FWD & Inverse Diode (Typical)

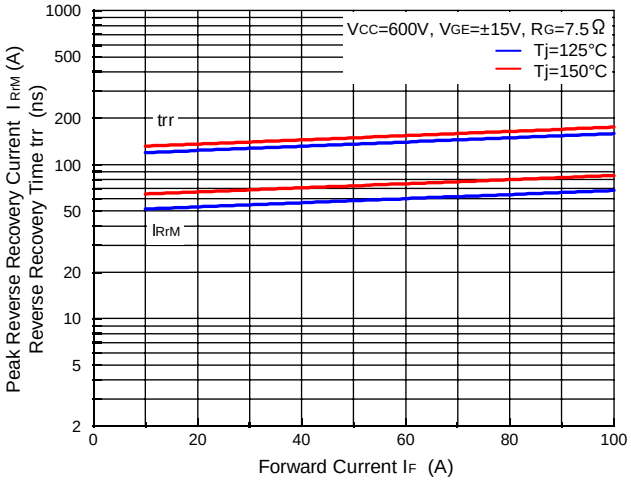


Fig.11- Reverse Bias Safe Operating Area

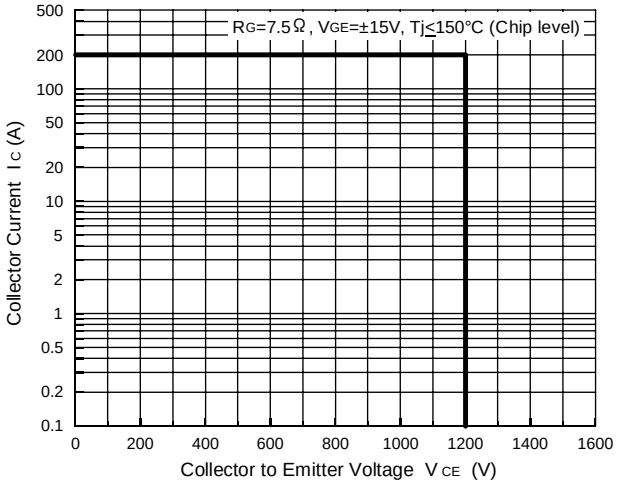
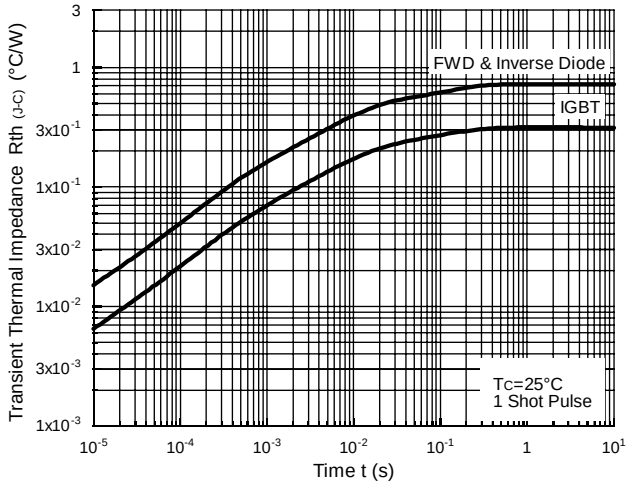


Fig.12- Transient Thermal Impedance



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