## **THJ Extended Series**

## High Temperature (200°C max.) - J-Lead





### **FEATURES**

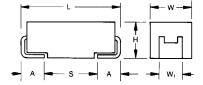
- SMD 200°C Tantalum Capacitor
- 200°C @ 0.33VR 1000hrs Continuous Operation
- Leakage Current After 200°C 1000hrs Less than 1mA
- 3x Reflow 260°C
- 100% Surge Current Tested
- Gold Plated Termination for Hybrid Assembly
- Oil Drilling, Aerospace, Automotive Applications
- CV Range: 10-220µF / 10-16V
- · 2 Case Sizes Available



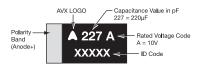


## **APPLICATIONS**

· Downhole Drilling



## **MARKING** B, E CASE



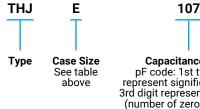
#### **CASE DIMENSIONS:**

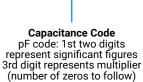
millimeters (inches)

Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H+0.20 (0.008) -0.10 (0.004)	W <sub>1</sub> ±0.20 (0.008)	A+0.30 (0.012) -0.20 (0.008)	S Min.	
В	1210	3528-21	3.50 (0.138)	2.80 (0.110)	1.90 (0.075)	2.20 (0.087)	0.80 (0.031)	1.40 (0.055)	
E	2917	7343-43	7.30 (0.287)	4.30 (0.169)	4.10 (0.162)	2.40 (0.094)	1.30 (0.051)	4.40 (0.173)	

W<sub>1</sub> dimension applies to the termination width for A dimensional area only.

## **HOW TO ORDER**







 $M = \pm 20\%$ 









### **TECHNICAL SPECIFICATIONS**

Technical Data:		All technical data relate to an ambient temperature of +25°C						
Capacitance Range:		10 μF to 220 μF						
Capacitance Tolerance:	±10%; ±20%							
Leakage Current DCL @ V <sub>R</sub> 25°C	0.01CV							
Leakage Current DCL @ V <sub>c</sub> 200°C,	1000 hrs	1mA						
Rated Voltage (V <sub>R</sub> )	≤ +85°C:	10	16					
Category Voltage (V <sub>C</sub> )	≤ +200°C:	3.3	5.3					
Surge Voltage (V <sub>S</sub> )	≤ +85°C:	13	20					
Surge Voltage (V <sub>s</sub> )	≤ +200°C:	4.3	6.5					
Temperature Range:	-55°C up 200°C with voltage derating							
Reliability:	0.5% per 1000 hours at 85°C, $V_R$ with 0.1 $\Omega$ /V series impedance,							
		1000 hrs at 200°C, 0.33V <sub>R</sub>						
Termination Finished:		Gold Plating						

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# CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Сара	citance	Rated voltage (V <sub>R</sub> ) to 85°C (Voltage Code)					
μF	Code	10V (A)	16V (C)				
10	106		В				
15	156						
100	107		E				
150	157						
220	227	E					

Released ratings

Note: Voltage ratings are minimum values. AVX reserves the right to supply

higher voltage ratings in the same case size, to the same reliability standards.

### **RATINGS & PART NUMBER REFERENCE**

AVX	Case Size		Rated	Rated	Rated Category Voltage (°C) (V)	Category Temperature (°C)	DCL Max. @ V <sub>R</sub> 25°C (μA)	DCL Max. @ VC 200°C 1000 hrs (mA)		ESR Max. @ 100kHz (Ω)	100kHz RMS Current (mA)				MOI
Part No.			Voltage (V)								25°C	85°C	175°C	200°C	MSL
	10 Volt @ 85°C														
THJE227*010#JH	E	220	10	85	3.3	200	22	1.0	10	0.25	812	731	162	81	1 <sup>1)</sup>
	16 Volt @ 85°C														
THJB106*016#JH	В	10	16	85	5.3	200	1.6	1.0	6	2.8	174	157	35	17	1
THJE107*016#JH	E	100	16	85	5.3	200	16	1.0	8	0.25	812	731	162	81	1 <sup>1)</sup>

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

All PNs also available with Dry pack option - MSL 3 (see How to order).

Base terminations material is copper for E case size and Nilo42 for B case size.

All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5V RMS with a maximum DC bias of 2.2 volts.

DCL is measured at rated voltage after 5 minutes.

For typical weight and composition see page 274.

NOTE: AVX reserves the right to supply higher voltage ratings or tighter tolerance part in the same case size, to the same reliability standards.

<sup>1) -</sup>Dry pack option (see How to order) recommended for reduction of stress during soldering.

# **THJ Extended Series**

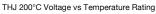
# High Temperature (200°C max.) - J-Lead

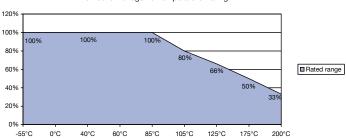


## **QUALIFICATION TABLE**

TEST	THJ 200°C series (Temperature range -55°C to +200°C)											
IESI		Condition		Characteristics								
				Visual examination	no visible damage							
		e (Ur) at 85°C and /	DCL	1.25 x ir	1.25 x initial limit							
Endurance		0°C for 2000 hours t Ω/V. Stabilize at roo	ΔC/C	within ±10% of initial value								
	1-2 hours before n		DF	initial limit								
				ESR	1.25 x ir	1.25 x initial limit						
			Visual examination	no visib	no visible damage							
	Store at 200°C, no	voltage applied, for	DCL	1.25 x initial limit								
Storage Life		emperature for 1-2		ΔC/C	within ±	within ±10% of initial value						
	measuring.		DF	initial lir	initial limit							
				ESR	1.25 x ir	1.25 x initial limit						
				Visual examination	no visible damage							
	Apply rated voltag	e (Ur) at 85°C, 85%	relative humidity	DCL	2 x initial limit							
<b>Biased Humidity</b>		abilize at room temp		ΔC/C	within ±	within ±10% of initial value						
-	humidity for 1-2 ho	ours before measuri	ng.	DF	1.2 x ini	1.2 x initial limit						
				ESR	1.25 x initial limit							
	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+125°C	+200°C	+20°C		
	1	+20	15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	IL*		
Temperature	2	-55	15		+			-		+		
Stability	3 4	+20 +85	15 15	ΔC/C	n/a	+0/-10%	±5%	+10/-0%	+18/-0%	±5%		
	5	+125	15	DF	IL*	1.5 x IL*	IL*	1.5 x IL*	2 x IL*	IL*		
	6	+20	15	ESR	1.25xIL*	2.5xIL*	1.25xIL*	1.25xIL*	1.25xIL*	1.25xIL*		
				Visual examination	no visible damage							
Cuma		ry voltage (Uc) at 20 6 min (30 sec chard		DCL	initial lir	initial limit						
Surge Voltage	'	o min (30 sec charç n a charge / dischar	, ,	ΔC/C	within ±	within ±5% of initial value						
Voltage	1000Ω	ra onarge / alconar	ge redictarioe or	DF	initial lir	initial limit						
				ESR	1.25 x initial limit							
				Visual examination	no visible damage							
Maahaniaal				DCL	initial lir	initial limit						
Mechanical Shock	MIL-STD-202, Met	hod 213, Condition	ΔC/C	within ±	within ±5% of initial value							
OHOUR				DF	initial lir	initial limit						
				ESR	initial lir	initial limit						
				Visual examination	no visible damage							
				DCL	initial lir	nit						
Vibration	MIL-STD-202, Met	hod 204, Condition I	D	ΔC/C	within ±5% of initial value							
			DF	initial limit								
				ESR	initial lir	nit						

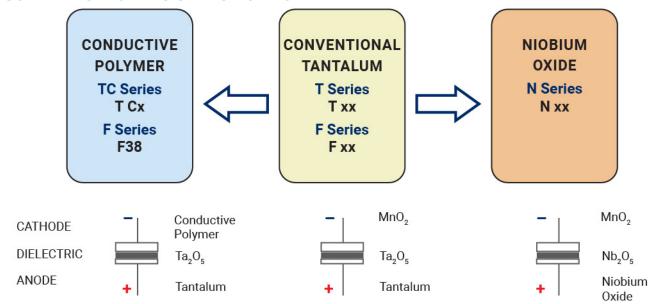
<sup>\*</sup>Initial Limit







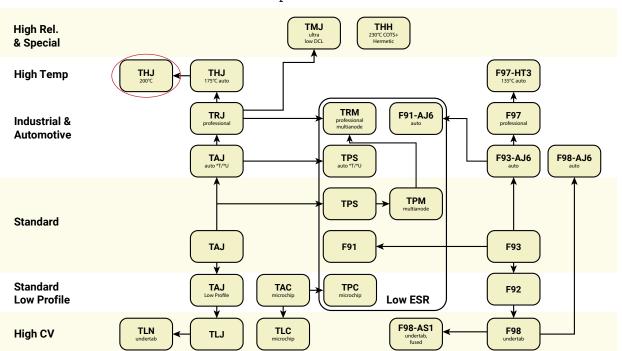
### **AVX SOLID ELECTROLYTIC CAPACITOR ROADMAP**



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