

### Description

The DMG2302UQ uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

### Dimensions SOT-23



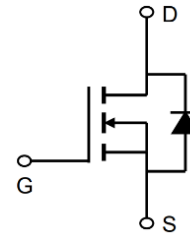
### General Features

$V_{DS} = 20V$   $I_D = 6.8A$   
 $R_{DS(ON)} < 21m\Omega @ V_{GS}=4.5V$

### Application

- Lithium battery protection
- Wireless impact
- Mobile phone fast charging

### Pin Configuration



### Package Marking and Ordering Information

Device	Device Marking	Device Package	Reel Size	Tape width	Quantity
DMG2302UQ	AE9T	SOT-23	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current	6.8	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current	6.0	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	30	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation <sup>3</sup>	1.5	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup>	83	°C/W

## Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	20	22		V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.50	0.65	1.0	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A		16	21	mΩ
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =2.5V, I <sub>D</sub> =3A		20	30	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V			1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V			±100	nA
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHZ		780		pF
C <sub>oss</sub>	Output Capacitance			140		
C <sub>rss</sub>	Reverse Transfer Capacitance			80		
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =10V, I <sub>D</sub> =6.8A		11		nC
Q <sub>gs</sub>	Gate-Source Charge			2.3		
Q <sub>gd</sub>	Gate-Drain Charge			2.9		
t <sub>D(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> =4.5V, V <sub>DS</sub> =10V, I <sub>D</sub> =6.8A R <sub>GEN</sub> =3Ω		9		ns
t <sub>r</sub>	Turn-on Rise Time			30		
t <sub>D(off)</sub>	Turn-off Delay Time			35		
t <sub>f</sub>	Turn-off fall Time			10		
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =6.8A, V <sub>GS</sub> =0V			1.2	V

**Note :**

- 1、 The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width ≅ 300us , duty cycle ≅ 2%
- 3、 The power dissipation is limited by 150°C junction temperature
- 4、 The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation

### Typical Characteristics

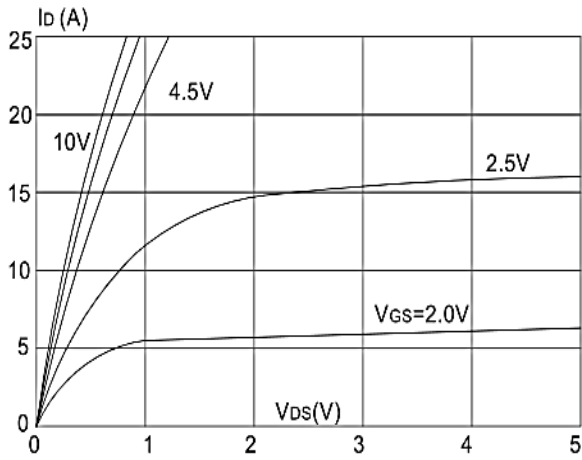


Figure 1: Output Characteristics

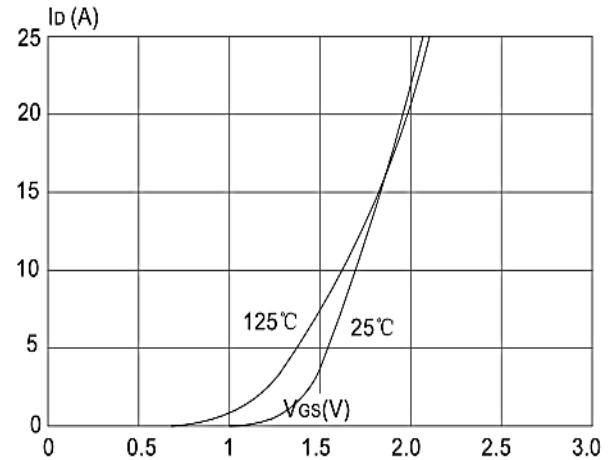


Figure 2: Typical Transfer Characteristics

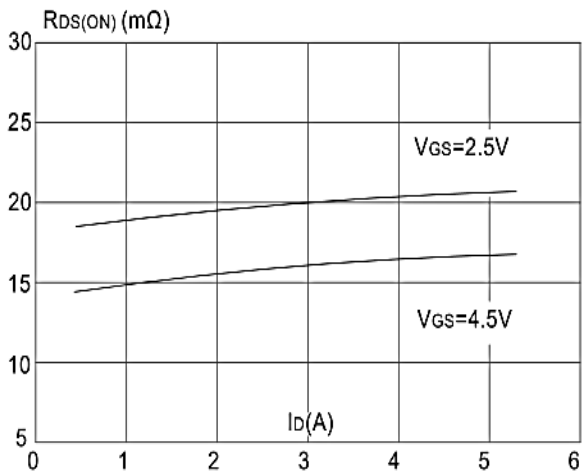


Figure 3: On-resistance vs. Drain Current

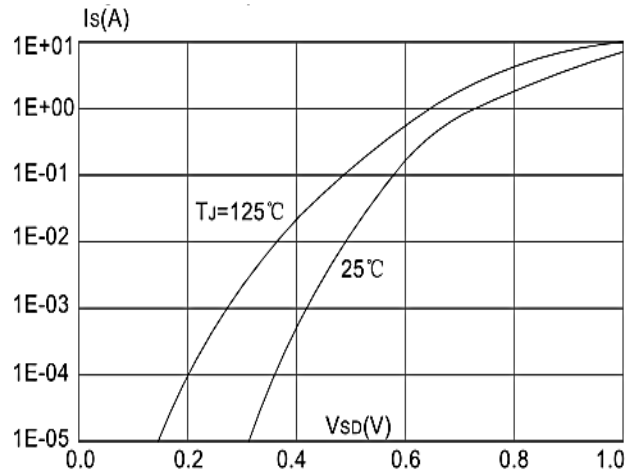


Figure 4: Body Diode Characteristics

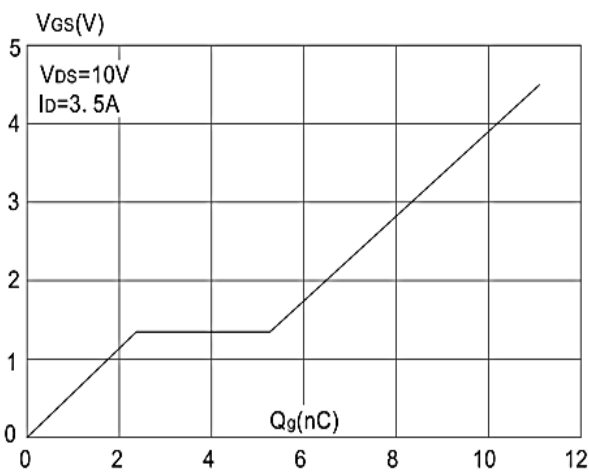


Figure 5: Gate Charge Characteristics

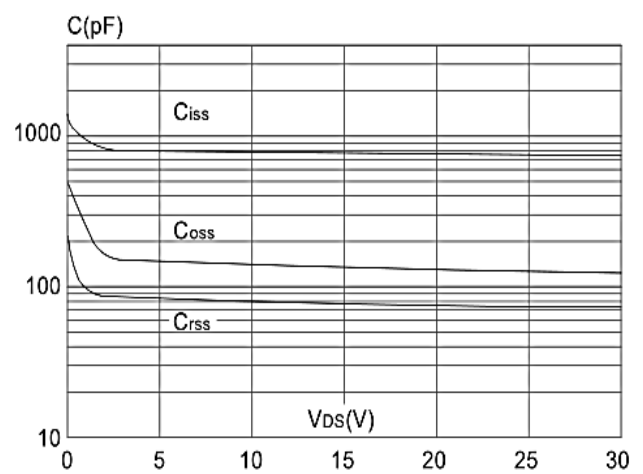
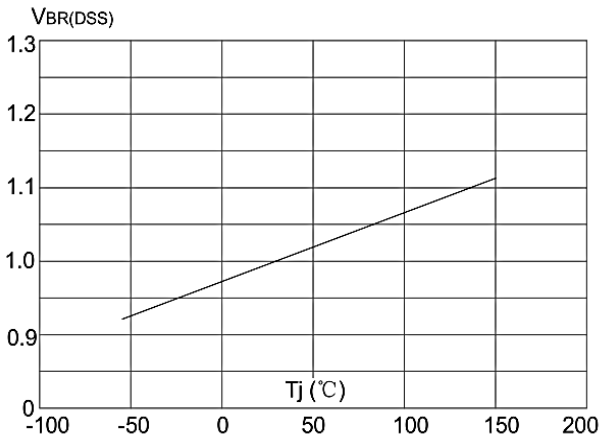
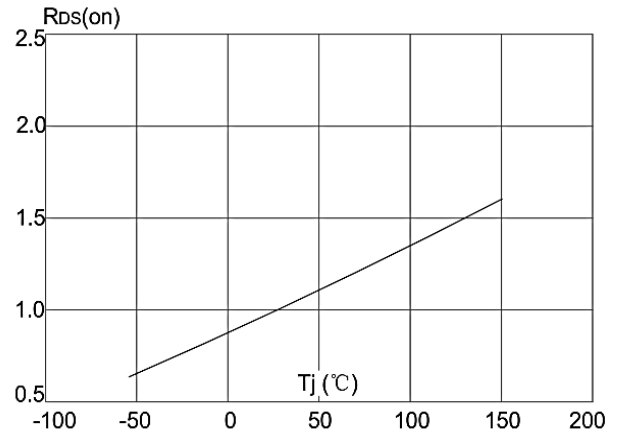


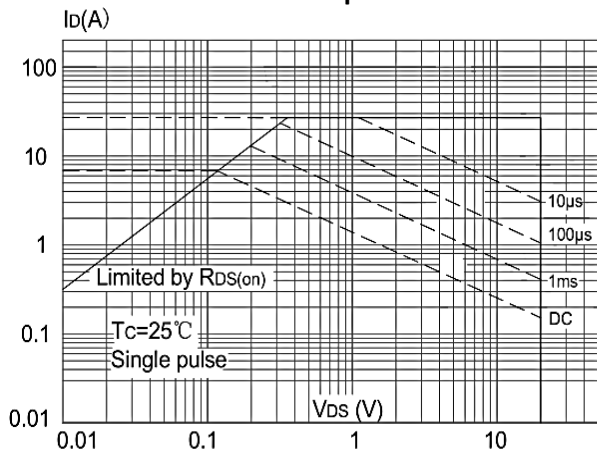
Figure 6: Capacitance Characteristics



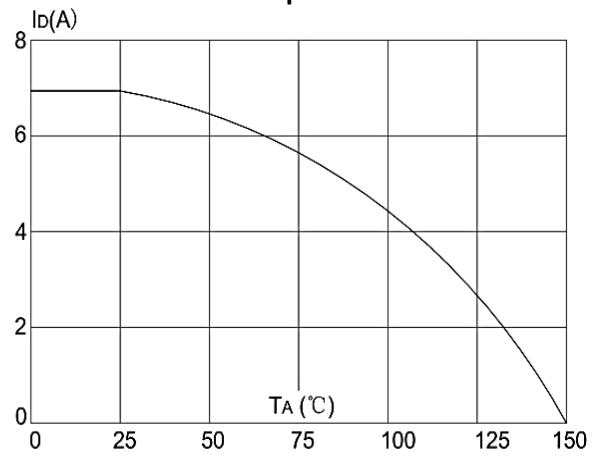
**Figure 7: Normalized Breakdown Voltage vs. Junction Temperature**



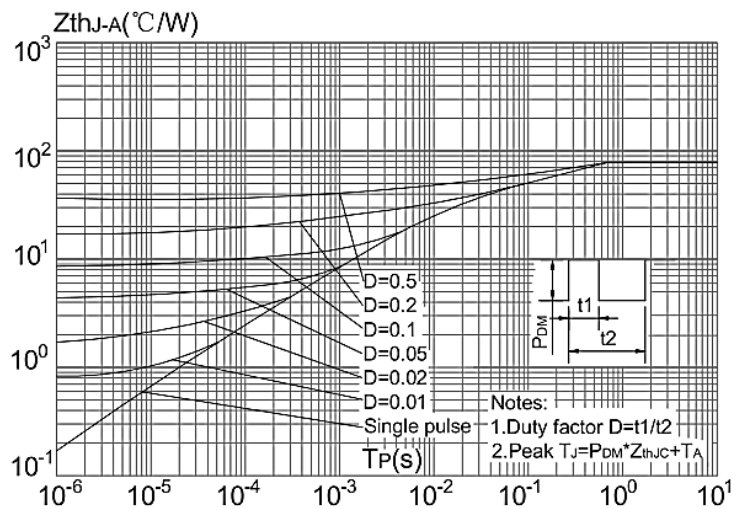
**Figure 8: Normalized on Resistance vs. Junction Temperature**



**Figure 9: Maximum Safe Operating Area vs. Case Temperature**



**Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature**



**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case**

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