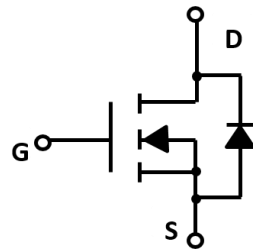
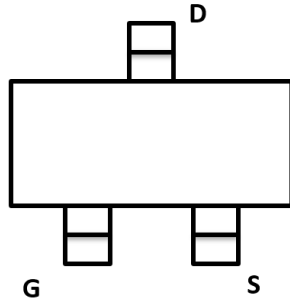


Top View

**SOT-23**



### Product Summary

- $V_{DS}$  60V
- $I_D$  340mA
- $R_{DS(ON)}$  ( at  $V_{GS}=10V$ ) <2.5ohm
- $R_{DS(ON)}$  ( at  $V_{GS}=4.5V$ ) <3.0ohm

### General Description

- Trench Power MV MOSFET technology
- Voltage controlled small signal switch
- Low input Capacitance
- Fast Switching Speed
- Low Input / Output Leakage

### Applications

- Battery operated systems
- Solid-state relays
- Direct logic-level interface: TTL/CMOS

### ■ Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		$V_{DS}$	60	V
Gate-source Voltage		$V_{GS}$	$\pm 20$	V
Drain Current	$T_A=25^\circ\text{C}$ @ Steady State	$I_D$	340	mA
	$T_A=70^\circ\text{C}$ @ Steady State		272	
Pulsed Drain Current <sup>A</sup>		$I_{DM}$	1.5	A
Total Power Dissipation @ $T_A=25^\circ\text{C}$		$P_D$	350	mW
Thermal Resistance Junction-to-Ambient @ Steady State <sup>B</sup>		$R_{\theta JA}$	357	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~+150	$^\circ\text{C}$

### ■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
LM-L2N7002LT1G-ES	F2	7002.	3000	30000	120000	7" reel

## ■ Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
<b>Static Parameter</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	60			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	μA
Gate-Body Leakage Current	I <sub>GSS1</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V			±100	nA
	I <sub>GSS2</sub>	V <sub>GS</sub> = ±10V, V <sub>DS</sub> =0V			±50	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA	1	1.5	2.5	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> =-300mA		1.2	2.5	Ω
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> =200mA		1.3	3.0	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =300mA, V <sub>GS</sub> =0V			1.2	V
Maximum Body-Diode Continuous Current	I <sub>S</sub>				340	mA
<b>Dynamic Parameters</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1MHZ		16		pF
Output Capacitance	C <sub>oss</sub>			10		
Reverse Transfer Capacitance	C <sub>rss</sub>			5.5		
<b>Switching Parameters</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =30V, I <sub>D</sub> =0.3A		1.7	2.4	nC
Turn-on Delay Time	t <sub>D(on)</sub>	V <sub>GS</sub> =10V, V <sub>DD</sub> =30V, I <sub>D</sub> =300mA, R <sub>GEN</sub> =6Ω		5		ns
Turn-off Delay Time	t <sub>D(off)</sub>			17		
Reverse recovery Time	t <sub>rr</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =300mA, V <sub>R</sub> =25V, di <sub>S</sub> /dt=-100A/μs		30		ns

A. Pulse Test: Pulse Width ≤ 300μs, Duty cycle ≤ 2%.

B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

## ■ Typical Performance Characteristics

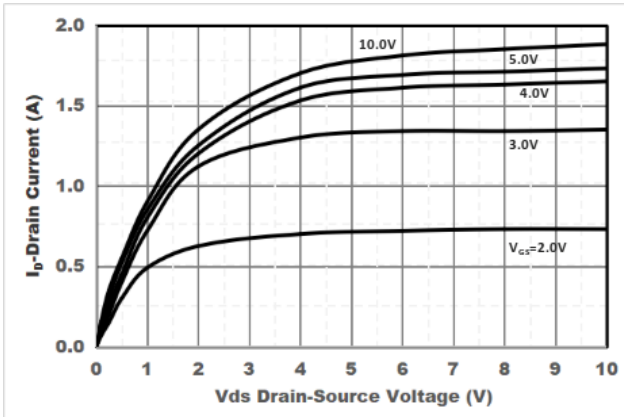


Figure1. Output Characteristics

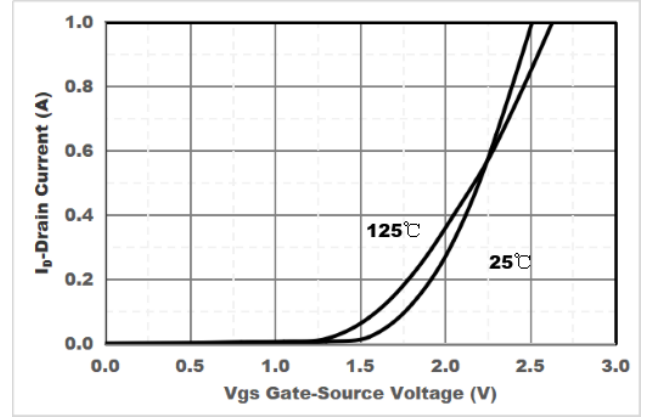


Figure2. Transfer Characteristics

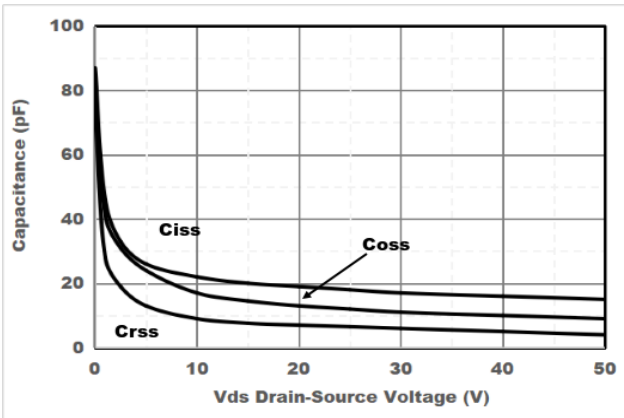


Figure3. Capacitance Characteristics

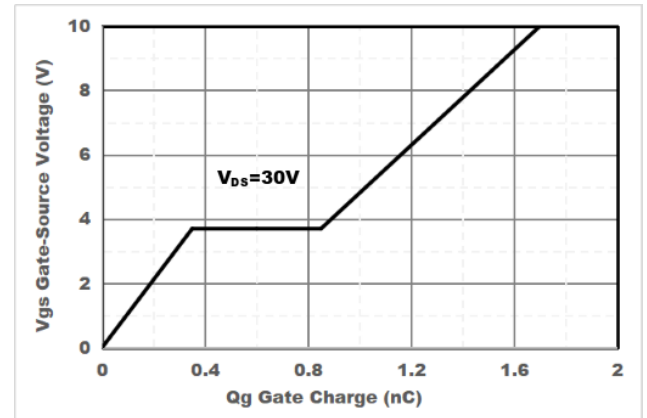


Figure4. Gate Charge

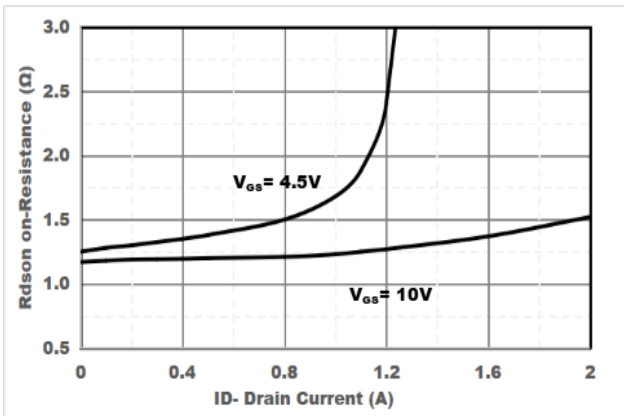


Figure5. Drain-Source on Resistance

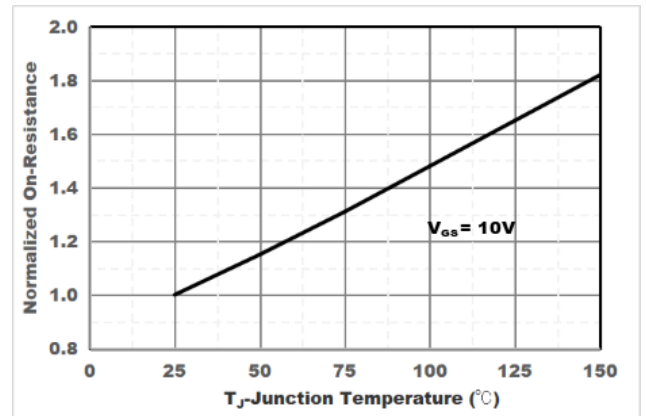


Figure6. Drain-Source on Resistance

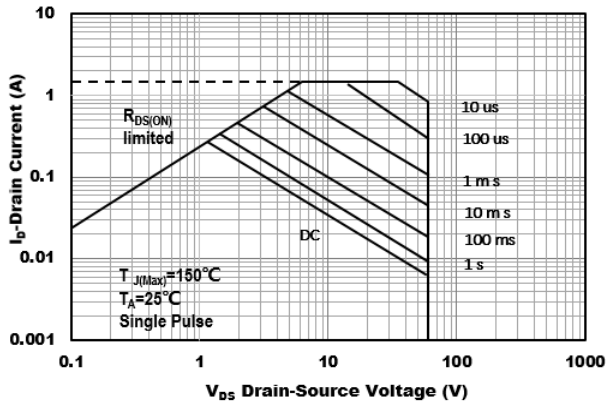


Figure7. Safe Operation Area

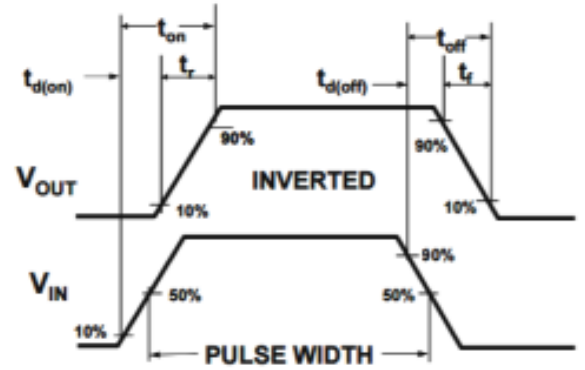
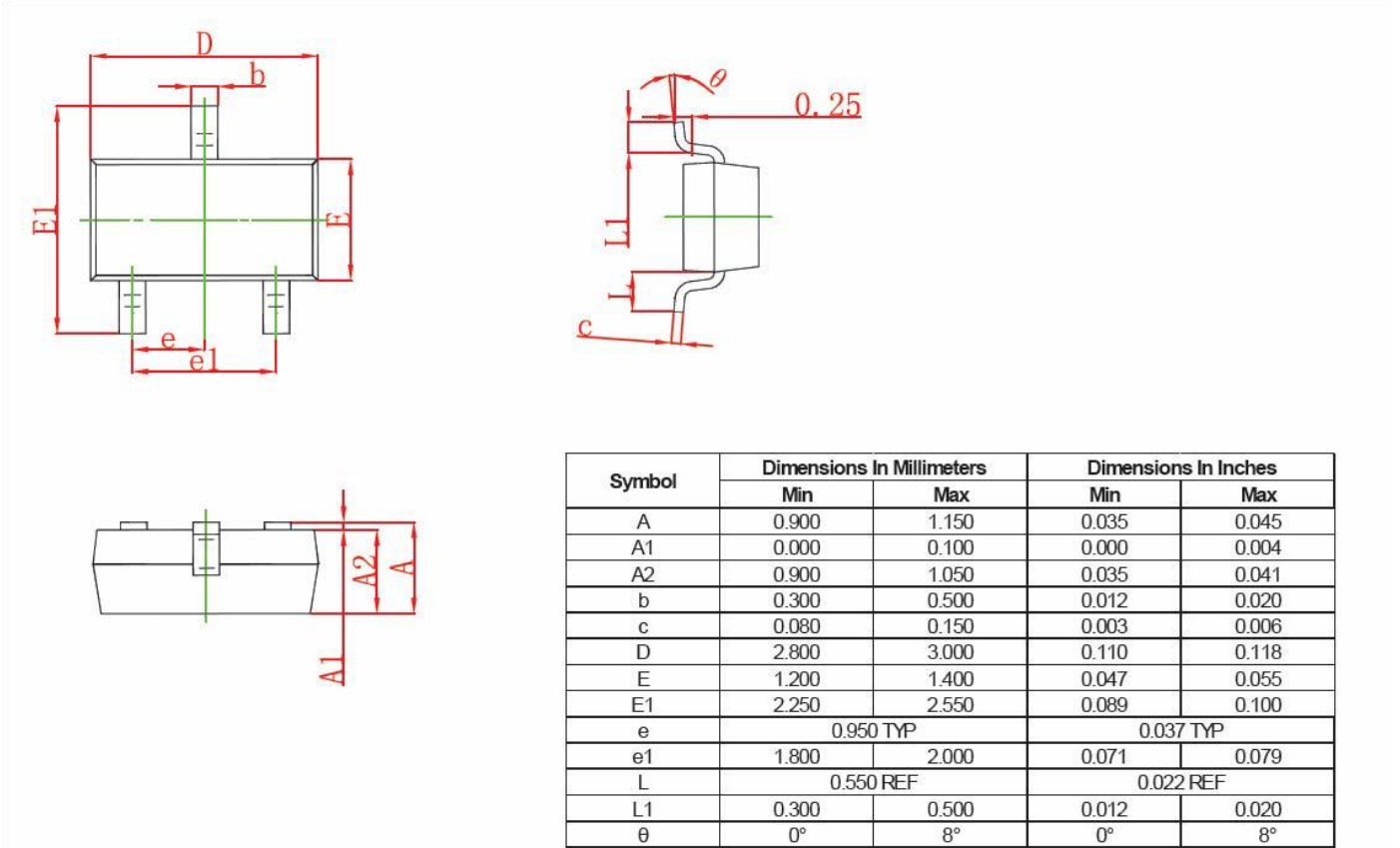
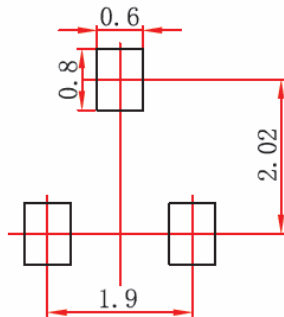


Figure8. Switching wave

## ■ SOT-23 Package information



## ■ SOT-23 Suggested Pad Layout



- Note:
1. Controlling dimension: in millimeters.
  2. General tolerance:  $\pm 0.05\text{mm}$ .
  3. The pad layout is for reference purposes only.

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